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PROJECTS ... THEORY ... NEWS ...  
COMMENTS ... POPULAR FEATURES ...

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# EVERYDAY PRACTICAL ELECTRONICS

INCORPORATING ELECTRONICS TODAY INTERNATIONAL

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A simple multi-purpose amp with built-in speaker and headphone output
- INGENUITY UNLIMITED** hosted by Alan Winstanley 582  
Cool Controller; VOM Continuity Buzzer; Square Wave Circuit
- QUIZ GAME INDICATOR** by Max Horsey and Tom Webb 598  
A low-cost fun project that cannot be questioned
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Have a relaxing "field-day" (or night) with Andy's latest mood modifier!
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The first in a series of "Top-Tenner" projects, featuring simple designs that that can be built for around \$16



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Our September 2000 issue will be published on Friday, 4 August 2000. See page 563 for details

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# NEXT MONTH

## ACTIVE FERRITE LOOP AERIAL

Whether you're a serious medium wave listener or just an inveterate band browser, this compact loop will be an aid to better reception.

The need to rotate the aerial in a horizontal plane to optimise reception is evident to every owner of a transistor portable radio. What is not so widely appreciated is the need to tilt it in the vertical if the deepest possible nulls are to be obtained.

Medium frequency radio waves reach the receiver by line-of-sight (direct waves), travel to it around the curvature of the earth (surface waves), and, at night, are reflected down from the ionosphere (sky waves). The loop must, therefore, tilt as well as turn in order to point its axis precisely at the advancing wave front.

This design provides the necessary mechanical orientation, a varicap tuning stage plus MOSFET Q multiplication and impedance matching.



Top  
Tanner

## STEEPLECHASE GAME

This game can be played by one person just for fun, but also makes a great game for two or more opponents.

At first glance, this is a very simple game. There is a row of seven l.e.d.s, all of them red except for the one on the right, which is green. A timer drives a counter that turns on the l.e.d.s one at a time, starting from the left, in order.

The travelling display represents a horse approaching a jump, which is the green l.e.d. If the player presses a button at the exact moment when the green l.e.d. is lit, this counts as perfect timing and a "clear jump" is scored. There is an eighth l.e.d. close to the button to indicate when this happens. However, there is no time to gloat over a successful jump because the horse is already pounding toward the next fence.

Now comes the catch! There is an element of uncertainty that taxes the skill of the player. Like most horses, the steeplechaser may accelerate or hang back as it approaches and takes the jump.

## MOODLOOP POWER SUPPLY

Basically designed to power the EPE Moodloop published in August 2000, this mains operated power supply delivers 13.2V d.c. at up to 1A. Its simplicity of design and construction also make it suitable for use as a general purpose workshop unit. It can be readily modified to alternatively supply 12V d.c. at 1A. In the text there is an informative discussion of the problems which face designers of higher current power supplies and how this particular author overcame them, with particular attention to minimising excessive heat generation.

**PLUS: ALL THE REGULAR FEATURES**

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- **3-CHANNEL WIRELESS LIGHT MODULATOR** No electrical connection with amplifier. Light modulation achieved via a sensitive electret microphone. Separate sensitivity control per channel. Power handling 400W/channel. PCB 54x112mm. Mains powered. Box provided. **6014KKT £24.90**
- **12 LUNNING LIGHT EFFECT** Exciting 12 LED light effect ideal for parties, discos, shop-windows & eye-catching signs. PCB design allows replacement of LEDs with 220V bulbs by inserting 3 TRIACS. Adjustable rotation speed (direction. PCB 54x112mm. **1026KKT £16.95; BOX (for mains operation) 2026KKT £8.50**
- **DISCO STROBE LIGHT** Probably the most exciting of all light effects. Very bright strobe tube. Adjustable strobe frequency: 1-60Hz. Mains powered. PCB: 60x88mm. Box provided. **6037KKT £29.90**

- **ANIMAL SOUNDS** Cat, dog, chicken & cow. Ideal for kids farmyard toys & schools. **SG10M £5.50**
- **3 1/2 DIGIT LED PANEL METER** Use for basic voltage/current displays or customise to measure temperature, light, weight, movement, sound levels, etc. with appropriate sensors (not supplied). Various input circuit designs provided. **3061KKT £12.95**
- **IR REMOTE TOGGLE SWITCH** Use any TV/VCR remote control unit to switch onboard 12V/1A relay output. **3058KKT £9.95**
- **SPEED CONTROLLER** for any common DC motor up to 100V/5A. Pulse width modulation gives maximum torque at all speeds. 5-15VDC. Box provided. **3067KKT £14.95**
- **3 x 8 CHANNEL IR RELAY BOARD** Control eight 12V/1A relays by Infra Red (IR) remote control over a 20m range in sunlight. 8 relays turn on only, the other 2 toggle on/off. 3 operation ranges determined by jumpers. Transmitter case & all components provided. Receiver PCB 76x89mm. **3072KKT £44.95**

## PRODUCT FEATURE

### P16PRO PIC PROGRAMMER

Our best selling PIC programmer for all 8, 18, 28 & 40-pin serial programmed PICs. Connects to PC parallel port. Will program, verify, read and erase. Designed to work with P16PRO DOS or Windows software that is supplied on a shareware basis. Power supply 16-30V DC/12-20V AC.

ORDERING INFO: Kit 3096KKT - £14.95; Assembled AS3096 - £24.95; Assembled with ZIF socket £39.95.

OPTIONAL EXTRAS: DOS software registration £14.95; Windows software registration £21.95; 40-pin wide ZIF socket £15.95; Power supply £5.95; lead £4.95.

- **SOUND EFFECTS GENERATOR** Easy to build. Create an almost infinite variety of interesting/unusual sound effects from birds chirping to sirens. 9VDC. PCB 54x85mm. **1045KKT £8.95**
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- **MUSIC BOX** Activated by light. Plays 8 Christmas songs and 5 other tunes. **3104KKT £6.95**
- **20 SECOND VOICE RECORDER** Uses non-volatile memory - no battery backup needed. Record/playback messages over & over. Playback as required to greet customers etc. Volume control & built-in mic. 6VDC. PCB 50x73mm. **3131KKT £11.95**
- **TRAIN SOUNDS** 4 selectable sounds: whistle blowing, level crossing bell, 'clackety-clack' & 4 in sequence. **SG01M £5.95**

- **PC CONTROLLED RELAY BOARD** One of our 286 upward PC into a dedicated automatic controller to independently turn on/off up to eight lights, motors & other devices around the home, office, laboratory or factory using 8 240VAC/12A onboard relays. DOS utilities, sample test program, full-featured Windows utility & all components (except cable) provided. 12VDC. PCB 70x200mm. **3074KKT £29.95**
- **2 CHANNEL UHF RELAY SWITCH** Contains the same transmitter/receiver pair as 30A15 below plus the components and PCB to control two 240VAC/10A relays (also supplied). Ultra bright LEDs used to indicate relay status. **3082KKT £27.95**
- **TRANSMITTER RECEIVER PAIR** 2-button keyboard style 300-375MHz Tx with 30m range. Receiver encoder module with matched decoder IC. Components must be built into a circuit like kit 3082 above. **30A15 £13.95**
- **PC DATA ACQUISITION/CONTROL UNIT** Use your PC to monitor physical variables (e.g. pressure, temperature, light, weight, switch state, movement, relays, etc.), process the information & use results to control physical devices like motors, sirens, relays, servo & stepper motors. Inputs: 16 digital & 11 analogue. Outputs: 8 digital & 1 analogue. Plastic case with printed front/rear panels, software utilities, programming examples & all components (except sensors & cable) provided. 12VDC. **3093KKT £8.95**
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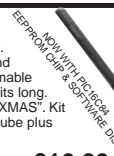
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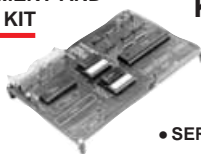
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EVERYDAY PRACTICAL ELECTRONICS EDITORIAL  
ALLEN HOUSE, EAST BOROUGH, WIMBORNE  
DORSET BH21 1PF  
Phone: Wimborne (01202) 881749  
Fax: (01202) 841692.

**E-mail:** editorial@epemag.wimborne.co.uk  
**Web Site:** <http://www.epemag.wimborne.co.uk>  
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## IT'S ALL RELATIVE

Just when you think everything is going along smoothly and all is well, along comes something to upset everything. Not only can we look forward to atomic level semiconductors with chips so small we won't even be able to see them or to connect them with conventional connecting wires (see *New Technology Update*), but now we have light travelling at 300 times the speed of light – if you see what I mean. This, of course, means that something can be in two places at once or, to put it another way, in another place before it has left the first place (see *News*) – tends to upset the mind doesn't it. If Einstein's theory of relativity is about to be disproved, what is there left of the old ways?

I guess the world will go on, it's just that we will all have to start thinking about time in a totally different way. And what will happen to our hobby when we can't solder components together any more? It's not the first time it has seemed like the future of d.i.y. electronics is doomed, and I guess conventional components will continue to be available for a decade or so more, otherwise how will anyone ever be able to repair anything (as if they would!)?

## SOLID FUTURE

Presumably at some time soon all electronic equipment will be designed and tested in a virtual world, then the chip will be automatically produced embedded as part of the case, together with all the controls/interfaces etc – just one lump of solid plastic forming everything including the battery. Maybe you'll just pop it in the microwave oven for a few seconds to charge it up.

*Mike Kenward*

### AVAILABILITY

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**Technical Editor:** JOHN BECKER

**On-Line Editor:** ALAN WINSTANLEY

**Business Manager:** DAVID J. LEAVER

**Subscriptions:** MARILYN GOLDBERG

**Editorial:** Wimborne (01202) 881749

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## HANDY-AMP

**TERRY DE VAUX-BALBIRNIE**

*A useful multi-purpose amplifier.*



UNTIL recently, the author's household hi-fi system had a piece of screened cable hanging down the back. This was left connected to the amplifier's high-level (auxiliary) input. When some piece of experimental audio equipment needed to be tested, the cable could be retrieved and connected to the circuit. It was then possible to listen to the result.

### NOT GOOD

This method was far from satisfactory, so a small battery-operated "bench" amplifier was designed for such purposes. As well as having an in-built loudspeaker, it has the facility for connecting personal stereo type headphones or an external loudspeaker. Also, it will accept both high-level and low-level input devices.

Magnetic record player cartridges and dynamic microphones provide a low-level output while the "line output" socket fitted to many pieces of consumer equipment (such as CD players and video recorders) provide a high level.

Many readers will, no doubt, wish to construct the amplifier for experimental purposes. However, it could have a variety of other applications. Examples include a small practice amplifier for electronic musical instruments and as the basis for an intercom, or toys and games.

Being battery-operated it may be set up outdoors and, with just a microphone (possibly with an extension lead) and a pair of headphones connected, it could be used to listen to wildlife.

Readers should note that the amplifier has been designed to be small and relatively inexpensive to construct. Although the maximum output power (one watt approximately) and sound quality are perfectly adequate for the applications suggested earlier, it is not suitable for critical applications such as serious music listening.

### WATT'S ENOUGH?

The stated power rating of 1W is applicable when the amplifier is connected to an 8-ohm load. If a 4-ohm loudspeaker were to be used, the rating would be some 1.5W. In fact, the subjective difference between the two is not great and battery life is reduced at the higher power. It is therefore recommended that an 8-ohm loudspeaker is used. A 4-ohm unit had to be used in the prototype for availability reasons.

The low-level input has its own gain control while the overall gain is set using the master volume control. This allows just about any input source to be connected, including microphones, musical instruments and consumer audio equipment.

Although not ideal, the headphone socket fitted to many pieces of electronic equipment provides a signal which will drive the high-level amplifier input. When doing this, the volume control on the equipment would need to be adjusted to obtain the correct input level. When this was tried with a small TV, the sound was better than from the TV itself.

Most of the time, the amplifier will probably be used in conjunction with the

internal loudspeaker. However, better sound quality is obtained when using either headphones or a good-quality external loudspeaker. Although the amplifier is monophonic (that is, not stereo), when used with headphones, the output is applied equally to each one. This gives a more comfortable effect than with only one headphone operating.

### ON THE PANEL

The completed Handy-Amp is shown in the photograph. For convenience, the rotary controls and all sockets and switches are mounted on the front panel. These are a jack and phono-type socket for the Low-level and High-level inputs respectively, together with the input selector switch, low-level Gain and master Volume controls, light emitting diode (l.e.d.) indicator and on-off switch, headphone jack socket, external loudspeaker sockets and output selector switch. On top, there is a matrix of holes to allow the sound to pass out from the internal loudspeaker.

There are several possible battery arrangements and the one chosen will be determined largely by the space available inside the case. This, in turn, will depend to a great extent on the dimensions of the internal loudspeaker. Whatever battery is used, it must have a nominal voltage of 9V (say, six 1.5V cells connected in series).

Cells should not have a capacity less than alkaline "AA" size. Note that a PP3 type battery would be totally unsuitable. The prototype unit was powered using two 4.5V alkaline "3LR12" batteries taped together and connected in series. These have around twice the capacity of alkaline "AA" cells.

The standby current requirement of the circuit depends on the load. In the prototype, it is 100mA. However, there will be peaks of several hundred milliamps and, depending on how the amplifier is used (operating time, load and volume), a life of some 15 hours may be expected from a pack of "AA" alkaline cells. This would be sufficient for occasional use.

With headphones connected, the standby current requirement of the prototype unit was only 50mA giving a longer battery life. The l.e.d. indicator reminds the user to switch off the unit after use.

Although battery operation is convenient and safe, for long periods of operation the use of a plug-in power supply unit might be more appropriate. More will be said about this later.



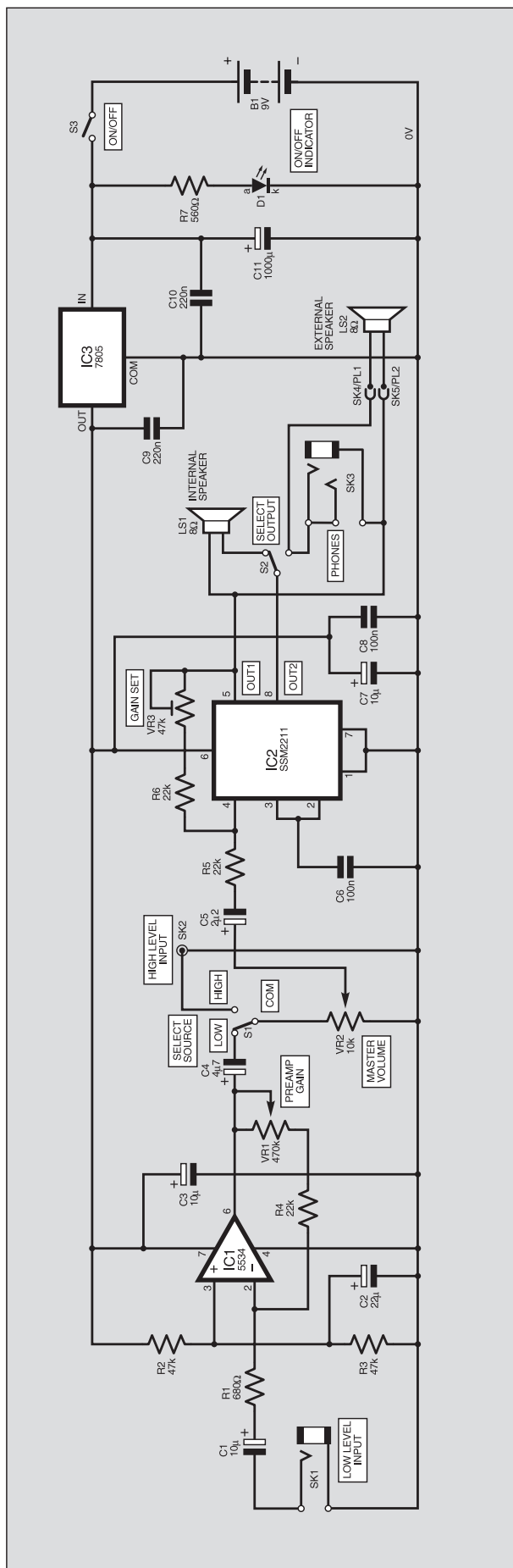


Fig.1. Complete circuit diagram for the Handy-Amp.

## CIRCUIT DESCRIPTION

The full circuit diagram for the Handy-Amp is shown in Fig.1. The design uses two main integrated circuits (i.c.s), IC1 and IC2, together with voltage regulator IC3.

Battery B1 provides a nominal 9V supply to the regulator which then gives a 5V supply for the main circuit. This will be maintained until the battery voltage falls to some 7V, whereupon the regulated output will fail. Thus, as the battery ages, the supply will remain constant throughout its useful life.

Note that the l.e.d. on-off indicator, D1, is connected in series with current-limiting resistor R7 directly across the battery supply – that is, it is not subject to the effect of the regulator. It will be obvious when the batteries need to be replaced because the amplifier output will become weak and distorted and the l.e.d. will become dimmer.

Capacitors C9 and C10 promote stability of the regulator. Capacitor C11 charges up from the battery and can then maintain the supply on the output current peaks when the amplifier is delivering maximum power. This helps to provide a distortion-free output.

If using a plug-in power supply unit, C11 will provide additional smoothing if a poorly-smoothed supply is used. This should not be necessary with a good-quality unit but will be useful with inexpensive ones.

## FIRST BOOSTER

When a low-level device such as a microphone is connected, via socket SK1, its output voltage is first boosted using a low-noise pre-amplifier, based on operational amplifier (op.amp), IC1. High-level (line) signals are input via socket SK2, thus bypassing IC1.

## COMPONENTS

### Resistors

R1	680Ω
R2, R3	47k (2 off)
R4 to R6	22k (3 off)
R7	560Ω
All 0.25W 5% carbon film	

### Potentiometers

VR1	470k min. panel mounting, lin or log rotary carbon
VR2	10k min. panel mounting, log rotary carbon
VR3	47k min. preset, vertical carbon

### Capacitors

C1, C3, C7	10μ radial elect. 63V (3 off)
C2	22μ radial elect. 63V
C4	4μ7 radial elect. 63V
C5	2μ2 radial elect. 63V
C6, C8	100n polyester, 5mm pin spacing (2 off)
C9, C10	220n polyester, 5mm pin spacing (2 off)
C11	1000μ radial elect. 16V

### Semiconductors

D1	red l.e.d., 3mm
IC1	NE5534AN op.amp
IC2	SSM2211 power amplifier
IC3	7805 5V 1A voltage regulator

### Miscellaneous

SK1	6.35mm plastic body mono jack socket
SK2	phono jack socket, single-hole fixing (see text)
SK3	6.35mm stereo jack socket, plastic body
SK4, SK5	2mm socket or as required (see text) (2 off)
S1, S2	s.p.d.t. toggle switch (2 off)
S3	s.p.s.t. toggle switch
LS1	small 8-ohm loudspeaker, 2W rating minimum (see text)

Printed circuit board, available from the *EPE Online store* code 273; aluminium case, 203mm x 127mm x 51mm; 8-pin d.i.l. socket (2 off); 3mm l.e.d. panel clip; control knob (2 off); alkaline AA-size cells (6 off – see text); holder and connector for cells (or as required).

Approx. Cost  
Guidance Only

**\$29.60**  
excluding batts. and case

The signal source is selected by switch S1 and, via volume control VR2, passed on to the power amplifier section centred on IC2.

Op.amp IC1 is configured as a voltage amplifier used in inverting mode. Pins 7 and 4 are the positive and 0V supply inputs respectively. Blocking capacitor C1 allows the alternating current signal from a source connected to socket SK1 to pass via resistor R1 to the inverting input, at pin 2.

The input impedance is set by the value of R1 and this will provide a good match for dynamic microphones. The op-amp non-inverting input, pin 3, receives a d.c. voltage equal to one-half that of the supply (nominally 2.5V) due to equal-value resistors, R2 and R3, which form a potential divider connected across the supply.

## NO PAIN, NO GAIN

The pre-amp gain is set by the ratio of feedback resistance (R4 plus VR1) to input resistance, R1. With VR1 set to minimum, this provides a gain of about 32 and at maximum, rather more than 700 (the fact that this is an inverting amplifier and the gain has a negative sign is of no real consequence here and may be disregarded).

This range of gain will suit microphones and other low-level input devices. VR1 is the low-level gain control (labelled simply "Gain" on the front panel). In use, this will be adjusted to take account of the sensitivity of individual input devices.

## MAKING THE SWITCH

The output from IC1 appears at pin 6. This passes via blocking capacitor C4 to the "Low level" (Low) contact of two-way "Select Source" switch, S1. With this set as shown, any low-level signal passes to the common contact and hence through the track of potentiometer VR2 to the 0V line.

The sliding contact (wiper) of VR2 draws off the required fraction of the signal voltage and passes it, via capacitor C5 and resistor R5, to the input of the power amplifier (IC2 pin 4). If switch S1 is set to the alternative position (High), the output from IC1 is disconnected from VR2 but now any signal applied to the high-level input socket, SK2, is directed through VR2 instead. VR2 is the master volume control (labelled "Vol" on the front panel).

## INTERNAL STRUCTURE

Power amplifier IC2 is an interesting device and a block diagram showing its simplified details is given in Fig.2. Basically, it consists of two operational amplifiers, A and B. The output of op.amp A (pin 5) provides one of the outputs (Out 1). However, it also feeds the inverting input of op.amp B (via the 50kΩ input resistor) whose output (pin 8) becomes Out 2. The loudspeaker is connected directly between Out 1 and Out 2.

Op.amp A is configured as an inverting amplifier. Thus, the signal appearing at Out 1 is an amplified and inverted version of that at the input, pin 4. Referring back to Fig.1, its gain is set by the value of external fixed resistor R6 and preset VR3 connected between Out 1 and input pin 4.

The non-inverting input (pin 3) is connected externally to pin 2 which sets it at a

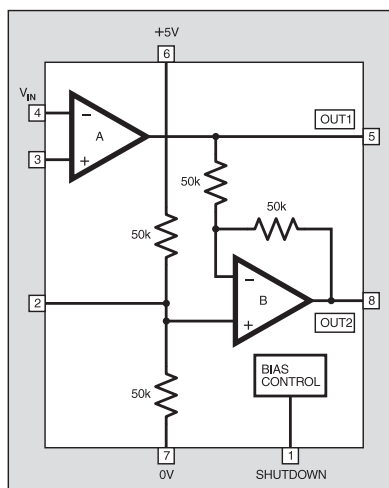


Fig.2. Block diagram of the SSM2211 power amplifier.

d.c. voltage equal to one-half that of the supply. This is due to the potential divider consisting of two internal 50kΩ resistors connected between supply positive (pin 6) and 0V (pin 7). Pins 2 and 3 are then connected to one end of the external bypass capacitor, C6, with the other end connected to the 0V line. This may be compared with the biasing arrangement used for IC1.

Op.amp B is also configured as an inverting amplifier and because the internal input and feedback resistors have equal values (50kΩ), the gain is set at minus one. Thus, any signal appearing at Out 2 is an inverted copy of that at Out 1. In this way, the input signal at pin 4 has an amplified but inverted copy of itself at pin

5 and a "straight" copy of itself at pin 8 amplified by the same amount. This is known as a bridge output configuration. Correct working depends on the two op.amp sections being exactly balanced but, of course, this is not easy to achieve precisely.

In theory, when no input signal is present, Out 1 and Out 2 will be at the same voltage. No current will then flow in a loudspeaker connected between them. When a signal is present, either Out 1 will drive current through the loudspeaker winding, which then sinks into Out 2, or Out 2 will drive current through the loudspeaker in the opposite direction and sink into Out 1. This will then reproduce the positive and negative excursions of the a.c. waveform presented to the input.

In practice, there will be a small voltage difference between the outputs in the absence of an input signal. A small standing current will then flow through the loudspeaker coil and the lower its impedance, the greater this current will be. This is added to the small current needed by the i.c. itself (for the working of op.amps A and B, and for the current drain through the internal potential divider). The overall current requirement is therefore somewhat load dependent.

## SHUTDOWN

The SSM2211 amplifier used as IC2 has a shutdown feature. Thus, if pin 1 is made high, the i.c. is put into "sleep" mode and requires very little current. However, this feature is not used here and is disabled by connecting pin 1 to the 0V line along with pin 7.

The gain of IC2 is calculated by the ratio of feedback resistance (VR3 plus

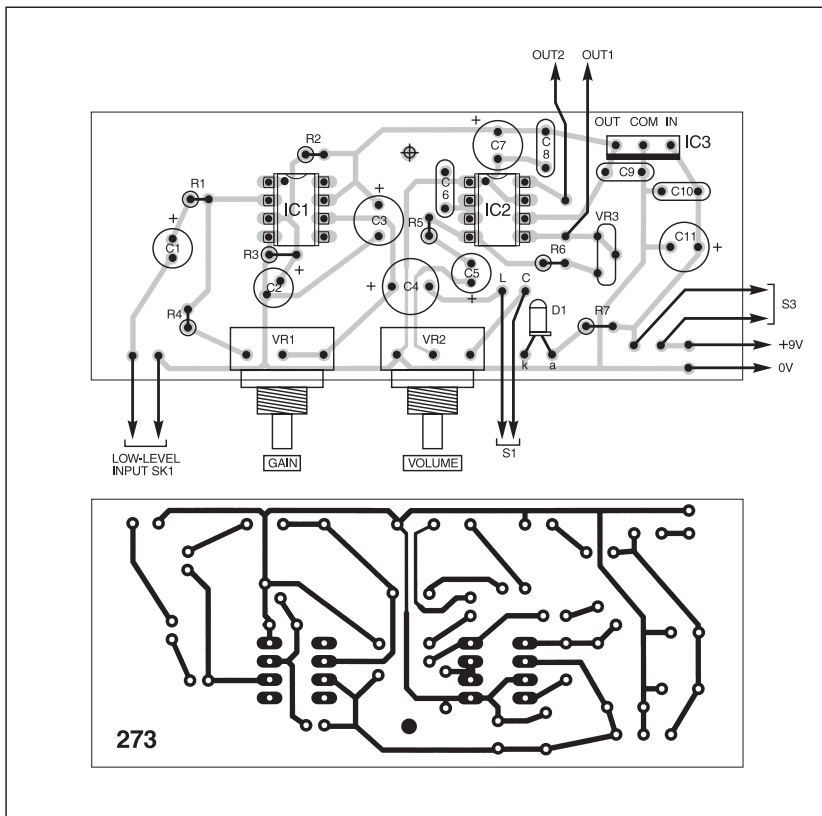


Fig.3. Handy-Amp component layout and full size copper foil track master pattern.



R6) to input resistance (R5) multiplied by two. This "multiplied by two" aspect comes about because of the bridged output configuration giving twice the voltage swing to the loudspeaker compared with a single op.amp.

With VR3 set to minimum resistance the gain is therefore two, and at maximum resistance is just over six. VR3 is a preset potentiometer which allows adjustment for the desired gain.

## OUTPUT ARRANGEMENTS

With switch S2 ("Select Output") in the position shown in Fig.1, the internal loudspeaker is connected between Out 1 and Out 2. With the switch in the alternative position, the output is directed to both the "Phones" socket, SK3, and the external loudspeaker sockets (SK4 and SK5).

It is thought unlikely that anyone would wish to connect an external speaker and a pair of headphones to the amplifier at the same time. However, even if they did, the load would not fall below the minimum impedance providing an 8-ohm loudspeaker was used.

When using headphones, a greatly reduced power is available to them compared with a loudspeaker. This is because the (usually) higher impedance allows less current to flow. The impedance of typical personal stereo type headphones is about 30 ohms for each unit.

In this design, the left and right units are connected in parallel giving a combined impedance of some 15 ohms. However, because headphones provide acoustic energy direct to the ears, only a very small amount of power is needed for them to sound with acceptable loudness.

## CONSTRUCTION

A metal case should be used as an enclosure for the Handy-Amp. A vinyl-effect aluminium box was used for the prototype unit because it gave a good appearance. Do not use a plastic box since this will not provide any screening and hum pick-up might be a problem.

Construction is based on a single-sided printed circuit board (p.c.b.). The topside component layout and full size underside copper track foil master are shown in Fig.3. This board is available from the *EPE PCB Service*, code 273.

Most of the components are mounted on the p.c.b. although there are quite a few off-board parts which will be hard-wired

to one another and to various points on the p.c.b. later.

Begin by drilling the fixing hole in the p.c.b. then solder the sockets for IC1 and IC2 in position (but do not insert the i.c.s themselves at this stage). Follow with all fixed resistors and capacitors. Note that the resistors are mounted vertically.

There are seven electrolytic capacitors and it is important to solder all of these with the correct polarity. The negative (–) end is clearly marked on the body and the corresponding lead is slightly shorter than the positive (+) one. Solder preset VR3 in place but not panel potentiometers VR1 and VR2 yet.

Fit the control knobs to VR1 and VR2. Measure how much of each spindle needs to be cut off then remove the knobs again. Hold the end of the spindle (not the potentiometer body or it could be damaged) in a vice and cut off the required length using a small hacksaw. Smooth the cut edges using a file and check that the knobs fit correctly.

Cut off the panel-location tags fitted to most potentiometers. If these are left in place, the bodies will not seat flat against the front panel when the p.c.b. is in position. The potentiometers should now be soldered to the p.c.b.

## RIGHT LEADS

Identify the l.e.d. end leads. The cathode (k) is usually shorter than the anode (a) lead. Also the body has a small "flat" to denote the cathode end. Solder the leads to the "D1" pads on the p.c.b. observing the correct polarity. Bend them through right-angles, as shown in the photograph, so that the body ends up in line with the centre of the potentiometer spindles and standing out to about the centre of the bushes.

Solder pieces of light-duty stranded connecting wire to the following points on the p.c.b.: "Low-Level Input", "S1" (L and C), "Out 1" and "Out 2". Using different colours of wire will help to prevent errors when connecting them up.

Solder the red and black battery connector wires to the "+9V" and "0V" points respectively on the p.c.b. (or use pieces of similarly-coloured stranded wire if soldered connections are needed to the batteries). Adjust the wiper of preset VR3 to approximately mid-track position.

Solder regulator IC3 in position noting that the back is towards the centre of the p.c.b. (the part that protrudes is towards the edge).



*Close-up detail of heatsink mounted on IC3.*

Note that the specified regulator has a current rating of 1A. Although the average requirement of the circuit is much smaller than this, in use there are peaks of several hundred milliamps and this regulator will cope well.

Due to the low average current, only a small heat sink is needed. In the prototype, this consisted of a piece of sheet aluminium size 50mm × 15mm bent through right angles as shown in the photograph. It was drilled with a small hole and attached securely to the back of IC3.

When choosing the internal loudspeaker also take into account the size of the battery pack to be used. The loudspeaker used in the prototype was a 90mm × 50mm elliptical type as used in small radios and TV receivers. Make sure that the power rating is sufficient because many small loudspeakers are inadequate in this respect. Do not use one having a rating of less than 2W.

Decide on a position for the p.c.b. and battery pack by arranging them on the bottom of the box. Consider also the loudspeaker which will be mounted on the lid section and the various off-board sockets and switches.

When the p.c.b. is finally attached, there must be several millimetres of clearance between the copper tracks and the bottom of the box. This will avoid any possibility of short-circuits. Take care that the heat sink does not touch wiring or other electrical connections.

## BORING BUSINESS

Mark the positions of the holes for the mounting bushes of potentiometers VR1 and VR2, also for the l.e.d. mounting clip. Drill these through and, gently bending the l.e.d. leads out of the way for the moment, secure the p.c.b. to the case using the potentiometer fixing nuts. Place washers (or spare fixing nuts) on the bushes on the *inside* of the case so that only a small amount of each bush protrudes through its hole. Mark through the p.c.b. fixing hole then remove the board again.

Mark the positions of the switches, the low-level input jack socket (mono 6.35mm type), the phono socket, the headphones



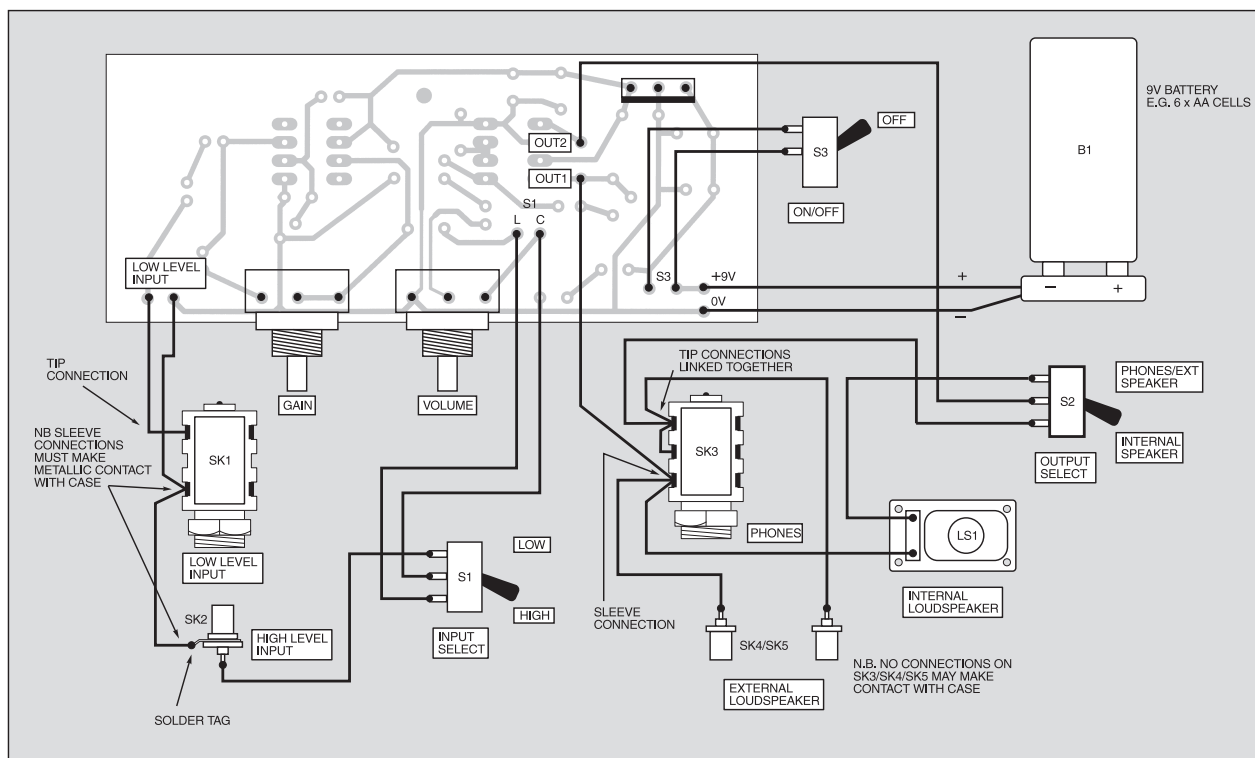


Fig.4. Off-board component connection details.

output jack socket (stereo 6.35mm – see Important Note) and external loudspeaker sockets. In the prototype, 2mm sockets were used for the loudspeaker, but the type used will depend on personal requirements. Drill the holes and mount the sockets, switches and l.e.d. clip.

### IMPORTANT NOTE

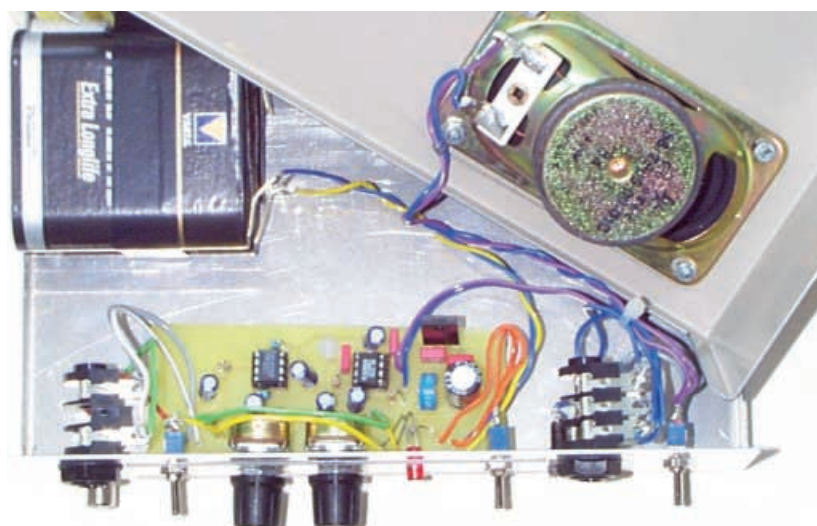
The case itself is connected to 0V (earth). It is not acceptable for the headphone stereo output socket, SK3, to be of a type where any of its contacts touch the case. If they were to, a short-circuit would be formed and this could damage IC2. This precludes using the ordinary metal sleeved type of 3.5mm jack socket because, when mounted in position, its outer (sleeve) connection would make contact with the metalwork.

There are various ways to avoid this. One method would be to use an insulating sleeve and insulating washers on a standard 3.5mm unit. However, the method used in the prototype was to use a 6.35mm plastic body stereo jack socket. This had all its connections isolated from the case. Headphones are then connected to it via a 6.35mm to 3.5mm converter.

Mount the socket and check, using a meter, which tag is which and that none of its tags make contact with the case.

The mono jack socket, SK1, used for the low-level input, unlike the headphone output socket *must* have its sleeve connected to 0V (earth). Since this socket will probably have a plastic body, it will not be done automatically and the sleeve connection will need to be hard-wired to a solder tag attached to the case.

The phono socket (SK2 – high-level input) must also have its sleeve connected to 0V. If using the specified single-hole fixing type, this will be done automatically. Note that this socket usually has a solder



tag on its bush and this may be used for the SK1 earth connection. If the phono socket is of a fully-insulated type, you will need to make a connection between the sleeve tag and the case using a solder tag (which will also be used for SK1).

### HARD WIRING

Referring to Fig.4, carry out all the internal wiring using light-duty stranded connecting wire. By using different colours, you will avoid errors (rainbow ribbon cable is ideal). Note that the two non-sleeve (tip) tags of the headphone socket are joined together so that both headphone units are connected in parallel.

Remember to leave all wires interconnecting the various points on the p.c.b. with off-board components long enough to enable the p.c.b. to be removed without straining them, should this ever become necessary. Also, the loudspeaker wires

should be sufficiently long to allow the lid of the box to be removed without straining them.

Place the loudspeaker in position and mark the fixing holes on the lid of the case. Take care to avoid the p.c.b. (especially the heat sink on IC3) and battery pack positions.

Mark out the holes which are needed to allow the sound to pass through. Drill these using a small (say, 1.5mm) drill then increase the diameter to 5mm approximately. Work carefully because the appearance of the finished project will be spoilt if the holes are drilled carelessly.

Carefully clean away any metal particles then attach the loudspeaker. Solder the wires to its tags and apply some strain relief so that they cannot pull free when removing the lid of the case. In the prototype, this was done using a solder tag





having a long "tail". This was attached to one of the loudspeaker fixings.

The wires were protected using a short piece of sleeving and the tail of the solder tag was gripped gently around them. Take care that the wires are not so tightly held that a short-circuit is produced.

Attach the p.c.b., making sure that it is parallel with the base of the box. Measure the clearance between the copper track side and the bottom of the case. Cut a plastic stand-off insulator to the same length. Slide it into position and secure the p.c.b. using a thin nylon nut and bolt.

Gently bending the leads as necessary, push the l.e.d. into its clip. Attach the control knobs to the potentiometer spindles. If the knobs have a white line or spot, this should be arranged to be vertically upwards when the control is at its half-way position.

## FINAL ASSEMBLY

Fit self-adhesive plastic feet to the bottom of the case to protect the work surface. Attach the battery pack using a small bracket or adhesive fixing pads (sticky Velcro pads were used in the prototype). Do not connect the battery yet.

Immediately before unpacking and handling IC1 and IC2, touch a metal object which is earthed (such as a water tap). This will remove any static charge which might exist on the body. This is a wise precaution because the i.c.s are static-sensitive and could be damaged by such charge. Insert them into their sockets with the correct orientation.

Place the lid of the case in position but do not actually attach it. Make a final check that nothing is obstructed and, especially, that the heat sink on IC3 is completely free of all wiring and internal components.

Make sure switch S3 is off. Before connecting the battery, make certain the polarity is correct. The circuit will be damaged if the polarity is incorrect. Make certain the positive battery connection cannot make contact with the case or the battery will be short-circuited. This could result in damage to p.c.b. tracks. Switch on S3 and check that the l.e.d. indicator lights up.

## TESTING

Begin testing by using the amplifier with a high-level input source, such as the line output of a CD player, cassette deck or the audio output from a video recorder; if this is stereo, use only one channel. Connect it to the phono socket using a piece of mono screened lead with suitable connectors at each end. Set switch S1 to "High" and S2 to "Internal Speaker". Turn both VR1 and VR2 fully anti-clockwise.

Switch on the input device and slowly increase VR2. The sound should be heard clearly. Adjust preset potentiometer VR3 so that the sound is undistorted when VR2 is at maximum. You will find that the setting is not particularly critical. Almost maximum resistance was correct for the prototype (that is, the sliding contact almost fully clockwise when viewed from the right-hand edge of the p.c.b.).

Leave the system operating for about ten minutes then switch off and check that the regulator heat sink is not excessively

hot. If it is uncomfortable to touch, increase its area. When satisfied on this point, attach the lid section.

Turn VR2 to minimum again. Switch S1 to "Low" and connect a dynamic microphone to the low-level input jack. Increase VR2 to approximately one-third of its total clockwise rotation then increase VR1 slowly while speaking into the microphone. The sound should be clearly heard.

If the controls are turned up too far, or the microphone is placed too close to the unit, acoustic feedback will become evident. This usually manifests itself as a loud squealing noise from the loudspeaker.

Acoustic feedback is a potential problem with any loudspeaker/microphone system. It comes about because sound from the loudspeaker re-enters the microphone and builds up in a positive feedback loop. To prevent it, turn down the controls, move the microphone away and/or point it in the opposite direction to the loudspeaker. Acoustic feedback may be largely eliminated by using headphones instead of a loudspeaker.

It is unlikely that the low-level gain will need to be increased. If it is found to be necessary, decrease the value of resistor R1 to 560 ohms or even 470 ohms. Note that excessive gain leads to instability.

When connecting an external loudspeaker, always remember to switch off the amplifier first. This will avoid any possibility of loudspeaker connections touching the case and possibly damaging IC2.

## POWER ADAPTOR

If you wish to use a plug-in power supply instead of a battery, use a 9V d.c. type having a current rating of 800mA minimum. A fuse and polarity-protection diode need to be included (see later) if damage to the unit itself or to the circuit are to be avoided.

Attach a power-in type socket to the rear of the box to suit the output plug on the power supply unit. If its sleeve connection does not make contact with the metalwork automatically, you will need to hard-wire this to a solder tag attached to the case.

Preferably, the power supply unit should have a fixed polarity with the centre (pin) on the output plug being the positive and the sleeve the negative. If the polarity can be reversed, make sure that the pin is made positive. If the polarity is incorrect, the circuit will be damaged. This is why a diode should be connected in the positive feed wire. If the polarity is incorrect, the diode will not conduct and nothing will happen. The fuse protects against possible short-circuits.

Referring to Fig.5, sleeve both end wires of a type 1N4001 diode. Solder the anode (non-striped end) to the centre (pin) connection of the socket. Attach a 20mm chassis fuse holder to the bottom of the box in such a position that the cathode of the diode can reach one of its tags. Solder this in position. A wire from the other fuse tag should then be taken to the "+9V" point on the p.c.b.

Insert a 20mm 1A quick-blow fuse in the fuse holder. Make sure none of the connections to the diode or fuse can touch the case. Use insulation if necessary. □

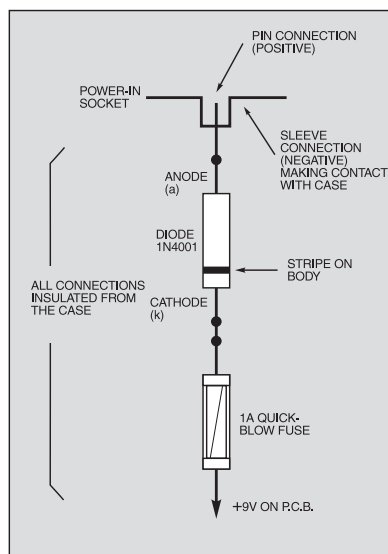


Fig.5. Using a fuse and diode as protection devices when a power supply adaptor is used.



## TAMING THE WORM

Barry Fox views the latest in holographic data storage techniques.

IMATION and parent 3M have been selling optical disk recorders for 25 years and are now promising a holographic CD-ROM recorder for £3000 by 2002. After working with Bell Labs and Lucent Technologies, Imation is confident it can achieve what others have only talked about by modifying technologies that are already being mass-produced for other applications.

Says Imation's European Technical Manager, George Purrio, "We know that others, such as Bayer, are working on holographic storage but we believe our experience in optical disk technology puts us at least two years ahead with a commercial product".

### Terabyte Storage

First generation holodisks costing \$10 will store 125 gigabytes, compared to under 20GB for a DVD. Storage will later increase to a terabyte, with data transfer rates up to a gigabit per second and at least 25 times faster than for DVD.

To cut costs Imation will use WORM (write once, read many times) blanks made from light sensitive polymer made by Lucent. With so much storage space per disk there is no need to use expensive and unstable erasable technology, such as lithium niobate. WORM users automatically build a permanent audit trail of all their data before and after any modification, without the risk of accidental erasure.

### Data Beams

A 200mW gas laser, already available for \$2000 and likely soon to cost \$300, shines through a half-silvered mirror to create a reference beam and data beam. The reference beam obliquely hits a polymer disk which spins on a modified CD-ROM drive. The data beam goes to a digital micromirror developed by Texas Instruments for video projectors. A million or more tiny mirrors on the surface of a chip flip-flop between on-off positions to let light pass through or reflect away.

The bitmap displayed on the micromirror is created either by buffering a stream of raw data from a PC, digital video or audio system, or by scanning an image or page of text.

The data beam, which now images whatever digital bitmap is displayed on the micromirror, falls on the polymer disk at the same spot as the reference beam, but at a different angle. This creates an optical interference pattern which permanently changes the chemistry of the polymer. So the disk records a sequence of bitmaps along a spiral track like a CD recording. Further series of bitmaps can be recorded

deeper into the polymer by altering the angle of the beams so that they cross at lower depths.

The disk is read by removing the micromirror and shining pure beams through the polymer and onto a megapixel CCD image sensor of the type now routinely built into digital still picture cameras that cost \$500 or less.

Data transfer is rapid because the recorder is capturing whole pages or blocks of data at a time.

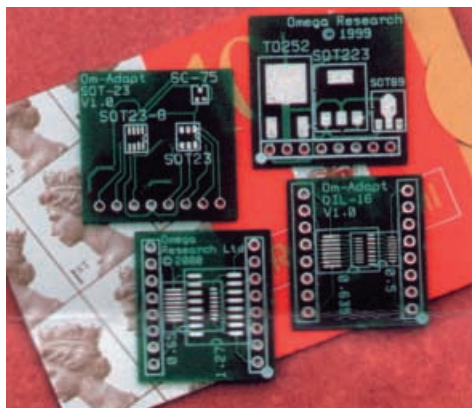
### Designer Challenge

The challenge, which defeats designers without know-how in the field of disk

recording, is to servo-control recording and playback of tightly spaced data tracks at different depths.

Dr Peter Fryer is Director of Research at B&W, a world leader in high quality loudspeakers. In the 1980s, while working for another speaker company, Wharfedale, Fryer pioneered the use of lasers to analyse cone motion. He laboriously stored a pair of images on a high resolution photo plate which then interfered to create a hologram. Fryer says he would jump at the chance of storing whole series of images in real time on a disk. "People have been talking about this for 20 years. Now let's see if they can really deliver".

## ADAPTING TO THE FUTURE



FOR some time, whilst chip manufacturers forge ahead with packing more functionality into yet smaller devices, development engineers have struggled with the problem of prototyping with these devices. The usual solution of using the leaded version of the chosen device is becoming increasingly difficult with more and more chip makers not producing these variants.

To address this situation, Omega Research has created the Om-Adapt family of miniature SMD adaptors, each tailored to suit a variety of package sizes. The adaptors have an outer array of 1mm pin holes tracked back to the SMD device. The holes allow the adaptor to be more readily connected to several successive developments of a prototype design without the need for tricky SMD soldering for each stage.

Whilst the soldering of many SMD packages to the adaptors is probably beyond the capability of hobbyists, the pitch between the tracks being far too minute to allow success without sophisticated equipment, the technique obviously provides a viable solution for industrial designers.

It does hammer home that increasingly many of the newer chips can never be used by hobbyists for their designs. However, even more than a decade ago, we recognised that this would increasingly become the case. It is reassuring, though, to know that many common devices so vital to our hobby are still readily available as standard packages, which we can conveniently use with our stripboards and p.c.b.s., and seem likely to be so for many years yet to come.

Nonetheless, it is interesting to note that even industry is finding the newer SMDs problematic at the circuit design stage, justifying the use of adaptors such as Om-Adapt.

For more information on Om-Adapt, contact Omega Research Ltd., Dept EPE, 44 Coles Road, Milton, Cambridge CB4 6BW. Tel/Fax: 01223 519458.

E-mail: [adm@omega-research.co.uk](mailto:adm@omega-research.co.uk)

Web: [www.omega-research.co.uk](http://www.omega-research.co.uk)

# LIGHT BARRIER BROKEN!

A fundamental concept has been challenged.

John Becker reports.

**A**STONISHING – the finite speed of light appears to have been exceeded! A report in the *Sunday Times* of 4 June 2000, authored by their Science Editor Jonathan Leake, states quite categorically that “scientists claim they have broken the ultimate speed barrier: the speed of light”.

Apparently, at the NEC Research Institute in Princeton, USA, Dr Lijun Wang “transmitted a pulse of light towards a chamber filled with a specially treated caesium gas. Before the pulse had fully entered the chamber it had gone right through it and travelled a further 60ft across the laboratory”.

Dr Wang’s interpretation of this phenomenon is that the pulse exceeded the speed of light by 300 times. The conventional definition of the speed of light (in free space) is  $2.997925 \times 10^8 \text{ms}^{-1}$ , or 186,000 miles per second in rounder terms.

## Profound Significance

If indeed the light barrier has been broken, this has the most profound effect upon Einstein’s assertion that the velocity of light is constant and cannot be exceeded. His famous formula of  $E = mc^2$  is based upon this assertion.

Further, it raises serious questions about our interpretation of the nature and size of the universe. It is understood that the latter is already in question based upon observations of far distant galaxies by the Hubble Space Telescope. They are far better formed than theory would predict, their estimated distances and evolution being based on how far they and their light would have travelled since the Big Bang.

The *ST* also suggests that Dr Wang’s light pulse travelled forward in time and that this “would breach one of the basic principles in physics – causality, which says that a cause must come before an effect”.

## Cautious Response

We contacted NEC Research and asked for more details of Dr Wang’s experiment. NEC spokesperson Carla Tomko replied by E-mail that “Dr Wang is not at liberty to discuss his research until such time as his article is published in *Nature*. Please watch for the article in an upcoming issue”.

Browsing through NEC’s web site ([www.neci.nj.nec.com/neci-website/bios/wang.html](http://www.neci.nj.nec.com/neci-website/bios/wang.html)) revealed a thumbnail biography of Dr Wang, which states that his “research was in quantum optics and laser physics during his Ph.D years, and light interaction with matter and the manipulation of atoms during his postdoctoral work at Duke University”.

Of himself, Dr Wang says “part of my research interest in quantum optics is to identify various aspects of such limitations”. He would appear to be exceeding his expectations!

It must be stressed, of course, that before the light barrier can be accepted scientifically as having been exceeded,

Dr Wang’s findings must be corroborated by independent researchers, a matter which may take many years of experiment and controversy. However, the *ST* also reported that physicists at the Italian National Research Council have propagated microwaves at 25 per cent above normal light speed.

We await seeing Dr Wang’s report in *Nature* with great interest. The *ST* story can be accessed at [www.sunday-times.co.uk/news/pages/Sunday-Times/stifnusa01007.html](http://www.sunday-times.co.uk/news/pages/Sunday-Times/stifnusa01007.html).

## ON-LINE TIMER

By Barry Fox

PARENTS who fear huge phone bills when their children get hooked on the Internet or on-line Dreamcast games can try paying £40 for a Timed Internet Connection from Pulse Design of Bognor Regis.

TIC plugs between the PC or games console and phone line and limits both the connection time allowed during each 24 hour period that follows the setup time, and the waiting time between connections. Bleeps warn when automatic disconnection is imminent.

Parents choose a 4-digit PIN which prevents unauthorised reset, and the unit sounds if unplugged. Savvy parents may also want to secure the plugs with glue.

For more details contact Pulse Design, Dept EPE, PO Box 81, Bognor Regis, Sussex PO22 8YP. Tel: 01243 827179.

## LONG LIFE LAMPS

By Barry Fox

MATSUSHITA, the Japanese company which makes Panasonic electronics, promises long life lamps that use microwave energy to make artificial daylight. Existing lamps use a white hot filament or electrodes that create plasma, and burn out after a few thousand hours. Matsushita’s lamp uses microwaves, at the same 2x45GHz frequency as microwave ovens.

The waves resonate and amplify as they pass through a series of 10mm compartments and hit a 3mm plate coated with a mix of indium and bromide compounds that radiate light with a colour temperature similar to daylight. Microwave power is 50 watts. Because there are no filaments or electrodes to fail the lamp lasts 60,000 hours. Matsushita says product will be ready to sell by 2002.

## Smart Home Security

BRITISH Telecom has announced a new technology, Smart-Electrics, which is set to revolutionise the protection of people and property in home and business settings.

It will make stolen TVs, videos and hi-fi equipment useless to burglars and provide an economical platform to monitor homes or workplaces for break-ins, fire, smoke and environmental factors.

The technology is based on a simple intelligent “home control centre” to which electrical appliances are registered through a low bit rate communications protocol. Subsequently, any Smart-Electrics equipped electrical item which is stolen will refuse to operate when plugged into the mains in an unauthorised location.

More information can be obtained via BT’s news release centre accessible through [www.bt.com](http://www.bt.com).

## NIMROD GAS IRON



INTRODUCED by Shesto, the Nimrod range of three stylish cordless butane soldering kits are described as “go-anywhere tools that can be instantly converted from soldering iron to blow torch, hot knife or heat blower”. They are said to be ideal for occasional hobby, electrical and DIY tasks as well as for professional use.

Supplied with a range of interchangeable heads, these 4-in-1 tools can perform soft soldering, silver soldering, heat shrinking, and cutting and modelling of waxes, thermoplastics and foams. All three kits in the Nimrod range are powered by standard butane lighter fuel.

For more information contact Shesto Ltd., Dept EPE, Unit 2, Sapcote Trading Centre, 374 High Road, Willesden, London NW10 2DH. Tel: 020 8451 6188. Fax: 020 8451 5450. E-mail: [sales@shesto.co.uk](mailto:sales@shesto.co.uk). Web: [www.shesto.com](http://www.shesto.com).

# New Technology Update

*Are we looking at the beginning of the age of atom-sized electronics, asks Ian Poole*

FOR SOME time now integrated circuit developers have been saying that the concepts currently used for semiconductor devices will not be able to meet the requirements for higher speed, greater levels of integration and higher levels of functionality that will be needed by equipment manufacturers in the years to come. Researchers have been warning that the limits of the current technology will be reached very soon.

In fact, it has been a tribute to the semiconductor industry that they have been able to take the technology as far as it has come. With sub-micron feature sizes now common place, it is believed that the end of the road for the current technology might at last be in sight, and totally new technologies will be needed.

With the requirement for a new technology established and a number of ideas on the horizon, it is likely that we will see a new revolution in the world of electronics. This could be more far reaching than that caused by the introduction of semiconductors and the subsequent introduction of the integrated circuit.

## Electron Electronics

In recent years a number of organisations have been investigating the possibilities of using single electrons to act as the stimulus in circuits, rather than the very large number currently required. This could, it is said, bring about a complete change in the way that circuits operate, and in the sizes that are achievable. It is also claimed that this will result in a completely new technology being introduced and semiconductor devices as we know them today being confined to the archive box.

The possibilities that the new technologies could offer are tremendous. Not only would the size reductions enable data to be stored in a far more compact way, vastly increasing storage capacity, but also they would enable much greater speeds to be attained. With the enormous requirement for faster and smaller devices, with greatly increased functionality, such a new technology would enable electronics to take a quantum leap forward.

Although the idea of being able to develop atom-sized devices has been demonstrated, this type of technology is in its infancy and there are still many obstacles to overcome before atomic electronic devices become available for use. Currently an atom-sized memory device has been demonstrated, where a single electron is stored to give a single bit of data. However, other devices are also required if the technology is to become a reality.

As part of the development of this new area of technology scientists at the IBM Almaden Research Centre in California have developed a new method of using the wave nature of electrons to transport information on an atomic scale.

## It's No Mirage

The effect is known as the "quantum mirage effect", and it should enable data to be manipulated on an atomic scale within true micro-circuits. These will be so small that any form of conventional technology within the circuit will not be possible, and this even applies to the interconnections.

This quantum mirage effect can best be described as a way of guiding information through a solid material and can be used within a number of devices for handling data.

The discovery of the mirage effect was made by three IBM scientists: Manoharan; Lutz; and Eigler. They used a low temperature scanning tunnelling microscope (STM) to arrange several dozen cobalt atoms into an ellipse on a copper surface. This acted as what is known as a quantum corral, reflecting the surface electrons of the copper into a wave formation.

By placing a cobalt atom at one focus of the ellipse it is found that a smaller "mirage" atom appears at the other focus. The actual intensity of the mirage depends upon the quantum state of the ellipse and in turn this is dependent upon its shape.

The research has set up a number of experiments with these devices. Corrals with a variety of shapes have been built and tested. Lengths up to 20nm and widths less than 10nm have been tried to assess the differing properties that can be obtained.

Using the new technology the research scientists have commented that devices using this technology could be made exceedingly small. The basic concepts also permit many interesting experiments into topics like investigating magnetism at an atomic level, and possibly manipulating individual electron spins.

These experiments are not directed towards any particular devices at the moment, but rather at gaining a greater understanding of these effects. This will undoubtedly be useful in developing this technology and could ultimately lead to the development of further new devices.

## Current State of Affairs

It is understood that the manufacture of a device for commercial applications is still many years away. Developing the concept for use in a realisable form for widespread use still requires very significant amounts of development. For example, the process

of making the ellipse is very much a laboratory technique and not practicable for widespread use.

Moreover, there are no techniques for connecting individual devices together and this will need to be resolved before any real use can be made of the technology, even on a laboratory scale. Also a rapid and power efficient way of modulating the available quantum states must be found.

Although this technology may seem rather futuristic, pressure will soon start to build as the feature sizes on integrated circuits fabricated using traditional circuits reach the final limits.

## Looking Ahead

The ability to manipulate atoms and molecules on an individual basis has opened a new experimental frontier that makes feasible the quest for single molecular-scale devices as successors to the transistor. Not only will digital circuits be available, but if the technology is to become truly universal then analogue circuits must be fabricated as well.

Further developments that are under way include the construction of an amplifier. Work on this is already in progress using a single molecule less than 1nm in diameter. Again this device works by using a quantum mechanical effect, and to date a gain of 5 has been verified.

The implications of these developments show that scientists are very seriously looking toward the development of devices on atomic scales using totally new techniques and ideas but based around quantum mechanical effects. It is claimed that this is likely to bring a completely new way of thinking to electronics, ultimately making making discrete components a thing of the past.

How and when this will happen, and the time-scales involved still remain to be seen, but it most certainly is a development that is clearly in view, even if it is right on the horizon. The big question is how long these circuits will take to become reality.

A tremendous amount of investment is required to be able to realise devices that could be used, and then it will take a considerable amount of time before they would be commercially viable. It took over ten years from the first discovery of the transistor before they were widely used.

Initially they were far too expensive for universal use. Now transistors and integrated circuits can be obtained for a few pence. As this technology is likely to require considerably more development and investment in plant, it could take much longer before devices appear in the equivalent of transistor portable radios.





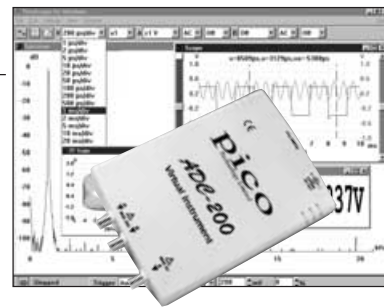
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## Cool Controller – A Cool Number

A NOVEL thermostatic controller for a small d.c. operated fan, perhaps for computer cooling or similar, is shown in Fig.1. The LM3914 bargraph driver IC2 contains 10 comparators which are referenced at 1.2V and set at pin 8. Pin 5 of the device provides the comparator network with an input. The sensor circuit stage, which provides the temperature-sensitive signal to the LM3914, comprises of a voltage divider made up of resistor R2 and n.t.c. thermistor R3, potentiometer VR1 and transistor TR3.

Changes in temperature are amplified by the transistor TR3 and applied to pin 5. A bargraph output is obtained from pins 10 through to 18 and also pin 1. These outputs are connected to ten current limiting resistors and

l.e.d.s. The motor driver circuit takes an input signal from any of the ten available points between the resistors and l.e.d.s. This drives a *pnp* transistor switch TR1 which in turn activates the Darlington power switch TR2 and drives the fan motor.

The temperature level selected is reflected by the corresponding l.e.d. being brighter than the rest of the display, which allows the user to monitor what level has been selected. The motor turns on once the predetermined temperature has been reached and switches off when the temperature falls below the selected level.

M. N. Beg,  
Johannesburg,  
South Africa.

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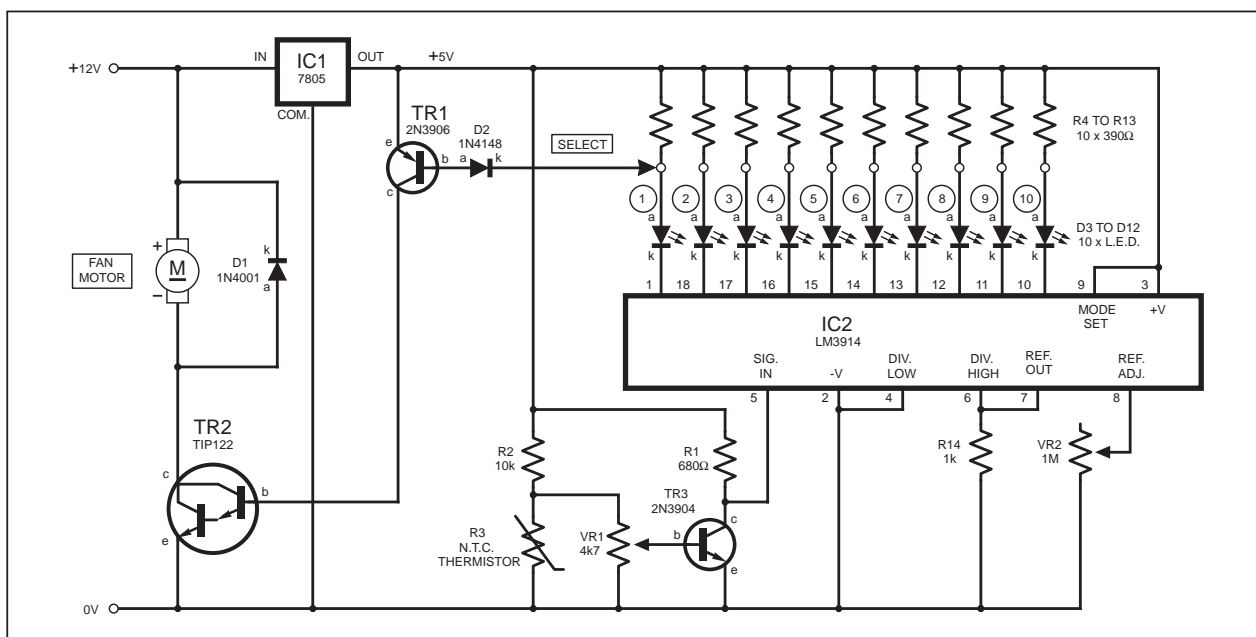


Fig.1. Circuit diagram for a thermostatic controller for small d.c. operated fans.

## VOM Continuity Buzzer – Making a Buzz

LIKE many electronic enthusiasts, I own both analogue and digital multimeters plus a home-made continuity buzzer. The continuity tester function on the digital voltmeter (DVM) is handy but I find that for most of the time I prefer my conventional analogue volt/ohmmeter (VOM) for troubleshooting purposes.

For many experimenters, it would be ideal to have a similar continuity buzzing function on an analogue VOM. The idea came about whilst testing the current sensitivity of a miniature reedswitch. It struck me that as little as 100mA was required to operate it with a coil of 200 turns. The coil resistance was only a few ohms.

Referring to the schematic of my VOM resistance range, see Fig.2a, it was noticed that a large current of 150mA passes through one 19 ohm resistor during the Rx measurement. By wiring the reedswitch operating coil in series with resistor R4 and putting a shunt resistor (R5) in parallel across it (see Fig.2b), the total resistance value between point A and B remains unchanged.

In my case, the added resistor is 180 ohms but its value depends on the resistance of the series reed operating coil. This does not affect normal Rx measurement but whenever the external resistance Rx falls below 33 ohms, the reed contact closes which sounds a 3V piezo buzzer WD1.

The circuit wiring as shown was inserted into my VOM but actually failed to work properly when the case was closed up. Eventually, I realised that the strong magnetic field of the meter had been biasing the reed's own closing magnetic field. The solution is simply to reverse the coil leads and everything worked happily ever after.

Mr. Lim Chung,  
Haywards Heath, W. Sussex.

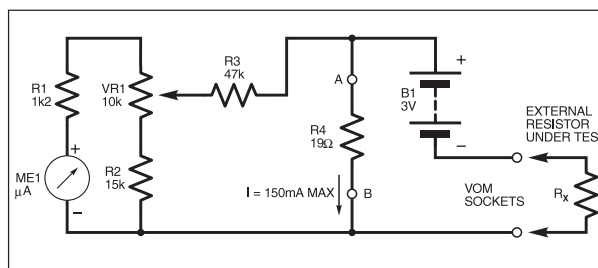


Fig.2a. VOM Rx measurement range circuit.

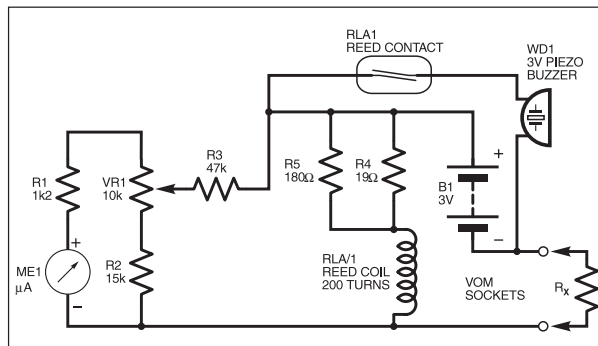


Fig.2b. Circuit showing VOM Rx measurement with buzzer added.

## Square Wave Circuit – Safe and Simple

THE circuit diagram of Fig.3 supplies a square wave at 100Hz derived from the mains but is isolated from it for safety by an opto-isolator. The mains voltage is reduced to around 9V using a small transformer (e.g. a scrap or surplus one) and full-wave rectified by bridge diodes D1 to D4 to give a waveform pulsating at 100Hz.

This is applied via resistor R1 to the i.e.d. emitter of an opto-isolator. The signal is then transmitted optically to the photo Darlington transistor (TR1/TR2) in the same package as the i.e.d. This isolates the mains by using the light rather than a direct connection to carry the signal.

A separate 9V d.c.-operated circuit is used in which the square wave is generated. The unusual part of the circuit is that the Darlington pair is used not only as part of the opto-isolator, but also as the input of a discrete Schmitt trigger which is used to "square" the output of the opto-isolator. The diode D6 in the emitter circuit of TR1/TR2 produces an almost constant emitter voltage which assists the Schmitt switching action.

### Opto-Isolator

The original circuit used a TIL119 isolator but it should be possible to use other types, even those using a single phototransistor instead of the Darlington, provided that resistor R1 is adjusted in value to give a reasonable i.e.d. current. With a 9V transformer a value for R1 of 4.7 kilohms (4k7) is a good starting point.

None of the part types is critical. Transistor TR3 is a BC149B which can be substituted with any silicon npn small signal high gain transistor (e.g. 2N3903 – ARW). The values given result in a 1:1 mark-space ratio at the output, which is

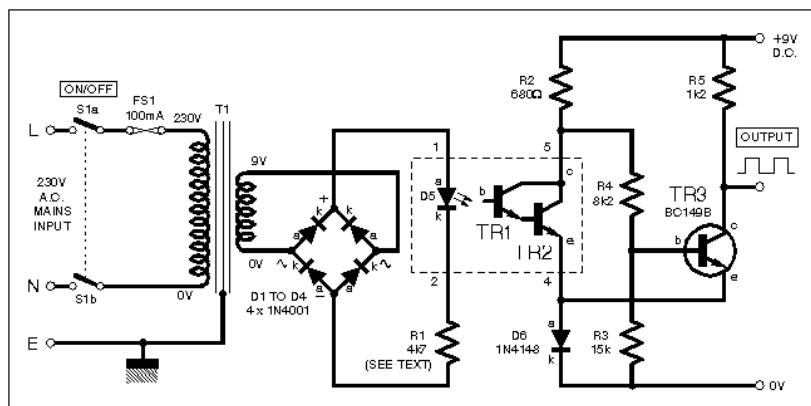


Fig.3. Square wave generator circuit. L.E.D. D5 and TR1/TR2 are contained within an opto-isolator. The circuit requires a separate 9V d.c. power supply.

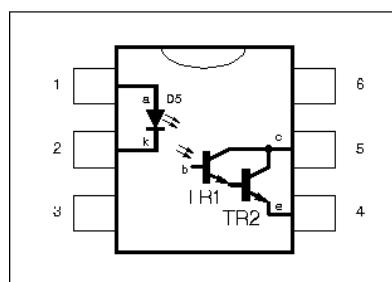


Fig.4. Pinout information for the TIL119 opto-isolator.

taken from the collector of TR3. (Overseas readers using a 60Hz supply will see a 120Hz signal. – ARW)

J. C. Stephens (G7WJK),  
Accrington, Lancs.

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# TEACH-IN 2000

## Part Ten – Transformers and Rectifiers

JOHN BECKER



Originally we had intended that the *Teach-In 2000* series should run to 10 parts. A somewhat restricted intent as it turns out! There has been far more information that we felt we should impart to you than could have reasonably fitted into just 10 parts. The Op.amps discussion, for example, had to be split into two sections, in Parts 7 and 8. Now Power Supplies have to be split into two sections as well.

In this part we examine how the mains a.c. supply can be converted to a much lower d.c. supply that can be used to power your electronic circuits. Thus it is Transformers and Rectifiers which are discussed this time. In Part 11 we shall look at how d.c. voltages can be regulated and how CR ratios affect their stability. The series will end with Part 12, in which we shall discuss displays and digital-to-analogue converters.

**P**OWER, without which *nothing!* Well, a rather grand and all-embracing statement, perhaps, but it certainly sums up the situation as far as running electronic devices is concerned – you can't use them without electrical power, as you discover each time you disconnect your battery . . .

We shall now discuss power transformation and control, partly using your computer to help demonstrate some of the fundamental facts. In the Experimental section we'll then get you trying out some of the concepts and see how they work. Most will be done with your breadboard and 6V battery, with the option to use a mains adaptor (battery eliminator) next month.

Before we start, run your computer and from the *Teach-In 2000* program's main menu select Power Supplies – Menu. From that sub-menu select Transformers – Principle (see Photo 10.1). We'll discuss the screen display presently.

### TRANSFORMERS

In the second paragraph above we used the phrase "power transformation". In the electrical sense that we discuss here, this means "transforming" the mains power supply that comes into your home at "officially" 230V a.c. (110V a.c. in some countries) to a lower and safer level more suited to conversion to a d.c. level that can power electronic circuits.

It is devices that do the transforming that we shall discuss first. Not unreasonably, they are called *transformers*.

A transformer is a device that can not only convert one a.c. supply voltage to

another, but it is usually designed so that it provides isolation (total separation) between the two.

That a transformer can do both of these things is due to another of those remarkable Laws of Nature, of which we have already met a few, this time related to magnetic fields and current flow.

One effect of this is that if an alternating current flows through a coil of wire that is wound around a magnetic material, a rod of

Furthermore, it is a hard-and-fast fact of life that if an electrical current flows, then a voltage differential exists between the current source and its destination.

Note that in a real transformer the wires are insulated to prevent electrical short circuits between each turn.

### TAKING A TURN

There is an interesting relationship that also exists when one coil induces current/voltage in the other. The voltage that is induced from the first coil into the second depends on the number of turns of wire that make up each coil.

For example, if an alternating current of 10V a.c. flows through the first coil (helpfully known as the *primary coil*) consisting of 100 turns of wire, then if the second coil (not surprising called the *secondary coil*) also has 100 turns of wire, the resulting "transformed" voltage will also have a value of 10V a.c.

However, if the secondary has only 50 turns of wire while the primary has 100, then the secondary voltage will be  $10V \times (50 / 100) = 5V$  a.c. Conversely, if the secondary has 200 turns when the primary has 100, the resulting output voltage will be

$10V \times (200 / 100) = 20V$  a.c.

(Our ancestors earlier than the 19th century would have loved to know such simple facts! Many eminent minds puzzled over magnetic fields and current flow before being able to define the principles, notably such researchers as Andre Ampere, Michael Faraday and Joseph Henry, for example.)

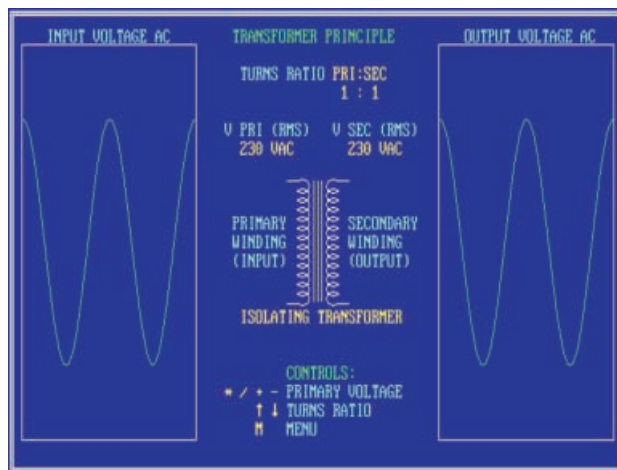


Photo 10.1. Screen dump of the interactive demo illustrating the transformer principle.

iron for example, an alternating magnetic field is set up around and within it. If another, separate, coil is also wound around that same rod, the alternating magnetic field caused by the first coil is *induced* into the second coil. This induction causes a current to flow through the second coil when its ends are connected together, usually via the circuit to be powered.



We can now summarise the voltage transformation according to:

$$V_{\text{sec}} = V_{\text{pri}} \times (T_{\text{sec}} / T_{\text{pri}})$$

where:

$V_{\text{pri}}$  = voltage across primary winding  
 $V_{\text{sec}}$  = voltage across secondary winding  
 $T_{\text{pri}}$  = number of turns in primary coil  
 $T_{\text{sec}}$  = number of turns in secondary coil

You should be aware, however, that the above formula represents the state of affairs in an ideal world – which seldom exists! There are conditions attached to voltage transformation, as we shall discuss presently.

## TRANSFORMER SYMBOLS

Have a look at the Transformers – Principle screen (and Photo 10.1). In the centre is shown the basic symbol for a transformer, comprising two coils representing the primary and secondary windings, with the vertical lines between them representing the magnetic material around which the coils are formed.

As shown, the coils have the same size, indicating that the primary and secondary voltages are equal. This is confirmed by the stated Turns Ratio of 1:1, and the voltages input and output are also given.

Because the two coils are not *physically* connected to each other, there is *complete electrical isolation* between them. The fact that current and voltage are present on the second coil is entirely due to *magnetic coupling* between it and the first coil.

Indeed, a transformer in which the primary and secondary coils are physically separate is actually known as an *isolating transformer*, although conventionally the term is generally taken to apply to a transformer connected to the mains power supply and having a turns ratio of 1:1 (same voltage out as in).

A transformer in which the secondary has fewer turns than the primary (voltage out is less than voltage in), is known as a *step-down transformer*. Where the secondary has more turns than the primary (voltage out is greater than voltage in), the term *step-up transformer* is used.

The circuit diagram symbols often associated with the three above transformer types are shown in Fig.10.1a to Fig.10.1c.

## PRIMARY DIRECTION

It is worth noting that the primary coil is that *into which* the power flows. The secondary coil is always that *from which* the transformed power is output. Even if you use the second winding as the power input (as it can be in some special situations), it then becomes called the primary, and the original primary becomes known as the secondary.

Via the screen display you now have running, you can change the primary voltage and the turns ratio, observing the way in which the gently scrolling sine wave (the normal waveform shape for which most transformers are designed) changes in amplitude, within the limits of the display. The control keys are stated on-screen.

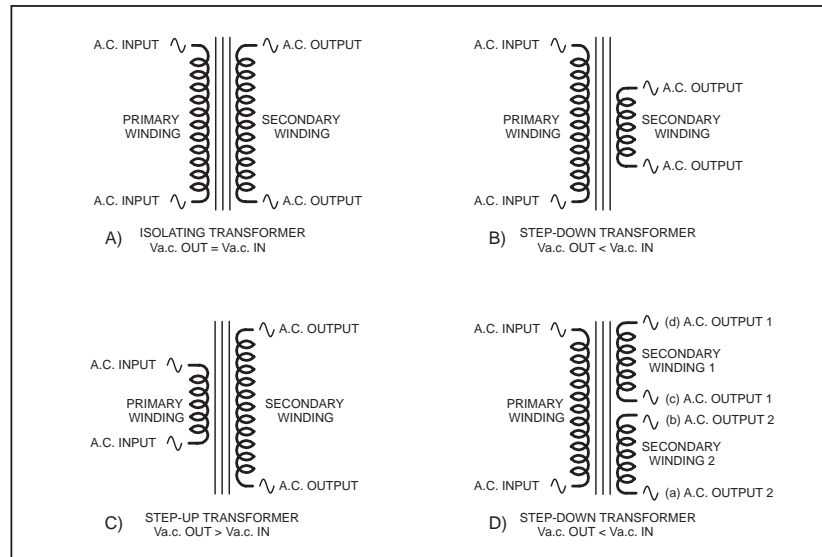


Fig.10.1. A selection of transformer symbols. Many variants exist.

Note how the transformer symbol changes between the symbols in Fig.10.1a to Fig.10.1c, depending on the turns ratio selected.

## MULTIPLE SECONDARIES

Which brings us to another interesting fact, as symbolised in Fig.10.1d.

As this illustration implies, it is permissible to have more than one secondary coil on a transformer. You could have dozens if the space is available. Note, though, that each of the additional coils is still referred to as a “secondary”.

Each of the secondaries can have its own number of turns of wire in its coil, so that each produces a different output voltage. These secondaries can either be used as separate voltage sources (also isolated from each other), or they can be connected in series so that the output voltage is the total of the two secondary voltages.

In this last situation, it is important to connect the coil terminations in the correct order. In Fig.10.1d, it is the terminations that are shown closest to each other (terminations *b* and *c*) that are joined when series connection is required.

This ensures that the phases of the sine wave do not oppose each other and cancel out the voltage transfer, while greatly increasing the current flow, possibly to the detriment of the transformer (windings could overheat and burn-out).

Note that some transformers have a *centre tap* terminal which is the equivalent junction of two separate windings having been joined.

Transformers usually have their terminals notated. For example, one having twin secondaries of 15V a.c. would typically have them marked as “0V-15V, 0V-15V”. In Fig.10.1d, the respective terminations would be *a-b*, *c-d*.

As separate windings it would not usually matter which of each winding’s terminals were connected to a subsequent circuit. For series connection, though, the two central terminals would be connected together, the two outer terminals then connected to the next circuit, in this case as a 30V a.c. supply.

Some mains transformers have two primary windings. They might typically both be 115V a.c. For 230V a.c. use they would be connected in series, again ensuring that the correct terminations are used.

## POWER RATIOS

There is a crucial fact to remember about the power available from a transformer. The total power available from the secondary windings can never be greater than that which passes through the primary winding. You don’t get free gifts in this context!

Suppose, for instance, that the primary has 100V a.c. alternating across it at a current of 1A. Suppose also that the secondary is a step-up winding having twice the number of turns of the primary, resulting in a secondary voltage of 200V a.c.

The power flowing through the primary (remember Ohm’s Law, watts = volts × amps) is 100V × 1A = 100 watts. Now, the secondary has 200 volts alternating across it, but because it can only supply the same *power* as passes through the primary, the current available from this winding is thus 100W / 200V = 0.5A.

Conversely, for a step-down transformer producing a 50V output for a 100V input at 1A, the output current available is actually greater, at 2A: 100W / 50V = 2A.

The rule that you can never take out more power than you put in applies irrespective of the number of secondaries. Each secondary can be capable of supplying different voltages and currents, but the total power available from them at any instant can never exceed that available from the primary.

In fact, because the power transfer between the primary and secondary windings is not perfect, the secondary power available is always somewhat *less* than that passing through the primary.

This fact, however, will not normally trouble you – it is the manufacturer who has to be concerned about transformer efficiencies. He then tells you how much power can be output from the secondaries, he might even tell you how much power the primary consumes in order to meet that maximum obligation (but don’t bank on it).

## VA POWER FACTOR

The way in which transformer power capabilities are expressed is as a “VA” factor. This is really just another way of stating the power in watts, since VA simply stands for Volts × Amps, which you already know equals Watts.

Thus, for example, a transformer that is said to have a 20VA rating might have been designed to supply 20V at 1A, or 10V at 2A, or even 100V at 0.2A. These values would only apply, though, if the transformer had a *single* secondary winding.

Where more than one secondary is provided, and each possibly having different voltage and current characteristics, the situation can become less clear. However, helpful transformer suppliers should clarify the capabilities of their products through their literature.

For instance, a catalogue statement about a 50VA mains transformer having two 15V secondary windings might also clarify it by stating “2 × 0-15V, 1.67A”. This means that with the total power available being 50VA, each secondary can supply (50VA / 15V) / 2 = 1.6666A, i.e. 1.67A. With a bit of luck, the catalogue might actually tell you that each secondary winding is rated at 25VA.

Assuming that the primary is being powered at 230V a.c., the current though that winding must be (at least) 50VA / 230V = 217mA.

Note that transformers having twin secondaries of the same voltage can usually have their windings connected in parallel to increase the current available (total current available is the total of the currents available from each winding). They must be correctly connected, though, e.g. 0V to 0V, and 15V to 15V, (a to c and b to d, referring to Fig.10.1d).

## INTOLERANCE

We have commented before that all electronic component values are subject to various tolerances. Transformers are especially intolerant!

A secondary winding’s voltage rating is normally only said to hold true when the current it supplies is at the maximum rating, known as “full load”. For currents less than this, the output voltage will be greater than nominal. For currents greater than ideally permitted, the voltage will drop below nominal.

The amount of output voltage change relative to the current drawn will vary between transformer types and quality of manufacture. In the case of the 50VA transformer just referred to (as used by the author in one of his workshop power supplies), this is quoted as having a typical regulation rating of 10 per cent, calculated as:

$$((V_o - V_f) / V_o) \times 100\%$$

where:

$V_o$  = off load voltage  
 $V_f$  = full load voltage

However, a 6VA transformer from the same manufacturing range is quoted as having a typical regulation of only 25 per cent.

Also note the use of the word “typical”: this is the manufacturer’s statement about

the regulation, but it is not necessarily qualified in the literature. The actual difference in practice is likely to be small, though.

What is of far greater significance is the fact that the UK mains supply is not held at a constant voltage level. To comply with EU regulations, the “official” UK voltage level is 230V a.c., although more typically it is likely to be around 240V a.c. It is permitted to vary within about six per cent of the nominal value.

Naturally, such deviant behaviour affects the voltages supplied by transformer secondary windings, by similar percentages.

Next month this is going to lead us into discussion about how you “regulate” or “stabilise” a d.c. power supply that has been derived from a somewhat unpredictable mains a.c. supply.

But, first things first – a discussion on “conversion”!

## RECTIFICATION

Although we can regard the changing of an a.c. supply to d.c. as a process of conversion, the correct term in this context is *rectification*. From your Power Supply program’s sub-menu select Rectifier – Half-Wave, see Photo 10.2.

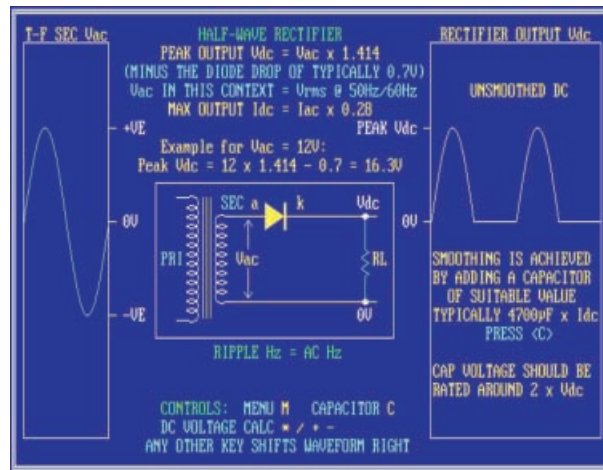


Photo 10.2. Screen dump of the interactive demo illustrating half-wave rectification.

We used the word “rectify” in passing while discussing diodes in Part 4. You will recall that a diode conducts in one direction only, from its anode to its cathode.

A mains a.c. supply is a sine wave (as discussed in Part 5) and as such alternates symmetrically above and below a mid range value, usually taken as 0V with respect to mains a.c. voltages.

This is the situation we assume in Photo 10.2 and on the Half-Wave Rectifier screen display. The circuit diagram shows a transformer secondary winding with one end of the coil regarded as the 0V termination. The other end is connected to a diode, on the other side of which is shown a resistor (RL), whose other end is also connected to the 0V line.

The sinusoidal a.c. waveform from the secondary winding ( $V_{ac}$ ) repeatedly sweeps above and below 0V at whatever the maximum a.c. secondary voltage is provided by the transformer. Let’s call it 12V a.c.

You will recall that in Part 5 we discussed waveform voltages in terms of

r.m.s., peak, peak-to-peak, etc. Because we are now dealing with a sine wave, its a.c. voltage value can be taken as identical to its r.m.s. value, which Part 5 stated has a peak value 1.414 times greater.

In other words, the 12V a.c. value of the example circuit in Photo 10.2 has an actual peak value of 12V r.m.s. × 1.414 = 16.968V peak, call it 17V. That is, the waveform is evenly and repeatedly swinging between +17V, though 0V and down to -17V, and then back up again.

While the voltage is positive, current is conducted through the diode and resistor, back to the 0V line. The resistor, of course, simply represents any other circuit (the “load”) to which the voltage can be sent.

A *negative* voltage, however, will not be conducted through the diode or the load.

In this circuit, the positive half of the sinusoidal waveform across the secondary winding thus appears at the junction of the diode and resistor ( $V_{dc}$ ), as the waveform shown at the right of Photo 10.2. It has lost the lower half of its waveform (the negative half), hence the name of this circuit, *half-wave rectifier*.

There is also the matter of the voltage drop across the diode itself to be taken into account. As discussed in Part 4, for a typical silicon diode, a voltage drop of about 0.7V occurs.

As a result, it is only when the secondary voltage rises above that 0.7V threshold that current and voltage will be conducted through it and into the load. The load thus only “sees” a maximum voltage of about 0.7V below the secondary’s positive peak value.

We can thus define the peak voltage seen across the load as being:

$$\text{Peak } V_{dc} = (V_{ac} \times 1.414) - 0.7V_{dc}$$

which in our example produces:

$$(12V_{ac} \times 1.414) - 0.7V_{dc} = 16.3V_{dc}$$

## WAVEFORM SMOOTHING

So far the rectified waveform is unsuited to powering a normal electronic circuit. It is still varying between two levels, 0V and 16.3V d.c. Electronic circuits, though, usually require a d.c. voltage that is considerably more stable than a repeating series of peaks and troughs, known as the *ripple* voltage. We need a way of smoothing out the ripple.

Such a smoothing component is readily available, and you’ve already been using it – the capacitor. Press <C> on your keyboard. In the resulting screen display a capacitor has now appeared in parallel with the load resistance, and the output  $V_{dc}$  waveform has become a straight line – the ideal rectified and smoothed d.c. supply voltage waveform!

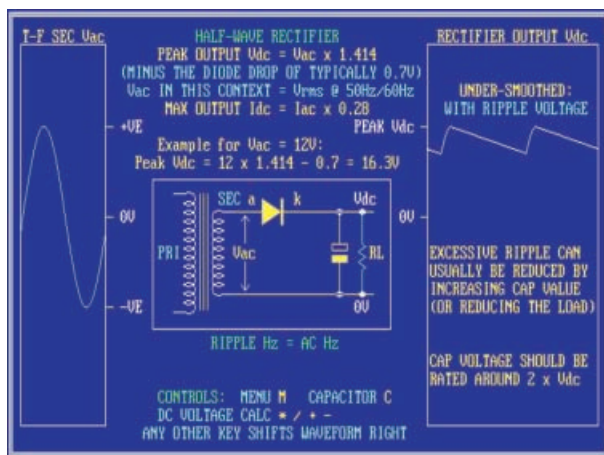


Photo 10.3. Screen dump of the demo showing a capacitor in parallel with the load (RL), and an exaggerated example of ripple voltage.

In Part 2, you proved for yourself how a capacitor can be charged up at one rate and discharged at another. This characteristic makes a capacitor ideal for smoothing rectified waveforms suitable for powering an electronic circuit.

The principle is that the capacitor in parallel with the load resistance charges up at the fastest possible rate while the rectified waveform is positive and above 0.7V d.c. When the a.c. waveform applied to the diode falls below the peak d.c. voltage now held by the capacitor, the capacitor discharges at the rate determined by the resistance of the load connected to it, not through the diode.

The knack is to choose a capacitor whose value is sufficiently great such that it does not significantly discharge through the resistance before the next cycle of the rectified waveform rises positively again, thereby "topping-up" to replace any charge lost.

Press <C> again to display an exaggerated waveform that illustrates the capacitor's ripple voltage as the resistive discharge and top-up cycle repeats, see Photo 10.3 as well.

## SMOOTHING CAPACITOR VALUE

To find out what capacitance value (C) is best suited to a particular load resistance to minimise the ripple, you need to know the maximum current (Idc) that the load is ever expected to draw.

Typically, the ideal minimum capacitance value that will reduce the ripple until it effectively no longer exists is calculated by the simple formula of:

$$C = 4700\mu\text{F} \times \text{Idc}$$

Be aware, however, that a very small amount of ripple voltage will *always* exist, even when using exceptionally high value capacitors and drawing a very small load current. In a lot of cases, though, a minor amount of ripple may not matter.

Whilst the above formula offers guidance, the final choice of capacitor value could be lower or higher than calculated, depending on just how smooth the load circuit requires its power source to be (but there are ways to avoid making an alterna-

tive choice, as we shall show next month when we examine voltage regulation).

The initial problem, though, is how to establish the value of the maximum current that will be required.

There are three basic solutions:

1. Calculate and total-up the current required by each component in the load circuit (a tedious matter that is fraught with mathematics).
2. To assume a worst case condition, that the maximum load will require the maximum current that the transformer can supply.
3. Or (as we've suggested before in another context) to cheat! Guess the likely maximum current and select a capacitance value based on that. Then, if your guess turns out to be too low, to increase the capacitance value. (Your increased experience will eventually tell you what range of capacitance value the guess should fall into.)

With a mains power supply, option 2 is the one that should be selected. There are, though, power control sub-circuits, driven from the principal power supply, for which the guesstimate technique can be satisfactory. Within certain limits, the test for power smoothing can be done using a meter, an oscilloscope, or even your ear.

The meter test will show if a *significant* lack of smoothing exists by the magnitude of the changing display numbers. An oscilloscope will show very fine detail of power line voltage smoothness. In the case of an audio circuit, the sound coming from the loudspeaker will sometimes tell whether or not mains "hum" exists in its power supply.

Whilst hum can come from other sources, it is always worth checking that the power supply is suitably smoothed, the hum being the residual multiple of 50Hz (or 60Hz in the USA and some other countries) ripple that the smoothing capacitor has not "mopped-up".

## OTHER FACTORS

Note that the smoothing capacitor's working voltage should ideally be at least twice the peak rectified voltage, and preferably more. For example, if the peak rectified d.c. voltage is 17V, choose a capacitor whose working voltage is at least 34V.

The diode type must be able to handle the current expected to pass through it. It

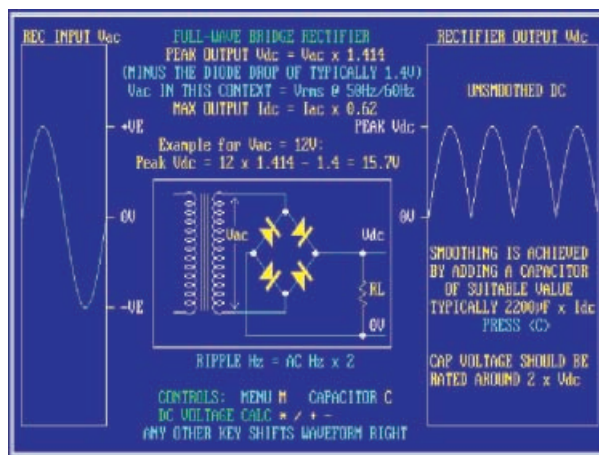


Photo 10.4. Full-wave bridge rectifier circuit as illustrated in the screen demo. Using a capacitor in parallel with RL would normally be used to smooth the ripple voltage.

must also have a voltage rating suited to the difference between the lowest voltage produced by the sine wave from the secondary winding and the peak d.c. voltage on the smoothing capacitor, otherwise known as the *peak reverse voltage*, or the *peak inverse voltage* (PIV), in this context.

As discussed in Part 4, diodes can only stop voltages flowing back through them within certain limits, as specified for the diode type. Again, allow a good margin for extreme circumstances.

In most normal power supply instances, a type 1N4001 rectifier diode will probably suffice. This can handle a current of 1A and a peak reverse voltage of 50V.

## POWER EFFICIENCY

In the half-wave rectifier circuit, only one diode is used, and so only one side of the waveform provides power. Instinct will tell you that the efficiency of a power supply rectified in this manner can at best only be half of what might be expected if both sides (phases) of the a.c. waveform could be used.

In fact, because we are dealing with a sine wave, the situation is worse than this and the maximum d.c. output current available is typically only 0.28 times the a.c. current consumed by the secondary winding. This is formally expressed as:

$$\text{Max Idc} = \text{Iac} \times 0.28$$

The screen display allows you to calculate the d.c. output voltage typically available using different a.c. input voltages.

## BRIDGE RECTIFICATION

There is a simple circuit configuration, known as a *full-wave bridge rectifier* circuit (or commonly just *bridge rectifier*), in which the current conversion efficiency is about twice as great as in the previous half-wave circuit.

From your Power Supply sub-menu, select Rectifier – Full-Wave Bridge, and also see Photo 10.4. In the screen display press <C> to bring the capacitor into circuit.

The equation for power conversion is now seen to be:

$$\text{Max output Idc} = \text{Iac} \times 0.62$$



Because the circuit effectively sees two diodes in series the formula becomes:

$$\text{Peak } V_{dc} = V_{ac} \times 1.414 - 1.4V$$

Note that the software incorrectly states a voltage drop of -0.7V instead of -1.4V.

There are four diodes involved in the bridge rectification process. They may either be individual diodes, or combined as a single purpose-designed component (actually known as a *bridge rectifier*) in which the four diodes are arranged in the same configuration.

The pin order for bridge rectifiers can differ with various types, but pin identities are invariably printed on the body. See Panel 10.1.

Note from the bridge rectifier symbol in Fig.10.2 how the diodes are arranged. As an exercise, work out which diodes are in use at which stages of the sinusoidal waveform. You've had sine waves and diodes explained previously, you should not find the task too hard.

Apart from increased output current availability, bridge rectifier circuits offer another benefit – the d.c. output is charged at twice the rate of the half-wave circuit, because both sides of the waveform are being used. This means that the UK 50Hz mains frequency is rectified at 100Hz, allowing a smoothing capacitor of about half the previous value to be used. A suitable capacitance value (C) is thus typically calculated as:

$$C = 2200\mu F \times I_{dc}$$

The working voltage of the capacitor should still be rated at around  $V_{dc} \times 2$ , or greater.

In all but the least demanding applications, the bridge rectifier circuit is the one that finds greatest favour.

Again the screen display allows you to calculate the d.c. output typically available using different a.c. input voltages.

## BI-PHASE RECTIFIER

A third type of rectification circuit also exists, using only two diodes. Select sub-menu option Rectifier – Full-Wave Bi-Phase, and also see Photo 10.5. With the screen display press <C> to put the capacitor into circuit.

In the circuit diagram displayed, the peak rectified voltage is less than in the previous circuits and is defined as:

## PANEL 10.1. BRIDGE RECTIFIER STYLES

Bridge rectifiers come in several package shapes, circular, square and oblong (two examples are shown in Photo 10.7). Most have four connections, usually wires, but tags and screw terminals are not uncommon, and printed circuit board mounting types are also available. The circuit diagram symbol for a bridge rectifier is shown in Fig.10.2.

The identification of the wires is marked on the package, usually two squiggles (~) for the a.c. connections, and plus (+) and minus (-) for the d.c. outputs. Rectifiers with more than four terminals are available for specialist applications.

With four-terminal bridge rectifiers, it does not matter which of the two a.c. terminals are connected to the a.c. power source. The correct connection of the rectifier's d.c. pins to the circuit being powered *must always* be observed. They are labelled in the same way as the battery you are using is marked.

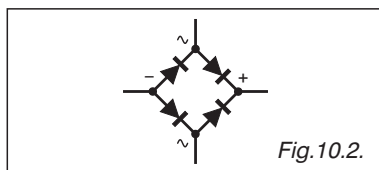


Fig.10.2.

$$\text{Peak } V_{dc} = V_{ac} \times 0.71 - 0.7V$$

The maximum output current is greater, though, at:

$$I_{dc} = I_{ac}$$

In other words, the input and output currents are the same (ignoring normal circuit losses).

As with the bridge rectifier, the rectified ripple frequency is twice that of the input frequency, while capacitor values and ratings are the same.

Once more the screen display allows you to calculate the d.c. output typically available using different a.c. input voltages.

## NEGATIVE AND DUAL-RAIL

Now select Negative and Dual-Rail Supplies from the sub-menu. Four circuits

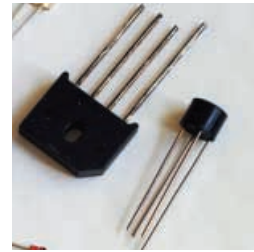


Photo 10.7.

However, an apparent contradiction can exist in circuits which have split power supply lines, i.e. +12V, 0V, -12V. In such a situation, where two bridge rectifiers are being used, one for the positive voltage and one for the negative, it is the *relative* polarity of the voltages output from each rectifier which is important.

Thus, for one of the rectifiers, its positively marked output (+) would feed to the circuit's positive power rail (+12V) and its negatively marked (-) output would feed the circuit's 0V power rail. With the other rectifier, though, the circuit requirement might be for its positive output (+) to be connected to the circuit's 0V rail, while its negatively marked output (-) would feed the circuit's -12V power rail.

are given which illustrate how negative voltages can be generated from an a.c. waveform, see Photo 10.6. Note the diode position changes in the half-wave and bi-phase circuits, compared to the positive voltage equivalents.

The bridge rectifier circuit (dual-rail supply) allows simultaneous generation of both positive and negative supply voltages.

Note that for the bi-phase and dual-rail circuits a transformer having two secondaries is used. These windings are assumed to be identical; a more complex situation exists if they are not identical (beyond the scope of this discussion).

## AUTOTRANSFORMERS

You will not normally require to use a special type of transformer called an *auto-transformer*, but we'll just say what they are for curiosity's sake!

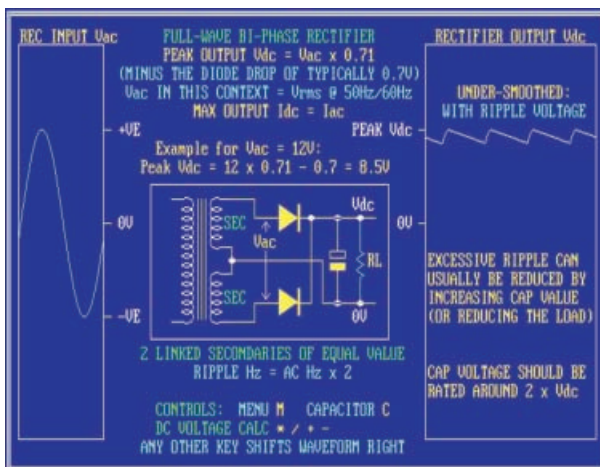


Photo 10.5. Full-wave bi-phase rectifier as illustrated via the interactive screen demo.

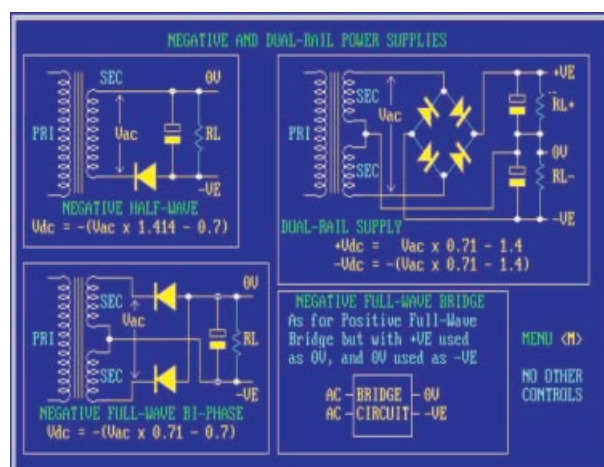


Photo 10.6. Examples of negative and dual-rail power supplies as shown via the demo screen.

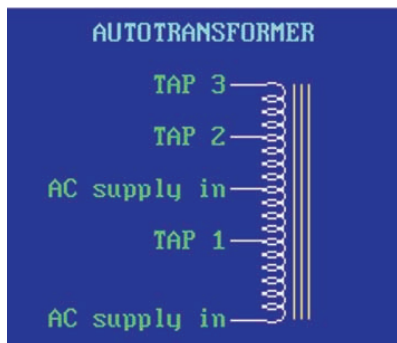


Photo 10.8. An auto transformer example as illustrated in the Miscellany demo.

They are transformers in which the primary and secondary windings are formed from a single coil wound around a magnetic material with access terminals available at various points in the winding. As such, they do not provide isolation from the mains voltages.

## PANEL 10.2. BRIDGE SPECIFICATIONS

In most situations commonly encountered in hobbyist electronics, the two principal specifications which are important for bridge rectifier choice are the current it can pass, and the maximum reverse voltage it is designed to handle.

Many of the constructional power supply circuits that you will encounter are likely to specify a 50V/1A (maximum working specification 50 volts, one amp) device. Although many such circuits operate at much lower voltages and currents, this particular bridge rectifier

specification is a minimum common-or-garden standard that is readily available.

It may often be specified by a type number of W005 (the circular device in Photo 10.7), but in reality any type number having the same two V and A specifications can quite happily be used instead, as long as the pinouts are the same. Bridge rectifiers which can handle voltages well in excess of 1000V and currents of up to 60A are available through some suppliers.

The mains a.c. supply is connected across two designated tappings on the coil. The secondary tappings can be made at selected points so that voltages both greater and smaller than the input voltage can be obtained, although the same rules about current availability still apply. The principle is illustrated at the right of the screen display accessed by

selecting sub-menu option Power Supply Miscellany (see Photo 10.8).

## NEXT MONTH

In Part 11 we examine power supply regulation and discuss capacitor integration and differentiation, showing how RC values change waveforms.

# TEACH-IN 2000 – Experimental 10

**W**ITHOUT asking you to experiment with mains power supplies, we would like you to experiment with waveform rectification on your breadboard, enabling you to see what happens via your meter and computer display screen.

## HALF-WAVE RECTIFICATION

For a start we'll take a look at half-wave rectification, making use of the waveform being generated by the oscillator you've been using since Part 4. Even though the waveform is triangular (nearly), whereas in a mains power supply it will be sinusoidal, the principle can still be displayed.

Assemble the breadboard layout in Fig.10.3, referring to Fig.10.4 for the circuit diagram and component values.

In Fig.10.4, IC3a is an op.amp (see Parts 7 and 8) into whose non-inverting input (pin 3) is fed the triangular waveform from the oscillator (IC1 pin 1). The op.amp's gain is set at about times 3 ( $R4 / R3 + 1$ ). Resistors R1 and R2 set the required mid-way bias voltage of about 3V (half the battery level of nominally 6V). Capacitor C1 adds a bit of smoothing to the bias.

At the output of IC3a (pin 1) accessible via terminal pin TP1, the amplified waveform swings above and below the midway voltage level, probably between about 0.5V and 5.5V (see Part 7), a total swing of about 5V. We can also express this swing as being 2.5V above the midway level (3V) and 2.5V below the midway level.

What we need though, is for the waveform to actually swing 2.5V above and below 0V rather than 3V, i.e. a swing of +2.5V to -2.5V, so more closely approximately the positive and negative aspect of

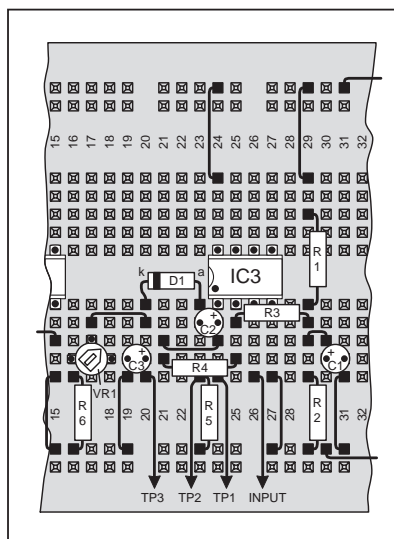


Fig.10.3. Breadboard layout for the half-wave rectifier circuit in Fig.10.4.

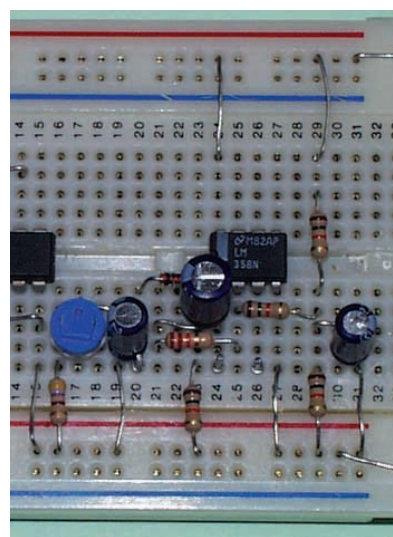


Photo 10.9. Details of the half-wave rectifier breadboard.

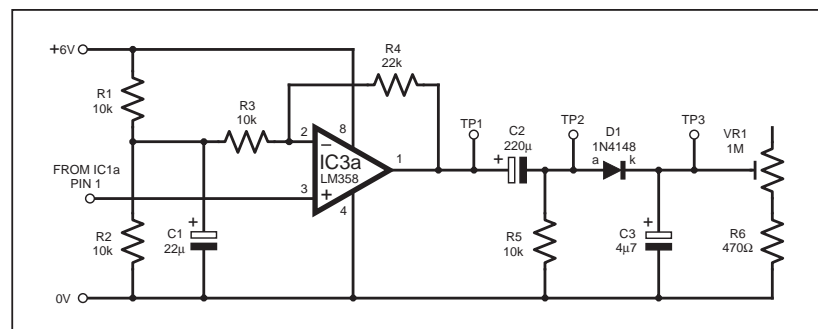


Fig.10.4. Circuit diagram for the half-wave rectifier experiment. Do not monitor test point TP2 via the ADC (IC2) or your computer – see text.



the sinusoidal waveform discussed in the Tutorial.

We have previously discussed how a capacitor can isolate one d.c. voltage from another. We can use this principle to “shift” the 0.5V/5.5V waveform so that it alternates between -2.5V and +2.5V.

In Fig.10.6 this is done by including capacitor C2 and resistor R5, with the latter setting the midway voltage of 0V. At the junction of C2 and R5 (TP2), the waveform swings above and below 0V as required.

DO NOT try to monitor this junction via the ADC device and your computer. The ADC could be damaged by negative voltages being connected to its input. So could the computer if a negative voltage is applied to its parallel printer port.

To monitor the C2/R5 junction, your multimeter should be used, with the oscillator frequency rate set slow enough so that the meter has time to display the negative and positive voltages.

Diode D1 is used as the rectifier, allowing waveform voltages above about 0.7V to pass through to charge capacitor C3. The capacitor is discharged at a rate dependent on the capacitance value, and the resistance of the path to the 0V line via preset VR1 and R6, in other words, on the CR value (see Part 2).

The voltage across capacitor C3 can be monitored at terminal pin TP3 via the ADC and computer, and by your meter. Remember that the meter has its own resistance value which may reduce the CR value when VR1 is preset at higher resistance values.

Resistor R6 prevents the output of diode D1 being connected directly to 0V.

## VARIABLE FREQUENCY

Experiment with observing the rectified and smoothed waveform at TP2 for different values of C3, settings of VR1 and at different oscillator frequencies (initially start off with a value of 22 $\mu$ F for oscillator capacitor C1).

Also monitor the complete waveform output from IC3a at TP1, and confirm that half-wave rectification is taking place as described in the Tutorial, with the ripple frequency being the same as the oscillator frequency. Photo 10.10 shows a composite

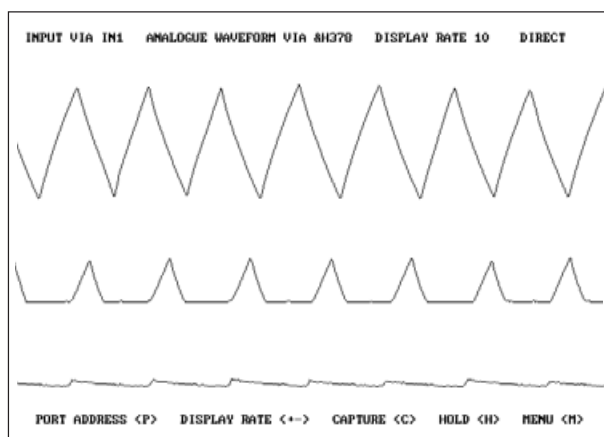


Photo 10.10. Composite screen dump created using the circuit in Fig.10.4 showing the unrectified waveform (top), the output from diode D1 (TP3) with capacitor C3 removed (middle) and with C3 in circuit showing a small amount of ripple voltage. The waveforms are not mutually aligned.

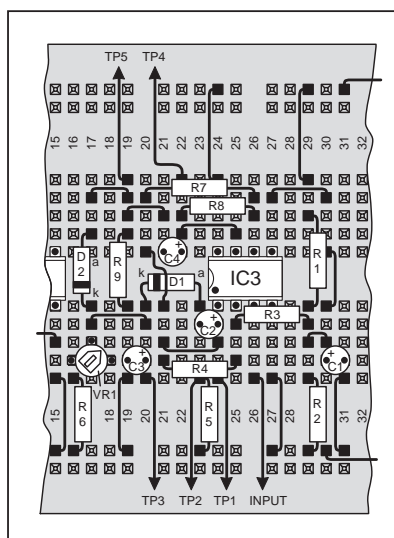


Fig.10.5. Breadboard layout for the bi-phase full-wave rectifier circuit in Fig.10.6.

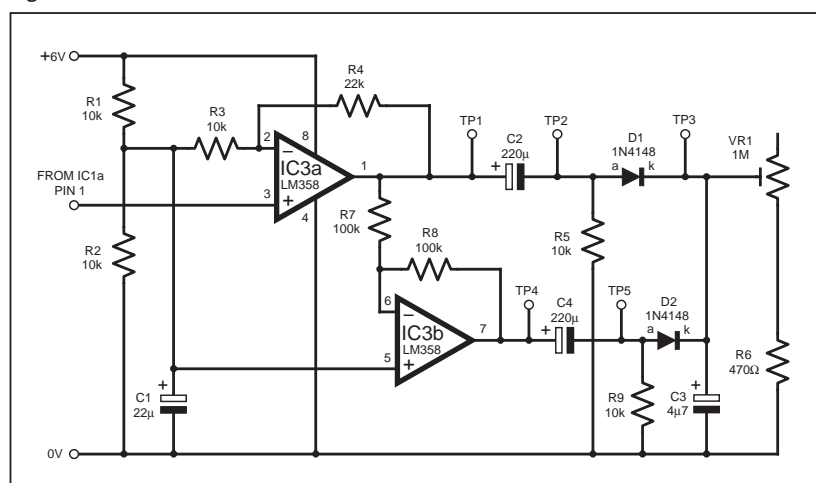


Fig.10.6. Circuit diagram for the bi-phase full-wave experiment. Do not monitor TP2 or TP5 via the ADC or computer (see text).

of three screen dumps created using the author's breadboard setup.

Recalling that the total resistance between C3 and 0V, via VR1 and R6, is the “load” discussed in the Tutorial, try to verify that the best waveform smoothing occurs when capacitor C3 is equal to or greater than the formula we quoted ( $C = 4700\mu F \times I_{dc}$ ) – use other values for C3 if you wish.

To find out  $I_{dc}$  (the peak current), measure the peak voltage at TP3 and divide this by the value of VR1 plus R6 (Ohm's Law:  $I = V / R$ ).

Also remember that the value of C3, as stated on its body, is subject to a wide tolerance factor (see Part 2).

A further experiment is to see what

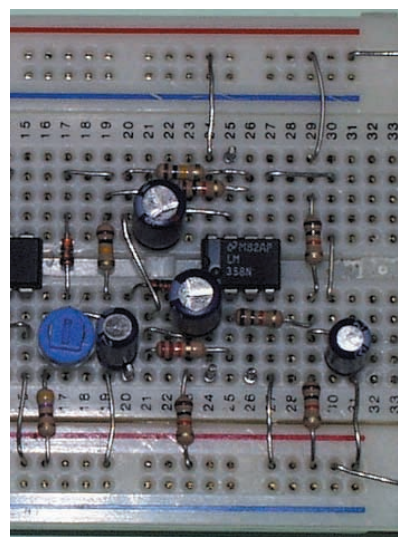


Photo 10.11. Details of the breadboard layout in Fig.10.5.

happens if the d.c. decoupling capacitor C2 is removed and replaced by a wire link. How does this affect the peak voltage across C3 and the appearance of the ripple voltage on your computer display?

## BI-PHASE FULL-WAVE RECTIFICATION

It is somewhat too complicated to set up your breadboard to demonstrate full-wave bridge rectification, but we can demonstrate the principle of bi-phase full-wave rectification, using the breadboard layout and circuit shown in Fig.10.5 and Fig.10.6.

When assembling the extra components onto your breadboard take especial care to use the correct positions as some components are very close together. Note that there is a link wire that crosses above diode D1 (without touching it).

The amplified waveform from IC3a is a.c. coupled by C2/R5, rectified through D1 and smoothed by capacitor C3 as it was for the half-wave rectifier, only the positive phase of the waveform being used.

We also invert the output from IC3a by taking it through the unity-gain op.amp IC3b. This is then a.c. coupled and rectified identically, using capacitor C4, resistor R9



and diode D2, and also fed into smoothing capacitor C3.

If you compare the waveform at TP3 with that at TP1 or TP4, you will see that the ripple frequency is double that from the oscillator. Experiment as before with different values for C3, the VR1/R6 resistance and with different frequencies.

Test the full-wave formula for smoothing capacitor C3, which effectively says that C3's value can now be about half that required for the half-wave circuit.

You will have spotted, we assume, that we are not actually using both sides of the waveform as it originated from the oscillator. We have had to use op.amp IC3b as an intermediate inverter because we do not have a negative power supply available, which could allow a waveform to be generated that would evenly swing above and below 0V.

DO NOT attempt ingenuity and try to improvise a negative supply for the breadboard (or use that described in the next section) – you could kill integrated circuits and your computer.

As before, see what happens when the a.c. coupling capacitors (C2 and C4) are replaced by link wires.

## NEGATIVE VOLTAGE GENERATION

We shall now show you how to turn a positive voltage into a negative one. It's a matter of relativity (where have we heard that one before?).

Referring back to Fig.10.4, the waveform entering capacitor C2 is alternating above and below a midway voltage of about 3V, with a peak to peak magnitude of 5V. The waveform exiting C2, however, has the same magnitude, but is now alternating above and below 0V, between -2.5V and +2.5V, entirely due to the 0V reference voltage to which resistor R5 is connected.

If resistor R5 were to be connected to another voltage level, say 10V, the output waveform would then swing above and below this level instead, between +7.5V and +12.5V in this case, still with the same relative magnitude of 5V. In other words, the output midway voltage level is the same as the voltage to which the resistor is connected (biased).

We can take this level shifting characteristic a stage further by following the capacitor with a diode (D3) instead of resistor R5, see Fig.10.9.

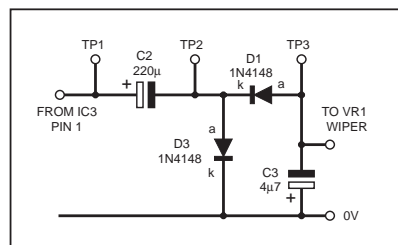


Fig.10.7. Amendment for a negative voltage half-wave rectifier.

Assuming the same 5V magnitude, when the voltage input to capacitor C2 rises, the output voltage tries to rise as well, by the same relative amount, as it did with the resistor. Now, though, the diode drains the positive voltage away through its

## PANEL 10.3. POWERFULLY LETHAL

The vast majority of you will know that a.c. mains electricity can be lethal. We wonder, though, if you realise how even a *small* a.c. mains electrical shock can kill you?

The severity of the shock will depend on a number of factors, including the magnitude of the current, whether it is alternating (a.c. – as in the vast majority of domestic mains supplies around the world) or direct (d.c.), and the route that the power takes through the body. The general health of the victim can also affect his or her reaction to electrical shock.

The magnitude of the current depends on the voltage which is applied and the resistance of the body (yes, even humans have electrical resistance). The electrical energy developed in the body depends on the time for which the current flows.

Table 10.1 illustrates just how even small mains power currents can be highly dangerous. It is reproduced from our sister publication, the *Electronic Service Manual (ESM)* and is in relation to the UK mains a.c. supply that comes into the home (nominally 230V a.c. at 50Hz).

The figures in Table 10.1 are quoted as a guide only. Note that there have been cases of lethal shocks resulting from contact with much lower voltages and at quite small values of current.

*In general, any voltage in excess of 50V should be considered dangerous.*

Remember that 1mA (one milliamp) is only one-thousandth of an amp. Thirteen thousand milliamps can flow through the standard UK domestic power plug when a 13 amp fuse is fitted. Even with a 3 amp fuse fitted, 3000 milliamps can flow before the fuse breaks the current flow.

Never tamper with mains electrical power unless you are qualified to do so. Seek help from those who are suitably qualified if a mains electrical connection needs to be made to non-standard electrical equipment (i.e. equipment that is not already correctly and fully wired by an approved equipment manufacturer).

Never run an electronic circuit at a voltage greater than specified. Never attempt to run a d.c. electronic circuit from an a.c. supply. There are specially designed power supply units that transform a.c. mains electrical power at high voltage to safe d.c. supplies at a low voltage.

We could go on for pages on this matter if we had the space. For more information contact your local medical practitioner (e.g. doctor) or electrical retailer.

Table 10.1. EFFECTS OF ELECTRICAL SHOCK

Current	Physiological effect
Less than 1mA	Not usually noticeable
1mA to 2mA	Threshold of perception (a slight tingle may be felt)
2mA to 4mA	Mild shock (effects of current flow are felt)
4mA to 10mA	Serious shock (shock is felt as pain)
10mA to 20mA	Motor nerve paralysis may occur (unable to let go)
20mA to 50mA	Respiratory control inhibited (breathing may stop)
More than 50mA	Ventricular fibrillation of heart muscle (heart failure)

Referenced to the UK mains a.c. supply (nominally 230V a.c. at 50Hz).

anode-cathode path. In this instance, the cathode is connected to the 0V line and so the output voltage can never rise above about 0.7V, the "diode drop" voltage we have referred to previously.

When the voltage input to C2 has reached its peak (5V above its starting value) and begins to fall, again the output voltage tries to change by the same relative magnitude. However, the fall in output voltage now commences at 0.7V, but the same 5V magnitude of change still occurs. The output swing thus becomes +0.7V to -4.3V. So already we have a negative aspect to the waveform.

## SIMPLY INVERTED

We now need to rectify the waveform so that only the negative voltage is retained as a d.c. level, the opposite of achieving a positive rectified d.c. voltage. The way in which this is done is simply to turn the rectifying diode round the other way, which as you will see has been done with D1 in Fig.10.7. Smoothing capacitor C3 also needs to be "turned upside-down" (positive lead on the 0V line).

With this configuration, the voltage stored across C3 is the minimum waveform voltage (-4.3V) less the 0.7V drop across D1, resulting in a stored voltage of -3.6V.

Obviously the conversion is not very efficient – about 1.4 volts have been "lost" because of the two diodes. The current which can be drawn from C3 is also very limited. Trying to draw too much current will increase the negative ripple voltage.

The current available can be increased, however, by increasing the value of C2. The ripple voltage can be decreased by increasing C3. The situation becomes a bit complicated since the values of both C2 and C3 also depend on the frequency and shape of the waveform entering C2.

In Part 11 next month we shall describe how capacitor values affect the way in which waveform shape (and hence power) can be retained or minimised.

In the meantime, modify the breadboard layout of Fig.10.5 to replace R5 by diode D1, and change the polarity connections of C3. Only monitor this circuit using your meter – DO NOT connect it to the ADC device or your computer.

Remove diode D2 from the second part of the bi-phase circuit. There is no need to remove its other components.

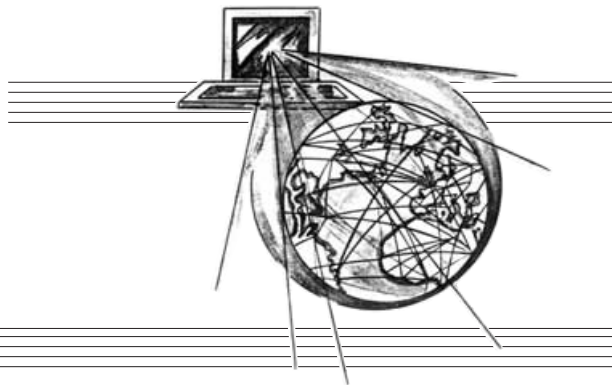
As a further experiment, modify this second half of the circuit to provide full-wave negative voltage rectification.

On that negative note, we positively look forward to you joining us again next month.

# SURFING THE INTERNET

# NET WORK

## ALAN WINSTANLEY



### BT SURFTIME

ON June 1 British Telecom (BT) duly sent an E-mail to those interested in signing up for BT's new Surftime packages. These tariffs are BT's answer to the demands for unmetered Internet access, and they offer flat rate access in return for a fixed monthly fee. As always, things are not quite what they seem and BT seems determined to maintain its stranglehold on the UK Internet access. Many users simply want to pay a flat-rate figure to enjoy unmetered access through their preferred ISP, but there is a snag with BT Surftime: you have to use a Surftime ISP.

"Surf the net without worrying about the cost" says the BT E-mail, which offers *Surftime Evening And Weekend* for just £5.99 (\$9.88) a month for unlimited Internet calls every evening and all weekend. The *Surftime Anytime* costs £19.99 (\$32.98) a month for unlimited 24 x 7 Internet calls. There are several "gotchas" involved, starting with the fact that apart from the Surftime tariffs, there may also be subscription costs charged by the ISP. There may be an option to have this bundled into your BT phone bill.

"You can choose an ISP from a number of participating companies which may include your current ISP" says BT. Checking the BT web site at [www2.btwebworld.com/netgeneration/surft/html/isps.html](http://www2.btwebworld.com/netgeneration/surft/html/isps.html) shows a list of over 40 ISPs, none of whom I've heard of apart from Freeserve and BT Internet.

Nearly all the remaining ISPs had "AIH" after their name – or Affinity Internet Holdings, which is a front end for Virtual Internet Providers Ltd. ([www.vip.uk.com](http://www.vip.uk.com)). They supply a number of branded Virtual Internet Service Providers (VISPs). VIP Ltd. is the "wholesaler" behind branded ISPs including Egg, PowerGen and Tiny Computers.

The BT list of 40-odd mostly obscure Surftime ISPs currently boils down to BT Click for Business, BT Internet, Freeserve, PlusNet and about 20 Affinity suppliers. This may hopefully have improved by the time you read this, and bear in mind that many ISPs will still charge a monthly subscription. For example the "tenner-a-month" ISP Demon Internet hasn't bought into BT's Surftime package yet but are expected to launch tariffs "soon"; until then you (and I) will be stuck with the same old metered tariffs.

Affinity end-users can buy all-inclusive (phone and subscription) Surftime 24 x 7 access for £25.99 (\$42.88) a month. Services offered by BT Internet (BT's ISP arm) aren't straightforward either, with confusion reigning between the Pay As You Go and Surftime tariffs, if complaints in the newsgroups are anything to go by. NTL ([www.askntl.com](http://www.askntl.com)) is a cable operator which has already launched free Internet access but they cannot cope with the demand for signing-up. Customers complain of serious delays in shipping the CD ROM, though NTL blames the delays on a postcode-based shipping system and an overwhelming demand for its disk.

### OTHER OPTIONS

It is worth reminding readers of other Internet access options, some of which may have been launched by the time you read this. Alta Vista UK is running late with its 0800 access (announced back in March), and others will launch through the summer.

Alternatives include ISDN2e (Home Highway etc.), which provides a speed increase (64Kbps or 128Kbps compared with typically 46Kbps for a V.90 modem). ISDN uses a TA (terminal adaptor) instead of a modem, and provides two analogue or two 64K ISDN lines (or one of each) at any one time. As an added benefit it usually accesses the Internet in a second or two instead of the minute taken to dial in with a modem. Make sure your preferred ISP provides ISDN access numbers, and note that many will not permit channel bonding – making two parallel 64K connections using the same log-in.

More demanding users should keep an eye on ADSL (Asymmetric Digital Subscriber Line), specifically BT Openworld ([www.btopenworld.com](http://www.btopenworld.com)) which uses a dedicated USB Openworld modem to provide "up to 512K" for £39.99 a month, always-on. It is early days yet for ADSL and some users may be better signing for a year's unmetered access now, then re-examine ADSL later: how well it works will depend on how far away you are from your digital exchange.

The UK Internet marketplace is a masterpiece of confusion marketing, and my advice would be to wait and see what develops before signing up. Also, check the small print very carefully before signing to any long term deal, and try to obtain a personal recommendation from someone in your locality who uses the same ISP, to see what access speeds and reliability are like.

At this point I would like to invite overseas readers, especially those in the USA and Canada, to E-mail me with details of the kind of tariffs they pay for their own Internet access, and I will publish a comparison in the future, perhaps on the web. Please E-mail a summary to [alan@epemag.demon.co.uk](mailto:alan@epemag.demon.co.uk). Every message will receive a reply. The more data I receive, the better!

### Unix Permissions

At the time of writing, the domain name of **unix.com** is up for auction at **Afternic.com** auctions, and bidding starts at a mere \$750,000. Whenever you access an FTP site using a web browser or an FTP client (such as the excellent WS-FTP Pro software by Ipswitch) you will undoubtedly come across the Unix operating system, even if you use Windows or Linux on your local system. The chances are you will see a directory structure containing a series of letters, but do you know what these mean? I'll round off this month's column with some general Unix pointers.

Unix file structures differ from DOS in several ways. Unix files are allocated to an "owner" who can change its user rights. The permissions for each Unix file or directory are described by a series of letters, perhaps looking like this on-screen:

```
- rwxrwx--- 1 guest guests 4752 Jul 2 11:50 mywebpage.html
```

The first character is either "d" for directory or it will be a dash (implying it's an ordinary file, as in this example). The nine letters that follow identify the permissions for the three possible groups of user. These are the owner, the group and "others" (everyone else).

You can set the permissions of files so that only its owner can execute (x) a file and others can only read (r) or write (w). A file with permissions of **-rwxrwx---** can be written, read and executed by its owner and others in the same group, but no-one else has any rights to read, write or execute (run) it.

Sometimes permissions are described using a number code instead, such as 755 or 555. By using the Unix CHMOD (change mode) command, permissions can be set by the owner using a simple code for the owner, group and other users. Simply remember that a Read is 4, a Write is 2 and eXecute is 1.

7	rwx	(= 4 + 2 + 1)	3	-wx
6	rw-	(= 4 + 2)	2	-w-
5	rx-	(= 4 + 1 etc.)	1	---x
4	r--			

A program such as WS-FTP will allow user codes to be set by right-clicking and selecting CHMOD. Other programs talk about "CHMOD 555" (r-x r-x r-x) and so on. Now when you see those strange letters before Unix file names, you'll know what they mean. See you next month! My home page is at:

<http://homepages.tcp.co.uk/~alanwin> and my E-mail is [alan@epemag.demon.co.uk](mailto:alan@epemag.demon.co.uk).

# PIC Real Time In-Circuit Emulator

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# Starter Project

# QUIZ GAME

# INDICATOR

MAX HORSEY AND TOM WEBB

*A low-cost fun project that cannot be questioned!*

**D**ESIGNED to take the pressure off the chairperson when deciding who pressed their button "first", this latest addition to our Starter Project collection is ideal for the newcomer to electronics and should provide hours of fun.

The Quiz Game Indicator shows which of two contestants presses their button first by blocking the slowest one. The circuit is based around a single i.c. and operates a buzzer and red and green l.e.d.s. The two colours being assigned to the participating teams for identification.

## BLOCK DIAGRAM

The circuit is based around two bistables or latching circuits, see block diagram Fig.1. The non-inverted output from each bistable feeds a light-emitting diode (l.e.d.) and buzzer, while an inverted output from each bistable is used to power the controlling pushswitch for the opposing bistable.

Hence, if one button is pressed before the other, the first bistable to latch will disable the opposing pushswitch.

## CIRCUIT DESCRIPTION

The complete circuit diagram for the Quiz Game Indicator, including

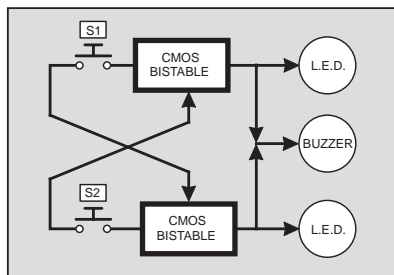
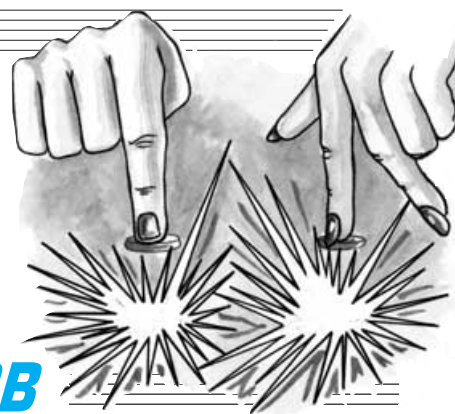


Fig.1. Quiz Game Indicator block diagram.

"contestant" switches and l.e.d.s, is shown in Fig.2. The circuit requires four NOT gates and one of the least expensive methods of achieving this is to use a single i.c. containing four NOR gates connected as shown in Fig. 2. Their inputs are connected together making the NOR gates into the required NOT gates or inverters.

The gates are arranged in pairs with positive feedback, so for example, gates IC1a and IC1b are connected together by joining output pin 3 to input pins 5 and 6. Positive feedback is provided by resistor R1.



If output pin 4 of IC1b is low (0V) this will hold pins 1 and 2 of IC1a low. So IC1a output, pin 3, will be high (positive), making IC1b pins 5 and 6 also high. The two gates will therefore remain in this state.

When power is first applied, the gates may start up in either state, and so it is possible that they will latch with IC1b pin 4 high. This is prevented by means of capacitor C1, which holds pin 1 and pin 2 of IC1a low for an instant at the moment of "switch on".

The same arrangement is used for gates IC1c and IC1d. Hence, at power-up pin 11 will be low.

Power for the pushswitch S2 is obtained from pin 3 of IC1a. At present pin 3 is high, and so S2 is operable. If S2 is pressed, the input to IC1c (pins 8 and 9) will go high causing the output pin 10 to go low, and in turn, causing IC1d output pin 11 to go high. With pin 10 low, switch S1 has no power supply and is therefore inoperable. Hence, if S2 is pressed first, pin 11 will go high and switch S1 is prevented from operating.

The same would have applied if switch S1 had been pressed first, except that pin 3 would have gone low, and S2 would have been inoperable. Note that resistors R1 and R2 are used to latch the respective pairs of gates by applying some positive feedback.

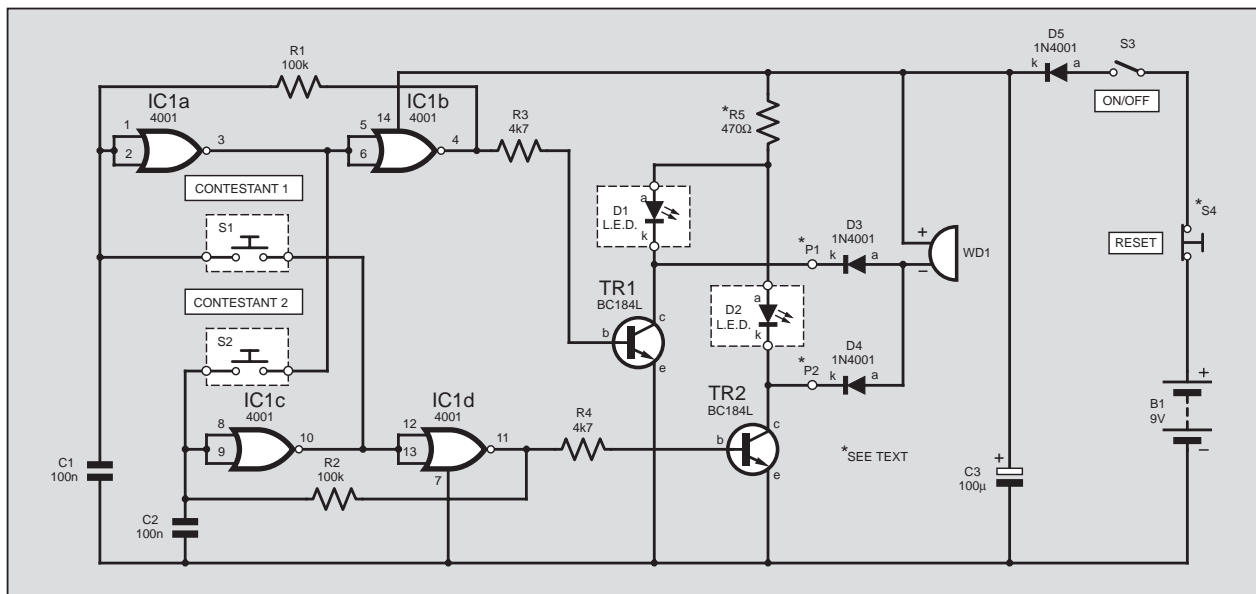


Fig.2. Complete circuit diagram for the Quiz Game Indicator. The l.e.d.s and switches S1 and S2 are housed in separate boxes.

## OUTPUT DRIVERS

The output from pin 11 of IC1d is applied, via current limiting resistor R4, to the base (b) of transistor TR2, which then turns on l.e.d. D2. Similarly, output pin 4 of IC1b is connected to TR1 via resistor R3.

The buzzer, WD1, operates when either transistor is turned on, with diodes D3 and D4 used to prevent both the l.e.d.s turning on at the same moment. The single buzzer shown in Fig 2 will sound when either button is pressed.

However, provision has been made for two buzzers with different tones, in which case each of the buzzers is connected between the positive buzzer terminal (+V) and the points labelled P1 and P2 respectively. This will be explained fully later.

Power is provided by a 9V battery or a 9V to 12V mains power supply, and capacitor C3 is used for general decoupling. The circuit can be reset by switching off the power supply switch S3. In practice it may be helpful to wire a push-to-break switch (S4) in series with S3 to enable easy resetting. Diode D5 is included in the circuit to protect against wrong supply polarity connections.

## CONSTRUCTION

To ease construction and to cut down on the chances of any wiring errors, the main circuit of the Quiz Game Indicator is built on a small printed circuit board (p.c.b.).

Only the contestant switches and two l.e.d.s are not mounted on the p.c.b.

The printed circuit board topside component layout and the full size underside copper foil master are shown in Fig.3. The board is available from the *EPE PCB Service*, code 272.

Begin construction of the p.c.b. by inserting a 14-pin d.i.l. socket for IC1, followed by the smallest components such as resistors and diodes. Ensure that the diodes are inserted the correct way round; the band on the diode indicates the cathode (k) end.

The two transistors should also be inserted with care, noting that a BC184L is specified for TR1 and TR2 (a BC184, without the "L" has its leads in a different order). In practice, virtually any small *npn* transistor can be used but take care to insert the leads in the correct order.

The small capacitors C1 and C2 can be fitted either way round, but C3 must be fitted with its positive side as shown in Fig.3. Positive is generally indicated by the longer lead.

## SATELLITE LINK

The l.e.d.s and push-to-make switches in the prototype have been fitted into separate satellite or "contestant" cases as shown in Fig.4 and the photographs. Four-core cable is employed to link the master unit with the satellites, and the master (on/off) switch has a Reset push-to-break pushbutton switch (S4) wired in series with it to enable easy resetting of the circuit, ready for the "next question and answer".

When connecting the l.e.d.s observe the correct polarity – the shorter lead normally indicates the cathode (k) and a "flat" usually appears on the body next to this lead. The p.c.b. contains connecting points for

## COMPONENTS

### Resistors

R1, R2	100k (2 off)
R3, R4	4k7 (2 off)
R5	470Ω (see text)

All 0.25W 5% carbon film

### Capacitors

C1, C2	100n ceramic (2 off)
C3	100μ radial elec. 25V

### Semiconductors

D1	5mm red l.e.d., plus optional one (see text)
D2	5mm green l.e.d., plus optional one (see text)
D3, D4, D5	1N4001 50V 1A rect. diode (3 off)
TR1, TR2	BC184L <i>npn</i> gen. purpose transistor (2 off)
IC1	4001B CMOS quad 2-input NOR gate

### Miscellaneous

S1, S2	pushbutton switch, push-to-make (2 off)
S3	min. single-pole toggle switch
S4	pushbutton switch, push-to-break (optional – see text)
WD1	3V to 20V piezo electric buzzer, p.c.b. mounting (see text)
B1	9V battery (PP3), with connector clip/leads

Printed circuit board available from the *EPE* Online store, code 272; plastic case, size 100mm x 76mm x 40mm; handheld plastic, snap together case, size 80mm x 55mm x 25mm (2 off); 14-pin d.i.l. socket; 4-core cable, length to choice; multistrand connecting wire; self-adhesive stand-off pillar, for p.c.b. mounting (4 off); solder pins; solder, etc.

Approx. Cost **\$22.40**  
Guidance Only  
excluding cases & batts.

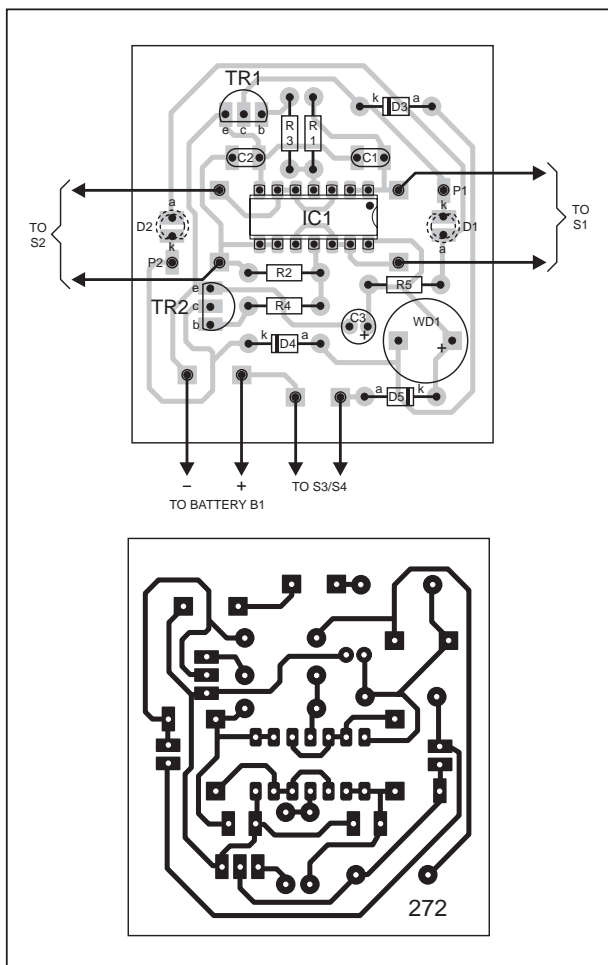


Fig.3. Printed circuit board component layout and full size copper foil master.

a single buzzer. If two buzzers are required, the positive side of each should be connected as shown in Fig.5, and the negative side of each buzzer connected to pins P1 and P2 respectively. If buzzers with different tones are obtained, the tone will indicate who pressed the button first.

When the circuit board has been completed IC1 should be inserted into its socket. Take care when handling any CMOS i.c. since it is static sensitive and may be damaged if handled without first earthing yourself by touching an earthed metal object. Note that pin 1 of the i.c. is indicated by a dot or notch; check that the i.c. is fitted the correct way round, see Fig.3.

## CASING-UP

The prototype Quiz Game Indicator is housed in three enclosures, a main case and two satellites as shown in the photographs. Begin preparation of the cases by drilling all the holes required for the l.e.d.s and interconnecting leads.

The satellite cases house the pushbutton switches S1 and S2 and can also house the l.e.d.s if desired, in which case four-core cable is used to link the main box with the satellites. If l.e.d.s are also required in the main box, they can be wired in parallel

with l.e.d.s D1 and D2. In this case the value of resistor R5 should be reduced to say 220 ohms.

## TESTING

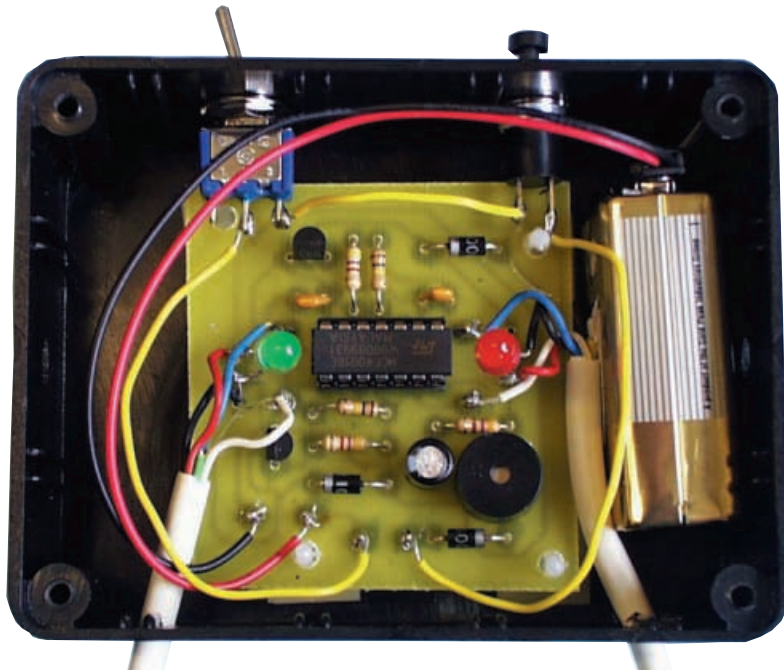
When power is first applied and on/off switch S3 closed, the circuit should start up in its reset state. Try pressing one of the contestant pushswitches. The appropriate l.e.d. and buzzer should activate. It should not be possible to activate the other l.e.d. once the first is lit.

Good test points are the output pins of each gate. Begin by connecting the negative lead of a voltmeter to 0V in the circuit, and use the positive lead as a probe.

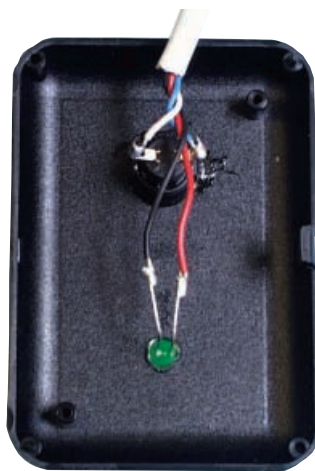
Each output should be close to 0V or close to the positive rail depending upon its state. Pin 3 should always be at the opposite state to pin 4, and likewise pins 10 and 11 should be in opposite states.

If pin 4 is positive, enough current should flow through resistor R3 to activate transistor TR1. The same applies to pin 11, R4 and TR2. If the base of either transistor is above 0.6V then it should turn on.

This project should finally settle any arguments about "who pressed first" since the chances of the buttons being pressed at exactly the same moment, and hence lighting both l.e.d.s, are very remote. Although, human nature being what it is, it's no guarantee! □



Layout of components inside the prototype master unit.



Wiring to the handset pushswitch and l.e.d.

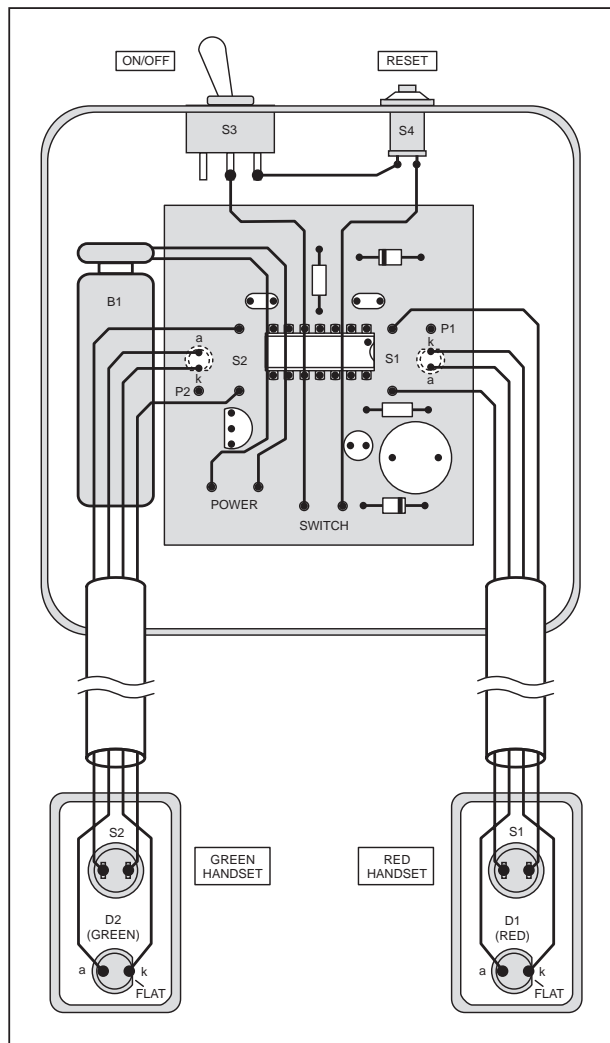


Fig.4. Interwiring between p.c.b. and the two "team" boxes. The main unit l.e.d.s are connected in parallel with l.e.d.s D1 and D2.

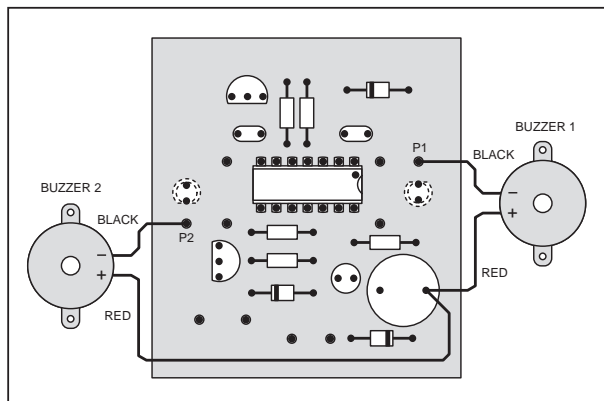


Fig.5. Wiring arrangement for the inclusion of two buzzers.



# Constructional Project

# EPE

# MOODLOOP

ANDY FLIND



*Relax in your own personal "space".  
A NEW PIC design generating  
selectable Alpha, Beta, Delta and  
Theta fields.*

**P**UBLISHED in July '99, the *EPE Mood PICKer* and its predecessor the *EPE Mood Changer* (June '98) both proved to be extremely popular with constructors.

These devices generated weak magnetic fields at "brainwave" frequencies, which are thought to encourage mental states of relaxation, creative mental imaging and even sleep. In fact, much of the feedback received from constructors concerned the ability to induce sleep since it has frequently proved very helpful in cases of insomnia.

## BRAINWAVES

To begin with some theory for readers not acquainted with this field, the human brain exhibits electrical activity in the form of tiny alternating currents. Using extremely sensitive equipment it is possible to monitor these currents from voltages present at the skin surface of the head and it has been established that different frequencies correspond to some extent with the subject's mood or mental state.

Of the frequencies established to date, the most important from our point of view fall into four broad categories which have been named by researchers. The lowest band is called **Delta** and covers the range from 0.5Hz to about 4Hz, and is found during deep sleep and in very young babies. The second is **Theta**, which spans 5Hz to 7Hz and is associated with creative mental imagery or mental picturing. Researchers have shown much interest in this area in recent years.

## WARNING NOTICE

It is known that photic stimulation at Alpha frequencies can cause seizures in persons suffering from Epilepsy. We would therefore also suggest that it is not wise for such people to try this project.

A user who is not a known epileptic, but when using the *EPE Moodloop* begins to experience an odd smell, sound or other unexplained effects, should TURN IT OFF IMMEDIATELY and seek professional medical advice.

The next frequency band on the scale runs from 8Hz to about 12Hz and is known as **Alpha**. This is the range that first came to the attention of people outside the medical profession when it was observed in Zen practitioners during a session of deep meditation.

This led to the notion that learning to generate high levels of Alpha activity might allow access to these deep meditative states without the years of rigorous training normally required. Needless to say this proved less than strictly true but many experimenters would agree that it is at least a step in the right direction and meditators sometimes refer to the "Alpha" state, which usually implies deep relaxation.

The highest brainwave frequencies commonly found are between 18Hz and 30Hz, and are called **Beta** waves. They appear during the normal alert, wakeful state. Other brainwave frequencies exist but are not as well defined and are rarely encountered outside medical EEG research.

## FORCE FIELD

Various ways of encouraging the brain to generate specific electrical frequencies exist, one of which is exposure to a suitable alternating magnetic field. Opinions on how this works vary but one likely method seems quite simple. An alternating magnetic field induces electrical currents in conductive material within range and brain tissue is such a conductor.

It seems likely that the production of weak currents of suitable frequency within the brain will either tend to produce the desired mental state directly, or it may do so by encouraging the brain to "synchronise"

to the frequency. Either way, the effect is one many people find worthwhile as shown by the interest in the two projects published so far in *EPE*.

Both of these produce tiny localised magnetic fields. This new project represents an attempt to increase the effect by delivering a much larger current into an inductive loop system which may be placed right around a small room (or around a bed in the case of insomnia!) to permeate a whole area with the desired field.

Roughly speaking, it can saturate an

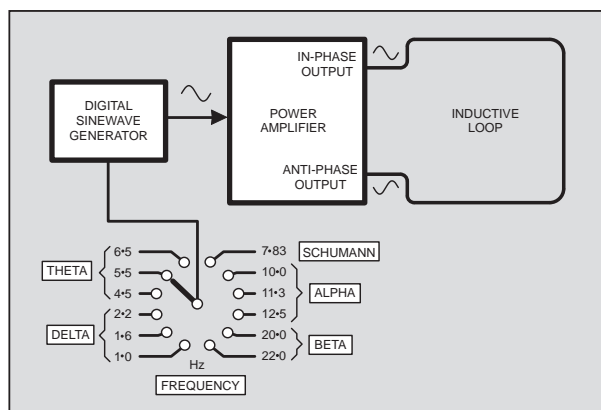


Fig.1. Block diagram for the EPE Moodloop.

area of up to four metres square with a field of intensity equal to that of one of the previous designs at a range of about three centimetres. This should be sufficient for the most ardent enthusiast of the system.

## HOW IT WORKS

The circuit consists of a low-frequency sinewave generator followed by a power amplifier designed for optimum performance at frequencies right down to d.c., see Fig.1. Low-frequency sinewaves are most easily produced using digital synthesis techniques, for which the PIC16F84 microcontroller is well suited.

A bunch of resistors with suitable values are connected to the eight outputs of Port B of the PIC, which are turned on and off in sequence at suitable intervals to give the desired frequency. The resulting output waveform is "stepped", but adequately sinusoidal when viewed on an oscilloscope, certainly sufficiently so for this project.

Each cycle takes a total of sixteen steps and the outline of the program flow is shown in Fig.2. It operates as follows.

Switches connected to the lowest four bits of Port A are used to select the desired output frequency. During initialisation Port A is configured as all input (only the lower five bits are available anyway), Port B is set to all output and both are cleared.

Next, the state of port A is copied into a register called "PTR" (for "pointer" as it is used to select the timing delay for each step). Then the main output program commences its run.

It begins by checking the current state of the four bits of Port A against the value held in "PTR". If they differ the program returns to the start where the new value is read into the register. Otherwise the first bit of Port B is set high and the appropriate delay selected by means of a "tabled go-to" and executed.

This process is repeated a further sixteen times until all eight bits of Port B are high and the output is at the maximum value. The next eight steps then sequentially set them all low again and this process is repeated continuously to generate a steady sinewave output at the selected frequency.

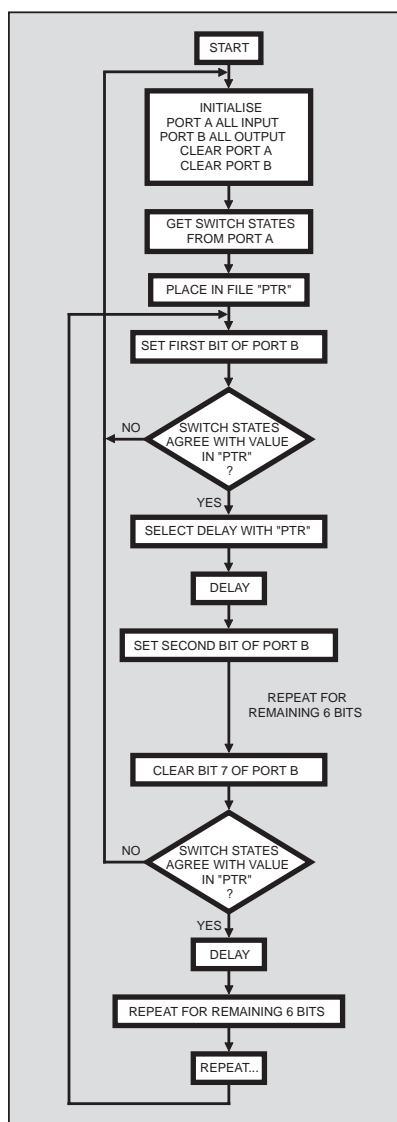


Fig.2. EPE Moodloop software flow diagram.

Points to note by anyone examining the software are firstly that the input states are read using the command "COMF" instead of the more usual "MOVF" since they are "active low", as this command inverts them so they arrive the right way up. All the delays are composed of two nested loops which take a fixed number of clock cycles to execute and hence occupy a finite time.

As the whole program is time-dependent, it is liberally sprinkled with "NOP"s to improve accuracy. The calculated output frequencies are all within a tiny fraction of a percent of the intended ones and some are theoretically spot-on.

### TIME CHANGE

The software differs in a number of ways from that of the EPE Mood PICker.

The timings are different since it uses a 4MHz crystal in place of the Mood PICker's 32kHz watch crystal, and the output frequencies have been changed slightly. Using a 4MHz crystal also means that the PIC is operated in XT mode instead of LP.

Different resistor values are used to generate the sinewave which no longer uses two steps at the top and bottom of each cycle, so these now execute in sixteen steps each instead of eighteen.

The outputs are turned on in sequence from 0 to 7, then turned off again from 7 to 0, rather than 0 to 7 as in the previous design. This makes little practical difference of course, but does provide a change for the programmer!

Details on obtaining the software are given in Shoptalk.

## COMPONENTS

Approx. Cost  
Guidance Only **\$54.40**  
excl. case & power supply

### Resistors

R1, R2, R3, R17, R18 1k (5 off)  
R4 to R7, R19 to R24, R27, R28 10k (12 off)  
R8 4k7  
R9, R16 120k (2 off)  
R10, R15 47k (2 off)  
R11, R14 33k (2 off)  
R12, R13 27k (2 off)  
R25, R30 22k (2 off)  
R26, R31 68Ω (2 off)  
R29, R32 10Ω (2 off)  
All 0.6W 1% metal film type

### Potentiometer

VR1 10k 22-turn cermet preset, vertical

### Capacitors

C1 to C4, C8, C11, C14 to C16 100n resin-dipped ceramic (9 off)  
C5 10μ radial elect. 50V  
C6, C7 22p ceramic plate (2 off)  
C9, C10 10n resin-dipped ceramic (2 off)  
C12, C13 1n resin-dipped ceramic (2 off)  
C17 4700μ radial elect. 16V

See  
**SHOP**  
**TALK**  
page

### Semiconductors

D1 5V6 400mW Zener  
D2 to D17 1N4148 signal diode (16 off)  
TR1, TR3 BD135 npn transistor (2 off)  
TR2, TR4 BD136 pnp transistor (2 off)  
IC1, IC4 OP279 dual op.amp (2 off)  
IC2 PIC16F84 pre-programmed microcontroller  
IC3 TL061 single op.amp  
X1 4MHz crystal

### Miscellaneous

S1 to S4 4-way d.i.l. switch  
S5 12-way single-pole rotary switch (see text)  
SK1, SK2 15-way D-type socket, chassis mounting (2 off)  
PL1/PL2 15-way IDC plug (2 off)  
SK3, SK4 4mm socket (2 off)  
SK5 d.c. power socket

Printed circuit board available from the EPE Online store, code 271; plastic case, size 180mm x 120mm x 65mm (see text); 8-pin d.i.l. socket (4 off); 18-pin d.i.l. socket; TO126 twisted-vane heatsink, 19mm x 22mm (4 off); 15-way ribbon cable, grey IDC 0.5in. pitch, 14 metres; multistrand connecting wire; solder etc.

## CIRCUIT DETAILS

The full circuit diagram for the *EPE Moodloop* is shown in Fig.3. The main supply was chosen to be about 12V so the first task is to reduce this to a suitable operating voltage for the PIC.

Normally this would be done by a regulator referenced to the negative (ground) supply rail, with a.c. coupling between the signal and the output amplifier. This proved unsatisfactory for this design because the very low frequencies necessitate large coupling capacitors and their charging times result in long settling times when the unit is switched on.

The solution employed was to split the supply voltage with resistors R1 to R3 to obtain two voltages with a difference of about 4V, symmetrically about half the supply, which are buffered by op.amps IC1a and IC1b to become positive and negative supplies for the PIC, IC2. These have their own local decoupling capacitors C4 and C5 whilst Zener diode D1 protects IC2 in the event of brief excursions beyond its safe supply range.

A further local decoupling capacitor C8 is provided in close proximity to IC2. The OP279 dual op.amp features rail-to-rail outputs capable of currents of up to 80mA, making it particularly suitable for this application.

A further advantage of supplying the signal generating part of the circuit in this manner is that since the output is directly proportional to the supply, the drive to the output amplifier varies in direct proportion to the main supply. This means that the circuit works with optimum drive level for supplies from below 9V up to about 15V with no further adjustment after the initial setting up. A conventional regulated supply would not offer this feature.

## ACTIVE INPUTS

The four inputs RA0 to RA3 of IC2 have "pull-up" resistors R4 to R7 from the positive supply to IC2 so that the frequency selection switches are "active low", pulling them to negative when "on". A quad d.i.l. switch S1 to S4 is fitted to the p.c.b. for testing but a panel mounted rotary switch S5 may also be used, more concerning this later.

Resistors R9 to R16 convert the output sequence from IC2 to a sinewave, and the final level is trimmed to the optimum value with preset potentiometer VR1. This preset is arranged with supply splitting resistors R17 and R18 so that the output signal stays symmetrical about the midpoint of the main supply, allowing d.c. coupling to the output stages.

Capacitors C9 and C10 remove high frequency components of the "stepped" waveform to eliminate r.f. interference radiation, important in a circuit which is going to be connected to what, in effect, is a large aerial!

## OUTPUT DRIVE

For maximum drive with the 12V supply a "bridge-tied" output is used, where the load is connected to two amplifier outputs (IC4a and IC4b), one of which is in-phase with the input whilst the other is anti-phase. This effectively doubles the output voltage to the load. An anti-phase signal is needed to drive the second amplifier so this is obtained using the op.amp inverter IC3.

Two identical output stages are used. They have to be capable of a maximum current of about 1A, with a mean of about 650mA. Op.amps capable of this level of output current are available but

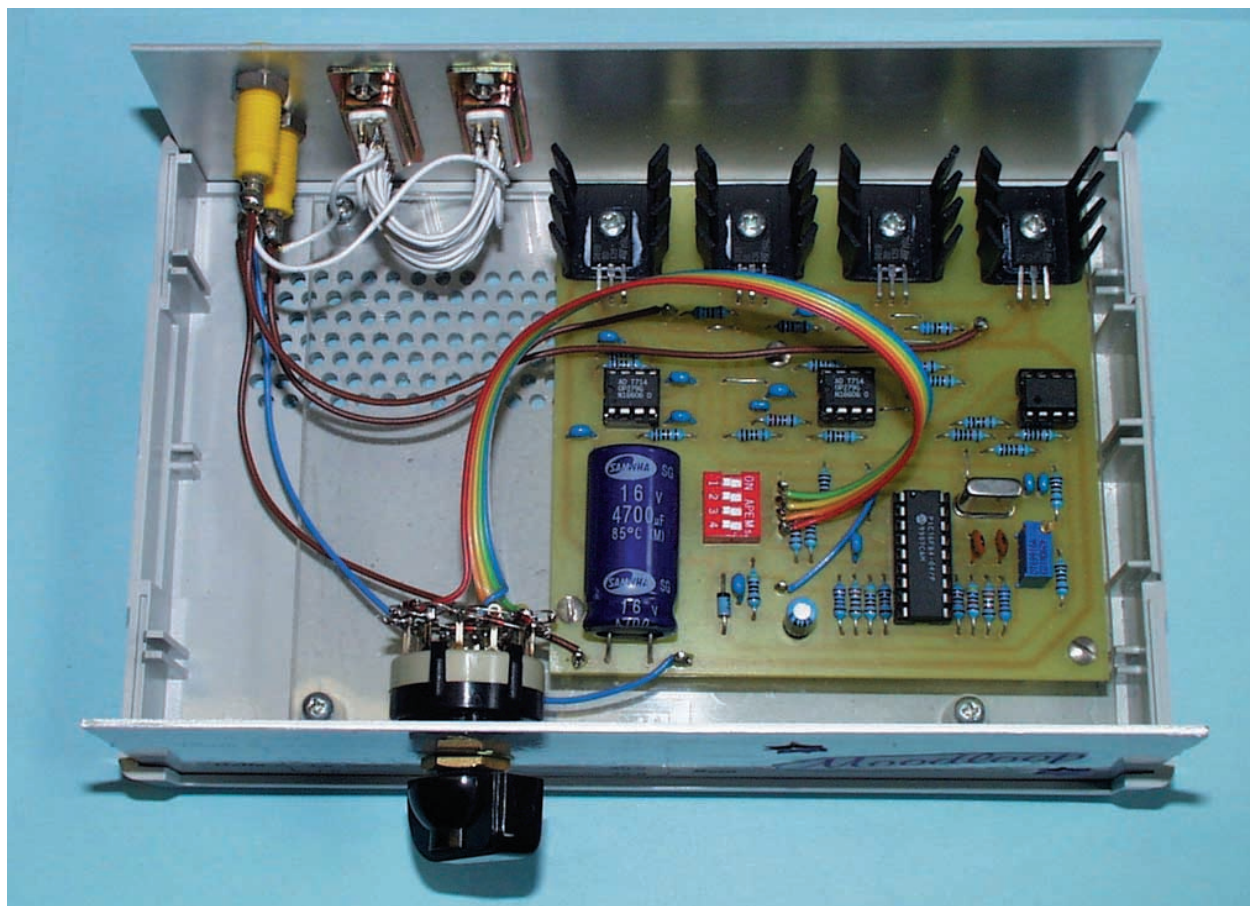
tend to be expensive so a design using power transistors to boost op.amp output power was decided upon instead.

The dual OP279 device was again chosen as the op.amp for its excellent output stage characteristics. Each amp drives the output directly through a 68 ohm resistor (R26, R31), but when the voltage across this resistor rises above about 0.6V in either direction the associated transistor will begin to conduct to provide the necessary load driving power.

The voltage gain of each output stage is about 5.5 so the total gain of the two stages in bridge mode is about 11. To prevent instability occurring with some types of load, resistor/capacitor "snubber" networks (R29/C14 and R32/C16), between each output and ground (0V) are used. Finally, capacitors C12 and C13 reduce the gain at high frequencies, also to improve stability and reduce high frequency components in the output.

Little mention of the frequency selection switch has been made so far. Although the unit can be operated with d.i.l. switches (one is provided on the p.c.b. for testing), it was decided to provide a rotary switch in preference to the fiddly binary d.i.l. switches.

A binary coded rotary switch can be used but most available types appear to be expensive, intended for p.c.b. mounting, fitted with non-standard shaft sizes or otherwise unsuitable for this project. So, a cheap 12-way rotary switch S5 was fitted with sixteen diodes D2 to D17 to provide a binary weighted output on four wires connected to pull-up resistors at the opposite end. The circuit arrangement for the switch and diodes is also shown in Fig.3 and their physical layout in Fig.5.





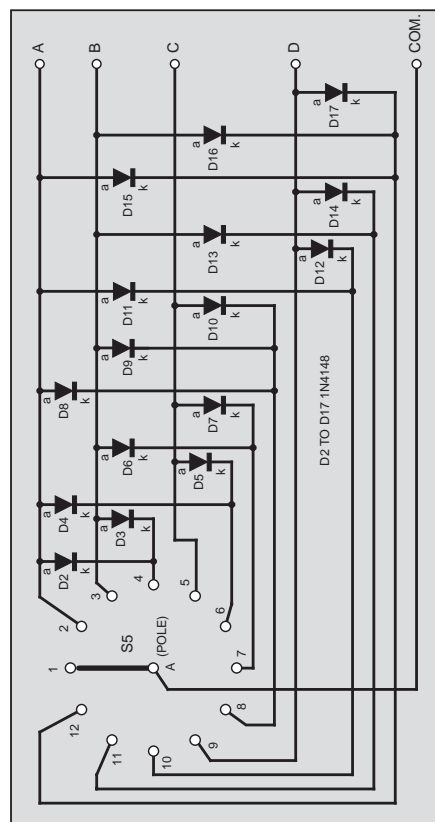
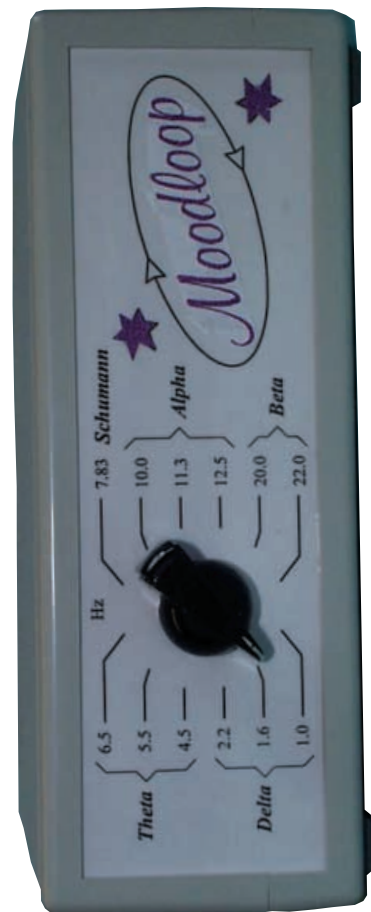
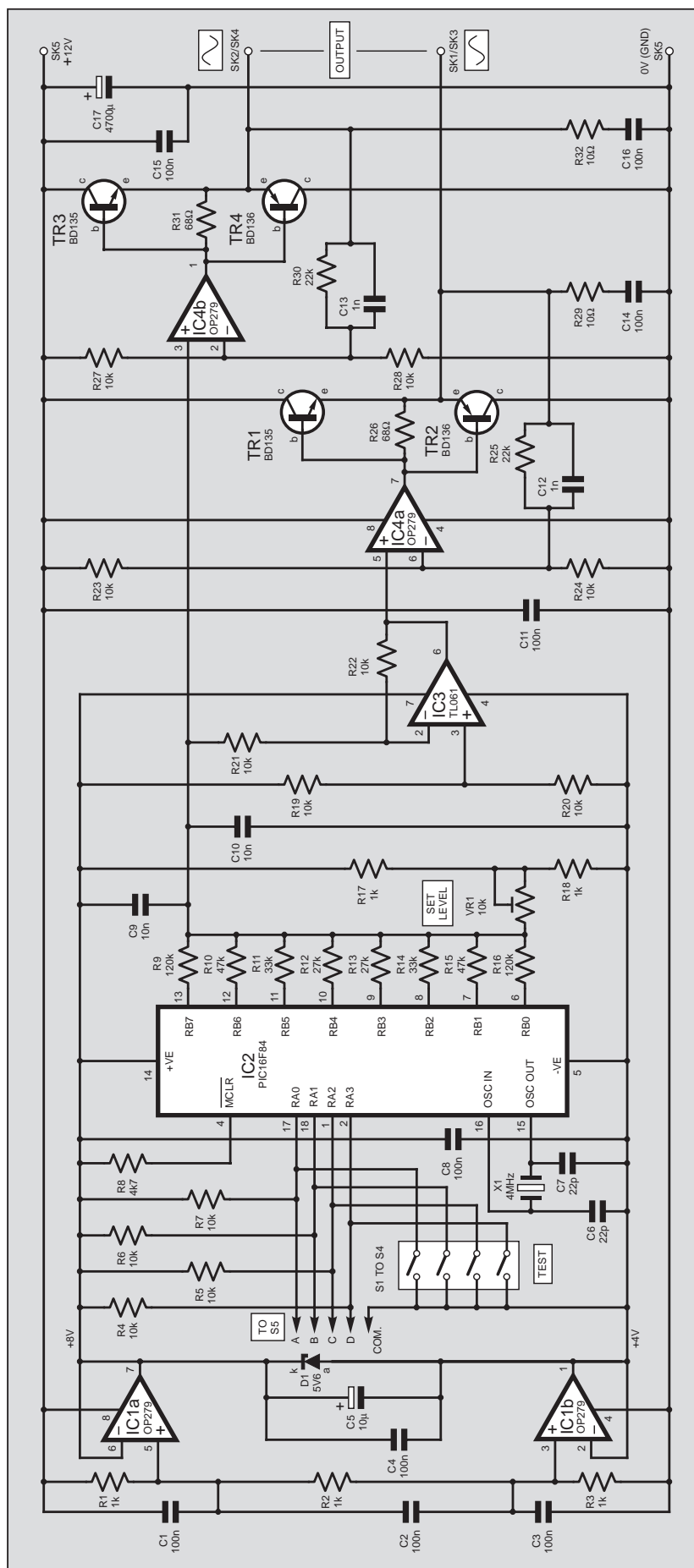


Fig.3. Complete circuit diagram, together with the frequency range switching, for the EPE Moodloop.

## CONSTRUCTION

The *EPE Moodloop* is built up on a medium size single-sided printed circuit board (p.c.b.) and the full size copper foil master pattern are shown in Fig.4. This board is available from the *EPE PCB Service*, code 271.

Construction should not present too many problems. There are six links which should be inserted first, followed by the resistors and the small capacitors. D.I.L. sockets are recommended for the four i.c.s as these simplify testing.

The large electrolytic capacitor C17 should not be fitted until testing is complete as until the load is connected it takes a long time to discharge when the power is disconnected. If a current-limited bench supply is used for testing it can cause a slow voltage rise at switch-on which in turn can lead to the PIC failing to start up correctly.

The four output transistors are mounted on small heatsinks. In the prototype they do not have insulated mounting washers and were just screwed on using dabs of heat transfer compound. Since the transistor mounting tabs are not isolated, they and the heatsinks must not come into contact with each other or with any parts of the circuit and surrounding metalwork.

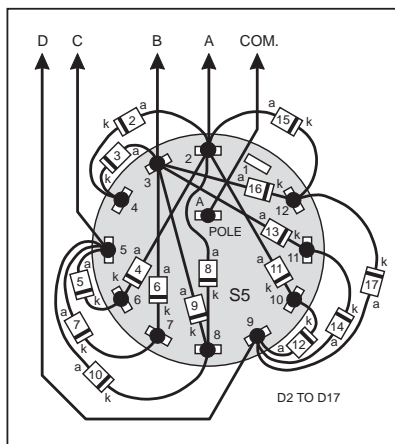


Fig.5. Frequency selection switch S5 construction and wiring.

## TESTING

For testing, the completed circuit should first be powered with a supply of 12V without any i.c.s fitted, preferably from a current-limited bench power supply. Until the load is connected, it will draw only a small current. Without the i.c.s it should draw about 6mA. The aim of this test is to check for any drastic problems before putting any of the i.c.s at risk, so it is worth doing.

If all appears well, IC1 can be inserted, the circuit powered again and the PIC supply tested. This will be found across the leads of Zener diode D1 (positive on the cathode, negative at the anode) and with a 12V supply it should be about 4V. If this checks out IC2 can now be inserted, following which things become more interesting.

Although the final intention is to fit a 12-way rotary switch for frequency selection, for testing purposes an inexpensive 4-way d.i.l. switch S1 to S4 is provided on

the board. Readers will be aware that this gives access to sixteen possible combinations, four more than the rotary switch. These have been programmed as special test frequencies.

The switches are binary weighted with S1 (top) as the lowest or least significant bit. A frequency of 0.5Hz is selected by the 13th setting, binary 12, given by S3 and S4 on, S1 and S2 off (8 + 4). Binary 13 (S1 + S3 + S4) gives 50Hz; 14 (S2, S3 and S4) sets all IC2 outputs high and 15 (all four on) sets all low.

With IC2 inserted the suggested testing procedure is as follows. The supply should be adjusted to exactly 12V for these checks. A voltmeter should be connected with the negative lead to the bottom (anode) lead of Zener diode D1 or the "common" connection point for switch S5, these being the negative supply for IC2, and pin 3 of the socket for IC4, which is the output resistor network from IC2.

With d.i.l. switches S1 to S4 all "on", preset VR1 should be adjusted for an

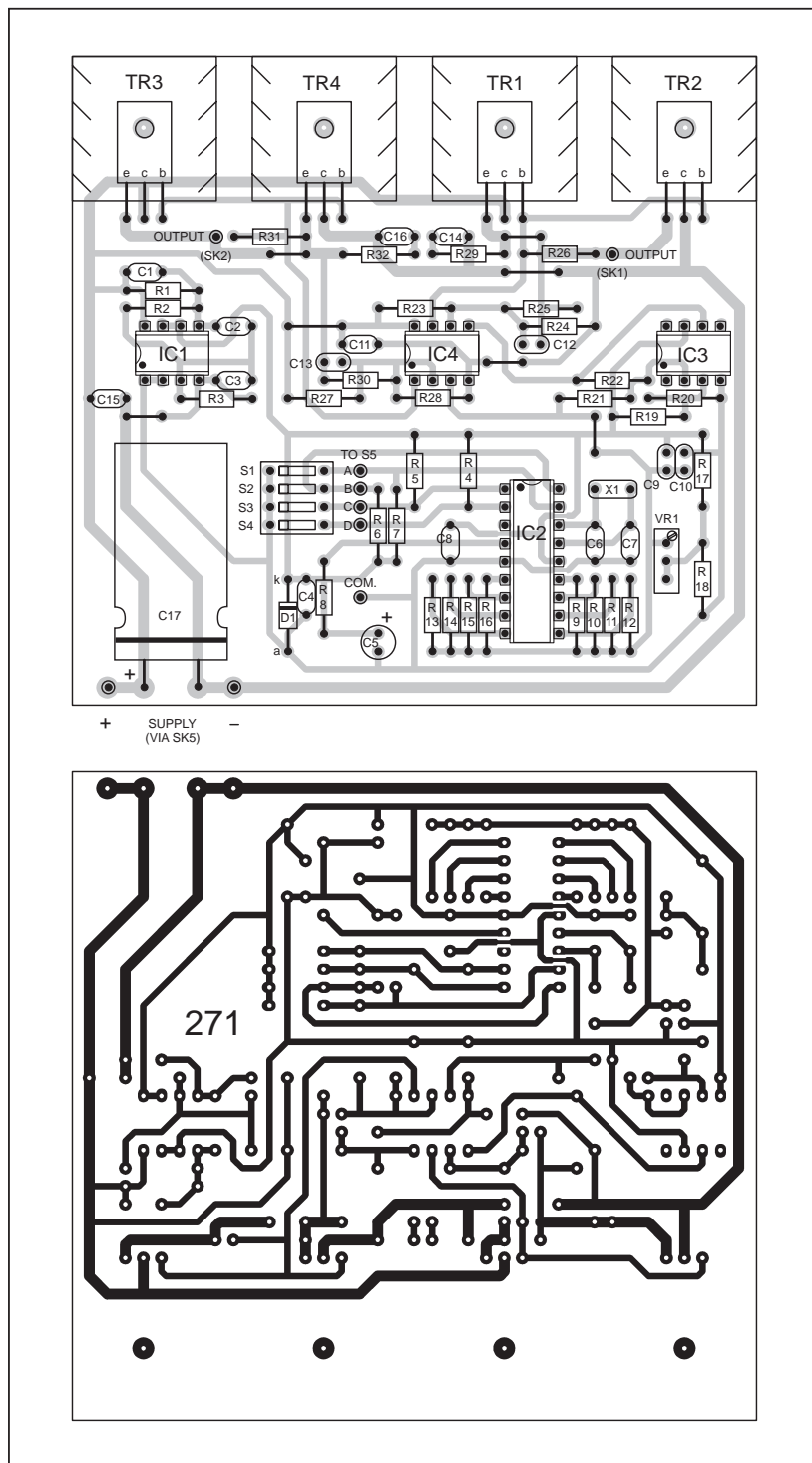


Fig.4. Printed circuit board component layout and full size underside copper foil master pattern.

indication of 1.0V. Switch S1 should then be set to "off", which should cause the reading to rise to about 3.0V.

IC3 should now be inserted on the p.c.b. This loads the output network slightly so it should reduce the above readings to about 1.2V and 2.8V. Their inverse should appear at the output of IC3, pin 6.

An analogue meter may now be used to check the action if the constructor prefers. With switches S3 and S4 "on", others "off", it can be observed following the 0.5Hz signal. With S2 "off" and the rest

## FREQUENCY SWITCH

The Frequency Selector S5 switch will probably have an end-stop behind its mounting nut to limit the number of selectable positions so this should be adjusted to give all 12 positions. In the type used in the prototype it was necessary to remove this device altogether.

Although some care is needed to ensure the diode leads do not short together, the assembly is not as difficult as it looks. It is best to solder the diodes directly to the

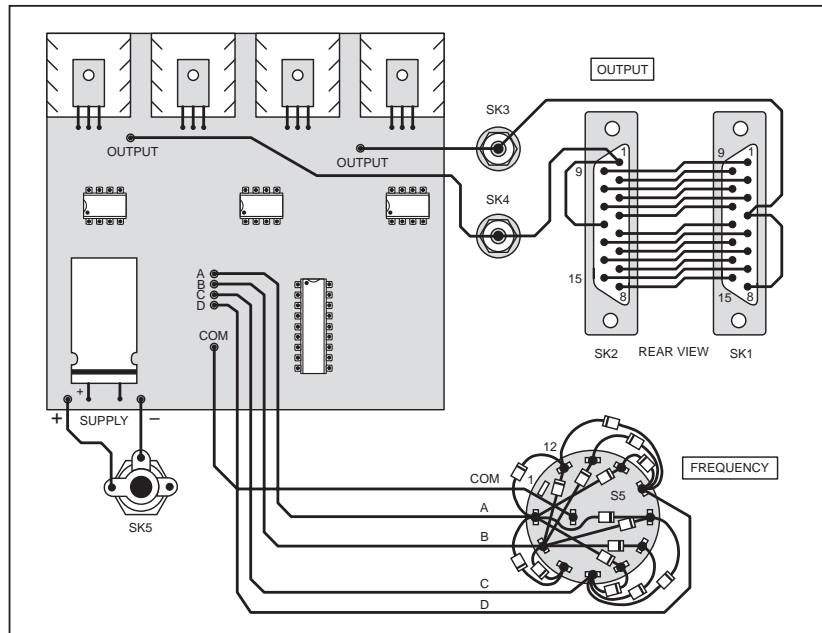


Fig.6. Interwiring from the p.c.b. to off-board components.

"on", the 50Hz output should give an average d.c. reading of about 2.0V. A digital voltmeter (DVM) on an A.C. range should read about 550mV r.m.s. for this output.

Next, IC4 can be fitted on the p.c.b. and a DVM connected across the output terminals, at SK3 and SK4. It should read about 6.0V r.m.s. on 50Hz, or  $\pm 8.6V$  d.c. for the "all high" and "all low" switch settings.

If these tests are all OK the large electrolytic C17 can be fitted. This is mounted horizontally as shown to reduce the overall height of the completed board. The prototype has a spot of "Blu-tack" to hold it securely but glue or double-sided adhesive foam would serve as well.

The p.c.b. should now be ready for use but if a suitable load is available, such as a resistor of about 8 ohms to 10 ohms with a rating of 5W or more, it can be tested at full power with this. It should draw about 600mA to 700mA and the resistor will warm up quite quickly. The r.m.s. reading for the 50Hz output should remain about the same, but it can be adjusted to an absolute maximum of 6.5V r.m.s. with preset VR1.

Note that the power supply used for making this adjustment should be capable of at least 1A, and that continuous operation into a load in the "all high" or "all low" test settings is not recommended as this puts the maximum output current continuously through just two of the output transistors which may cause overheating.

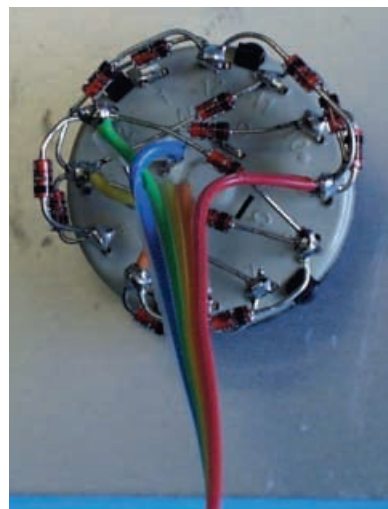
switch tags before mounting S5 in its case.

When switch S5 is connected to the p.c.b. the d.i.l. switches (S1 to S4) should all be "off" to enable it to operate correctly and, conversely, it should be at position 1, where all the outputs are open-circuit, if the d.i.l. switches are to be used for further testing. Out of interest though, if the d.i.l. switches are set up for binary 12 (S4, S3 on) the first four positions of the rotary switch will correspond to the four "test" settings, which may prove useful.

## FINAL ASSEMBLY

The prototype model is fitted into a Vero "Patina" box with dimensions of 180mm  $\times$  120mm  $\times$  65mm, though a cheaper case can be used if preferred. The wiring between the p.c.b. and the switch, power and output sockets is shown in Fig.6. The prototype has no on-off switch as this is provided on the power supply, but one can easily be added if required.

The only points to note are that the metalwork of the transistors such as mounting bolts, heatsinks and so on **must not** come into contact with any other metalwork, and provision for airflow around the heatsinks must also be made. In the prototype this consisted of drilling plenty of holes in the top and bottom of the case, which can be seen in the photographs, and attaching stick-on rubber feet so that air can enter from below the case and leave by convection through the top. This has proved adequate in practice.



Mount the frequency switch diodes directly on the switch tags before the switch is fitted in the case.

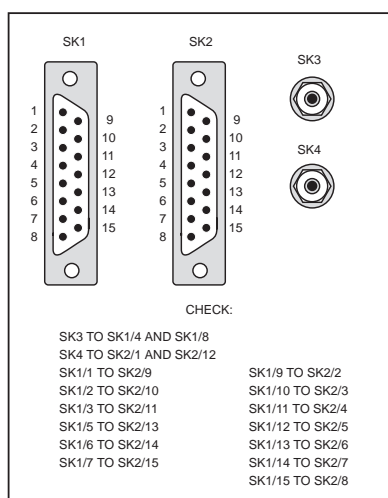


Fig.7. Output socket wiring check.

## INDUCTIVE LOOP

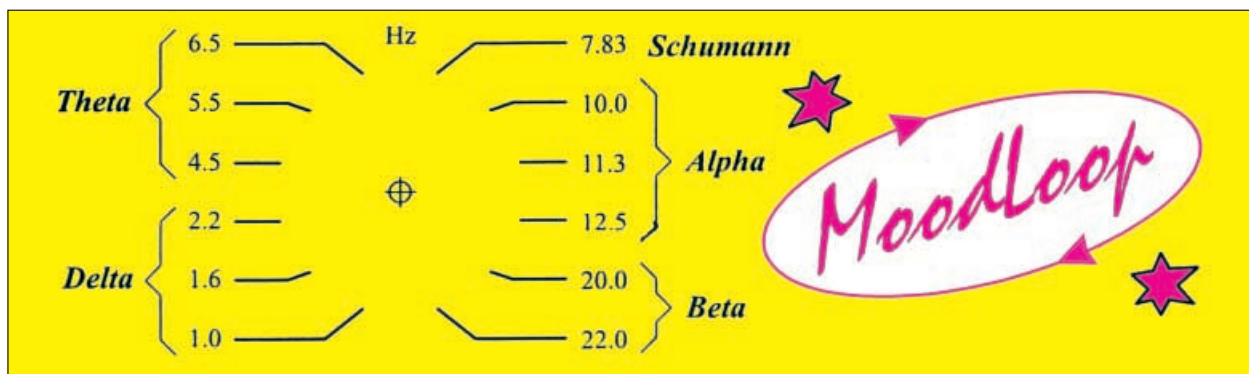
The output can be connected to just about any load with a resistance greater than 8 ohms, the ideal being around 10 ohms. It is intended to drive an "inductive loop" system consisting of multiple turns of wire running around the area to be subjected to the magnetic field.

One way to do this is to use ribbon cable with the cores connected end-to-end to form several turns in series. If the overall resistance is significantly higher than 10 ohms groups of series-connected turns can be connected in parallel to achieve the target resistance.

The prototype uses about 14 metres of ribbon cable connected to give one loop of 7 turns in parallel with another of 8 turns. Two 15-way D-type chassis sockets are fitted to the case and wired as shown in Fig.6 to achieve the necessary arrangement of the cable which is fitted with 15-way IDC D-type plugs.

Polarity of these is arranged so that pin 1 of one plug connects to pin 1 of the other, and so on. They allow loops to be installed and left in place, so that the *EPE Moodloop* unit can be taken to any desired location and just plugged in.





Full-size front panel legend master for the EPE Moodloop project.

For future experiments with other types of coil or loop the unit is also fitted with 4mm sockets (SK3, SK4). It would be quite simple to make a small adapter to connect these to multiway sockets of other types for different types of loop.

If the 15-way sockets are fitted and wired as shown, this should be done with extreme care as there will be little indication of errors. It is suggested that after wiring, continuity testing should be carried out from the front of the sockets as shown in Fig.7 to ensure that it is correct. The "loop" can then be plugged in and the overall resistance measured to ensure it has about the right value of 10 ohms.

## COMFORT ZONE

Positioning of the loop is up to the user. The obvious position is around the area to be covered, at floor level or possibly higher, though if it were placed vertically, perhaps against a wall, anyone in front of or behind it would be exposed to the field. The suggested length of loop may allow more than one turn around a small area for even greater field strength!

It seems likely that the user's position relative to the field is not particularly important, so long as the strength is sufficient. Experiments with a sensitive magnetic field detector show that the field actually extends for quite a distance outside the loop.

It might also be interesting to try using a solenoid of suitable resistance, although this has not been attempted with the prototype yet. A point to watch here, though, is that a few watts of heat are dissipated by the load so it should have the ability to dissipate this, which may not be the case with a solenoid.

## POWER SUPPLIES

Power for the unit is nominally 12V, but the prototype has been tested with supplies ranging from just below 9V to a maximum of 15V, at which setting the four heatsinks become rather warm, but not beyond acceptable limits.

The average current taken will normally be around 600mA to 700mA depending on supply voltage, but the peak current will be over 1A so the supply should be capable of this in view of the low frequencies involved. The voltage must be regulated, as fluctuations with load will cause corresponding distortion of the output so this rules out most "plug-top" supplies as most of these have no internal regulation.

Many constructors will already have suitable power supplies of some kind and

it is also possible to operate the unit directly from a car battery, where the use of "Alpha" frequencies may help to reduce "road rage", or "Beta" might combat fatigue on long trips. However, next month we will be giving details of a simple mains operated low-voltage regulated supply which can be used to supply this project and many others

## SCHUMANN RESONANCE

Before ending, an explanation of the front panel setting labelled "7.83 Schumann" must be given. This refers to the "Schumann Resonance", an intriguing phenomenon amongst the naturally occurring magnetic fields that have always surrounded us. It appears that the space between the earth's surface and the ionosphere forms a gigantic resonant cavity having physical dimensions which give it a frequency somewhere between 7Hz and 8Hz. Events such as lightning excite oscillations in this cavity and very low attenuation at these frequencies allows them to keep going more or less continually.

Enthusiasts of the effects of fields at this frequency say that modern man is missing out on its supposed beneficial effects

because it tends to be masked by more powerful fields from the electrical equipment and wiring which nowadays surrounds us all. It has even been claimed that NASA installed Schumann frequency magnetic field generators in spacecraft after finding that space sickness was in part due to the astronauts travelling beyond the range of this field, although the author has been unable to confirm whether this is true.

However, constructors may now create the Schumann field in their own homes and judge for themselves whether its effects are beneficial. ☐



Completed EPE Moodloop (right) together with Moodloop Power Supply (next month) and a Field Strength Checker (October issue).

# CAVE ELECTRONICS

MIKE BEDFORD



*There are some absolutely amazing sights in the world beneath our feet, and electronics is increasingly helping us to explore them.*

**H**ELMET, lamp, rope, ladders, carabiners, abseiling gear, boots, waterproof oversuit, soldering iron – which is the odd one out?

Not the most difficult of brain teasers, you might think, and it probably took you all of five hundred milliseconds to figure out that the first eight items – but not the soldering iron – are pieces of kit used by potholers.

So you'd be a little surprised, no doubt, to hear that within my sphere of caving acquaintances there is no odd one out – all these items are used regularly in the pursuit of caving. However, before you try to conjure up some spurious caving use of the soldering iron, let me come clean and point out that I know some pretty unusual cavers.

## CAVING GROUPS

As members of the Cave Radio & Electronics Group (CREG) of the British Cave Research Association (BCRA), these particular cavers have a specialist interest in applying their expertise in electronics and computing to the sport of potholing.

This article is an investigation of cave electronics. As with any specialised area, we expect that only a minority of readers will have a first-hand need for many of the techniques or equipment which we'll discuss. However, even if you don't feel inclined, in the slightest, to venture underground, we trust that you'll find this article to be an interesting eye-opener to a most unusual field of research.

We also believe that although caving has been the motivation behind all the developments described here, some of the material will be relevant to above-ground use too.

From CREG's name, you might reasonably assume that a major emphasis of the Cave Radio & Electronics Group is cave radio. This would be a correct assumption – the development of underground communication equipment, and specifically of low

frequency induction radios for through-rock transmission, is a priority for the majority of cave electronics enthusiasts.

There's a good reason for this – effective communication is essential to the UK's cave rescue organisations. Cave radios are used on a regular basis for controlling rescues and it's probably true to say that some individuals owe their life to this technology.

Nevertheless, apart from this brief mention, this article will stay well clear of communications. The subject has been well aired in the amateur radio press, so our

emphasis here will be on some of the less known applications of electronics and computing to potholing.

If you are interested in the radio side, though, a few references to further reading on cave radio (and cave electronics) are provided at the end of this article.

## LIGHTING

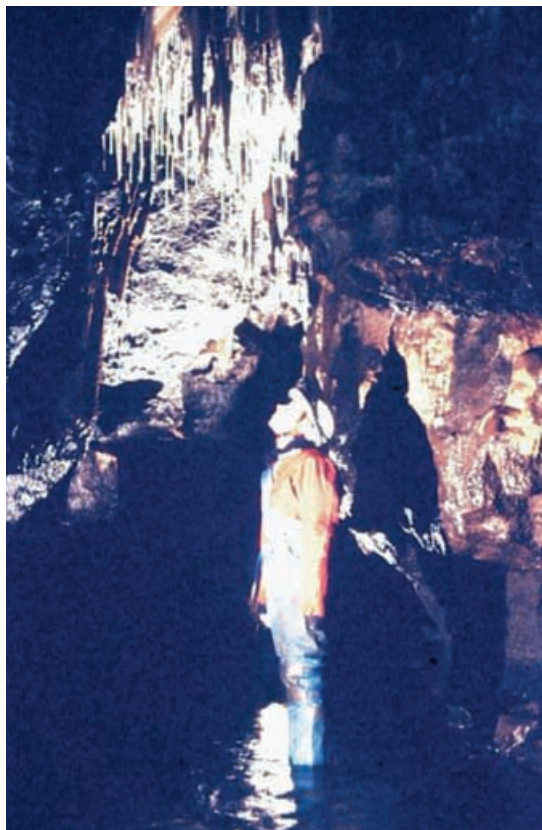
As a tourist on a trip down a well-lit show cave, lighting is something you soon forget about until the guide does his usual trick of turning the lights out – caves are totally and absolutely dark. Arguably, therefore, the single most important piece of caving equipment is a lamp, something which, traditionally, isn't a particularly high tech device.

In the UK, the most popular form of caving lamp consists of a lead-acid or NiCad (nickel-cadmium) battery pack mounted on a belt and connected to a cap-lamp containing a krypton bulb which clips onto the helmet.

Some cavers, especially in Europe, swear by an even more basic solution – the carbide lamp. Here, water drips onto calcium carbide in a belt-mounted reservoir and a plastic tube carries the acetylene gas which is produced to a burner on the helmet.

Whereas many cavers take the "if it ain't broke, don't fix it" view of lighting, many of those who take an interest in electronics feel that there's got to be a better way. So, despite its rather mundane image, cave lighting is one of the main areas to exercise the minds of technically-minded cavers.

Ideally, a caving lamp should last for at least eight hours to enable people to complete most common trips without having to carry spare batteries. It needs to be body-mounted so that the caver has both hands free – essential for negotiating climbs – and it needs to be sufficiently robust that it will be reliable, even when subjected to knocks or to immersion in water. Surprisingly, a caving lamp doesn't actually need to



*Has anyone ever seen that before?*



be particularly bright since our eyes soon become accustomed to low levels of light.

## BRIGHTNESS

Although the current generation of lights do, admittedly, provide most of this, there's undoubtedly scope for improvement. For example, although krypton bulbs are remarkably efficient (given their low power rating) at turning electricity into light, the price you pay is a short lifetime.

Whereas household tungsten bulbs last for a thousand hours or more, small krypton bulbs have a lifetime measured in tens of hours. It's essential, therefore, to carry some sort of reserve lighting, indeed traditional caving lamps have a pilot bulb and it's advisable to also carry a completely independent backup lamp.

Secondly, although today's caving lamps are acceptably bright, if you want to be able to see your surroundings in some detail, as opposed to just having sufficient light to ensure your personal safety, a brighter light would be preferable.

Thirdly, it would be nice to be able to get rid of the battery pack or carbide generator on the belt, which always seems to work its way into the most uncomfortable position when you're attempting to negotiate a tight squeeze. It's not uncommon to get stuck purely because you're wearing a battery pack.

Certainly you can buy caving lamps in which the battery fits onto the helmet but, in order to keep the weight down, these only provide two or three hours of light and tend to be used by school parties and novices on short trips, as opposed to serious cavers on more protracted outings.

## LIGHT EMITTING DIODES

Given these problems with the currently available caving lamps, many electronically-minded cavers have thought intuitively in terms of light emitting diodes (l.e.d.s). They're robust, they consume a very small current and, unlike high efficiency technologies such as discharge lamps and metal halide bulbs, they are cheap, available in low power ratings, and are easy to drive using low voltage circuitry.

What many people fail to take into account, however, is that although l.e.d.s don't consume much power, they don't



Conventional Oldham lamp with large lead-acid belt battery.

## SO WHAT'S THE FASCINATION?

To understand this article, you don't need to be a caver, but a little bit of background knowledge will help you to appreciate it. Perhaps the most obvious question to try to answer is why people go potholing. This is a very valid question given the look of horror which appears on many people's faces when I mention that I'm a caver, especially if I then suggest that they might like to join me on an easy trip to find out what it's all about.

Public perception of potholing is of a dangerous and uncomfortable activity, so what is it about the underworld which potholers find so attractive? How can people get enjoyment from crawling through a passage half full of gelatinous mud, of abseiling down a three hundred foot pitch or of attempting to breath in the three inch air space in the roof of a flooded passage?

There's no single answer to these questions – ask half a dozen cavers why they do it and you'll probably get half a dozen different answers. For some people, caving is, first and foremost, a sport. So the appeal is similar to that of many other sports.

It provides physical exercise and a sense of competition although, unlike football or cricket, the competitive element is a matter of pitting yourself against nature rather than against other players. The challenge of potholing as a purely physical pursuit shouldn't be underestimated. Some of the world's more demanding caves require a very

high degree of skill and stamina over a protracted period of time.

Other cavers spend a major proportion of their time in trying to find new caves, or extending caves by discovering new passages. The appeal of finding a cave which no human being has ever seen before isn't hard to imagine, especially in view of the fact that there must now be so few places on the surface of the earth which nobody has seen before.

Then we have those people for whom a caving trip is a voyage of personal discovery. Even if countless other people have been there before, there are few people, for example, who haven't been awestruck on first seeing the huge main chamber of Gaping Gill. These are the people for whom caving is primarily about seeing the wonders of nature and caves are much more than just muddy holes in the ground – there are some absolutely amazing sights in the world beneath our feet.

We also have those individuals who have some specialist interest in caving. This article concentrates on one such specialist interest – cave electronics, but this is one of many. Cave photography is another popular interest within caving and despite the extra challenges which cave photographers face, the results can be absolutely stunning.

And last, but by no means least, we have hydrologists who study the formation of caves, cave biologists, cave archaeologists, cave surveyors and even cave artists.

actually generate a lot of light. Nor are conventional l.e.d.s particularly efficient at turning electricity into light, achieving a figure of, perhaps, 0.01 to 0.1 lumens per watt. A 2W krypton bulb, on the other hand, clocks up about 15 lumens per watt.

You'll notice, however, that we are referring above to conventional l.e.d.s, and as new high brightness, high efficiency l.e.d.s start to appear, there is a lot of talk about caving lamps based on l.e.d.s. New high efficiency l.e.d.s are more efficient than krypton bulbs in addition to being more robust and having a much longer lifetime.

Furthermore, filament bulbs are a mature technology, but in the realm of l.e.d.s we can expect further significant improvements. However, to use l.e.d.s as caving lamps, as opposed to as indicators, a new way of thinking is required.

Traditionally, digital equipment has been driven from a 5V supply; l.e.d.s, on the other hand, operate off a lower voltage – typically two to three volts – indeed most l.e.d.s will be destroyed if you simply put 5V



Experimental l.e.d. based lamp.

across them. So, a series resistor is used to limit the forward voltage at the 20mA operating current.

Note, however, that the dropper resistor will frequently end up dissipating at least as much power as the l.e.d. itself. For a 2V l.e.d. driven from a 5V supply via a 150Ω resistor, the resistor dissipates 60 per cent of the total power so the efficiency of a 20 lumens per watt l.e.d. would be effectively reduced to eight lumens per watt.

Of course, there's no reason to use a 5V battery or even the 6V battery packs which are conventionally used for caving lamps. Nevertheless it frequently won't be possible to juggle 1.2V NiCad or NiMH (nickel metal-hydride) cells to produce a battery with exactly the right voltage for a chosen l.e.d.



## MULTIPLE L.E.D.S

However, since a single l.e.d. doesn't produce enough light for a caving lamp, multiple l.e.d.s will be needed, and this offers an alternative driving method. With a 6V battery pack, for example, strings of three 2V l.e.d.s connected in series can be used. But even this isn't a particularly effective way of getting the most out of the l.e.d.s, especially if you're using a battery chemistry such as NiCad, which has a noticeably sloping discharge curve.

The current drawn by an l.e.d. has an exponential relationship to the voltage, which means that as the voltage drops there will be an increasingly large reduction in the current and hence in the light output. A modest degree of over-voltage, on the other hand, will severely reduce the efficiency of the l.e.d.

The ideal arrangement, therefore, especially if you want the flexibility to use the l.e.d. lamp with a wide range of battery types, is to incorporate a switching regulator to provide a constant voltage supply.

But, whereas the common high brightness red or green l.e.d.s might seem an ideal solution in many respects – especially if the efficiency continues to increase – monochromatic light certainly is not ideal. Quite apart from the fact that most cavers would prefer not to see everything swathed in red, green or amber light, our eyes are

optimised for white light and our perception of depth can be impaired by monochromatic illumination.

So, the recently-introduced white l.e.d.s – actually high performance blue l.e.d.s with red and green phosphors deposited onto the dies – are of particular interest.

## PROTOTYPE L.E.D. LAMPS

CREG's David Gibson has been experimenting with a prototype caving lamp using no less than 48 of these l.e.d.s. Although it consumes as much power as a conventional caving lamp, the quality and spread of that light is reported as being better.

David's long term aim is to incorporate a PIC microcontroller which will provide a range of operating modes (e.g. normal, pilot lamp, ultra-bright, emergency low power) as well as monitoring battery charge and giving advanced warning of battery failure.

Other experimenters, though, are spurning these white l.e.d.s in favour of a mixture of red, green and blue l.e.d.s to achieve a higher level of efficiency – at the moment white l.e.d.s are not as efficient as krypton filament bulbs.

A lamp designed by Chris Vernon, for example, uses just a handful of l.e.d.s in a mix of colours and is powered by a

switching regulator and a small NiMH battery pack on the back of the helmet. It is more efficient than a lamp based on krypton bulbs, lasts for eight hours, and is much more reliable than lamps based on filament bulbs.

The ratio of the red, green, blue and amber l.e.d.s was a result of balancing a couple of factors – the resultant colour of the light and ensuring that the devices could be used without loss-inducing drop-per resistors.

## PRACTICAL CIRCUIT

The circuit diagram for Chris Vernon's lamp is shown in Fig.1.

The power supply for the l.e.d.s is obtained via a SEPIC (single-ended primary inductance converter) integrated circuit (IC3), actually intended for battery charger applications, which allows operation over a wide range of input voltages.

The MC33465 (IC1) is an under-voltage reset device which produces a timed shut-down if the battery voltage falls below the SEPIC's minimum input voltage of 2.7V. This causes the lamp to flash with increasing urgency as the battery's charge level becomes critical.

The lamp also has an integrated battery charger (the top half of the schematic) which operates from a 9V to 15V d.c. supply. This uses another SEPIC voltage

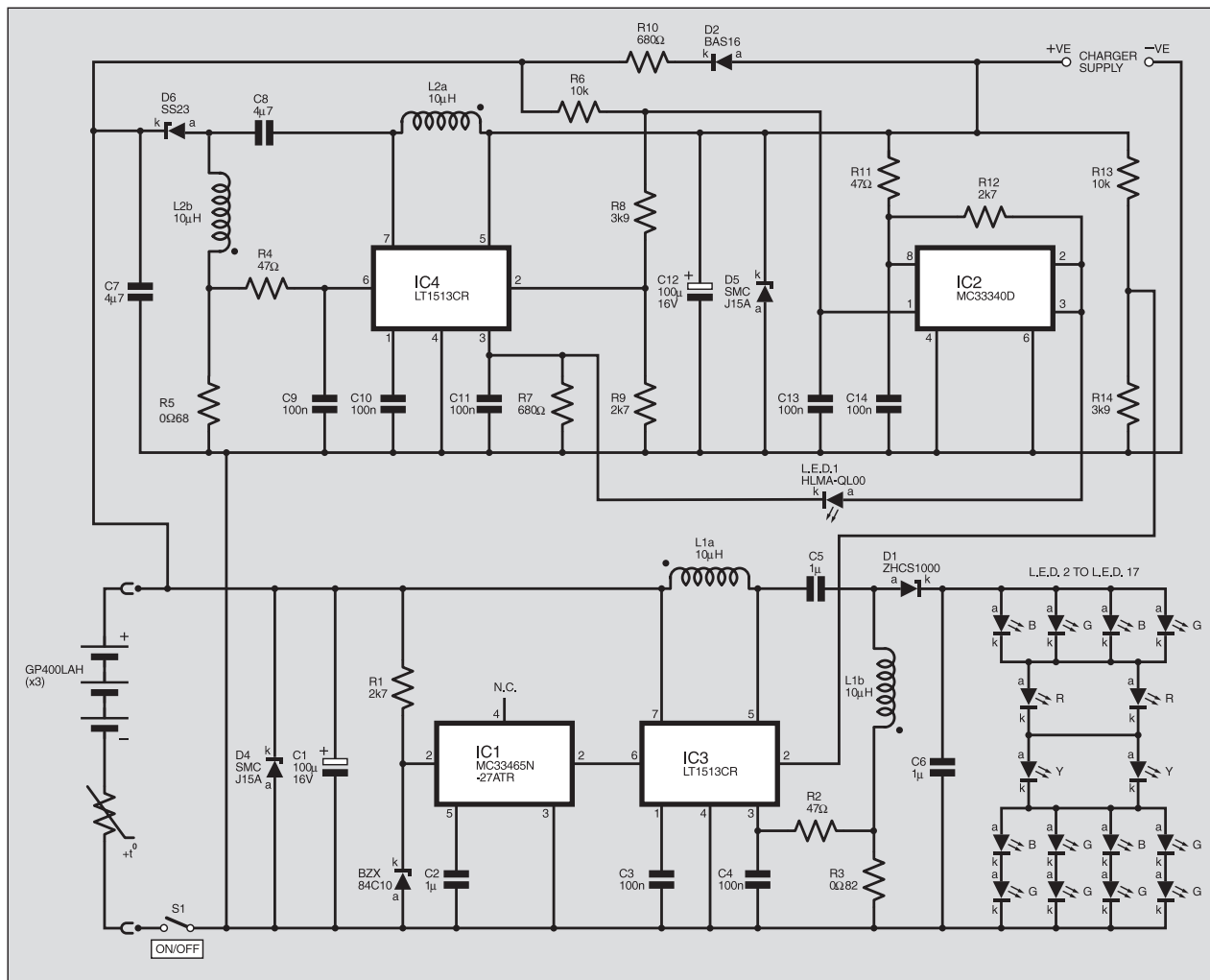


Fig.1. Circuit diagram of Chris Vernon's multiple l.e.d. lamp. Constructional advice is not offered to readers but Shoptalk provides some information on unusual components.

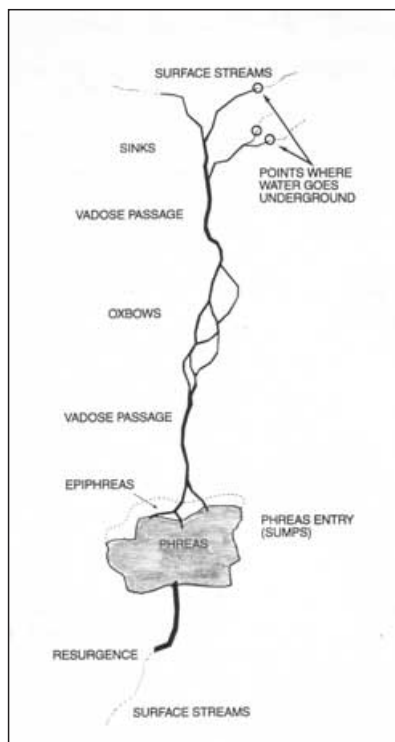


Fig.2. Representation of a typical cave system.

converter (IC4) plus an MC33340D charge controller chip (IC2) which terminates charge by the minus delta V method with time and abnormal voltage as safety measures.

So despite the fact that the component cost of a lamp like this escalates its cost beyond that of a conventional lamp, the fact that a full-blown charger is not needed reduces the overall system cost. A small d.c. power supply is all that's needed, or alternatively the lamp can be charged directly from a car's cigar lighter socket – useful if you've forgotten to put your lamp on charge before leaving for a weekend's caving.

Although the theme of this article is the application of electronics to caving, a high performance lamp like this has obvious above ground applications too.

## FINDING NEW CAVES

Given that a major ambition for many cavers is to discover a brand new cave, it's appropriate to raise the question "how do you go about finding a new cave?"

Quite a number of the caves in the UK were discovered purely by luck, but such is the interest in caving in this country that your chances of just stumbling across a new cave are now very remote indeed.

Instead, finding a new cave is nearly always the result of many days, weeks, months or even years of dedicated effort. Increasingly, even this sort of determined effort is only likely to yield results if you understand the earth sciences such as hydrology and geology sufficiently to know where unknown caves are likely to be located. This is becoming an increasingly high tech activity as electronics and computer science are coming to the fore alongside the earth sciences.

## PIONEERING

Take, for example, the pioneering work which is being carried out by John Wilcock

of Staffordshire University. Fig.2 shows a representation of a typical cave system. Various streams on a hill-side flow into one or more vertical shafts, normally called potholes. Hydrologists – scientists who study the flow of water through caves – tend to call these potholes the sinks, that is, the points at which the water enters the system.

The various sinks meet up underground and the water then flows through a network of vadose canyons – tall narrow passages with a stream flowing along the bottom. These vadose canyons then enter the phreas – an area of phreatic or flooded passages – to emerge some time later, from a horizontal cave in the valley bottom. This cave is what hydrologists call the resurgence – the point where the water leaves the cave system.

Now, let's think about what happens following a heavy rainstorm. In a very short period of time, the water level in the surface streams will rise. During a particularly wet period, that increased water level may last for quite some time, but after a quick heavy storm, especially if the ground is waterlogged, a graph of level against time will show a pronounced peak – this is called a flood pulse.

Clearly, a flood pulse in a stream feeding a cave system will enter the cave as an increased volume of water, and will reappear at the resurgence as a flood pulse some time later. Exactly how long it takes, however, is a function of the cave system and timings will differ depending on the length and types of the passages.

In fact, except in the simplest of caves, the flood pulse won't remain as a single pulse after its passage through the cave. For example, if there are two parallel routes of different lengths through the cave, then a pulse showing two distinct peaks will be seen at the resurgence.

Normally, the flood pulse at the resurgence is much more complicated than this and since there's so much variety between different caves, the shape of the pulse could almost be thought of as the cave system's fingerprint.

## FLOOD SIMULATION

John Wilcock's work involves simulating the passage of a flood pulse through a cave system using Microsoft Excel. This interesting use of a spreadsheet shows that these ubiquitous business tools are also very useful for scientific use.

John's spreadsheet has columns for each of the variables in the simulation – specifically flows and storages – and time is represented vertically. In other words, each horizontal row represents the variables one time interval later than the previous row.

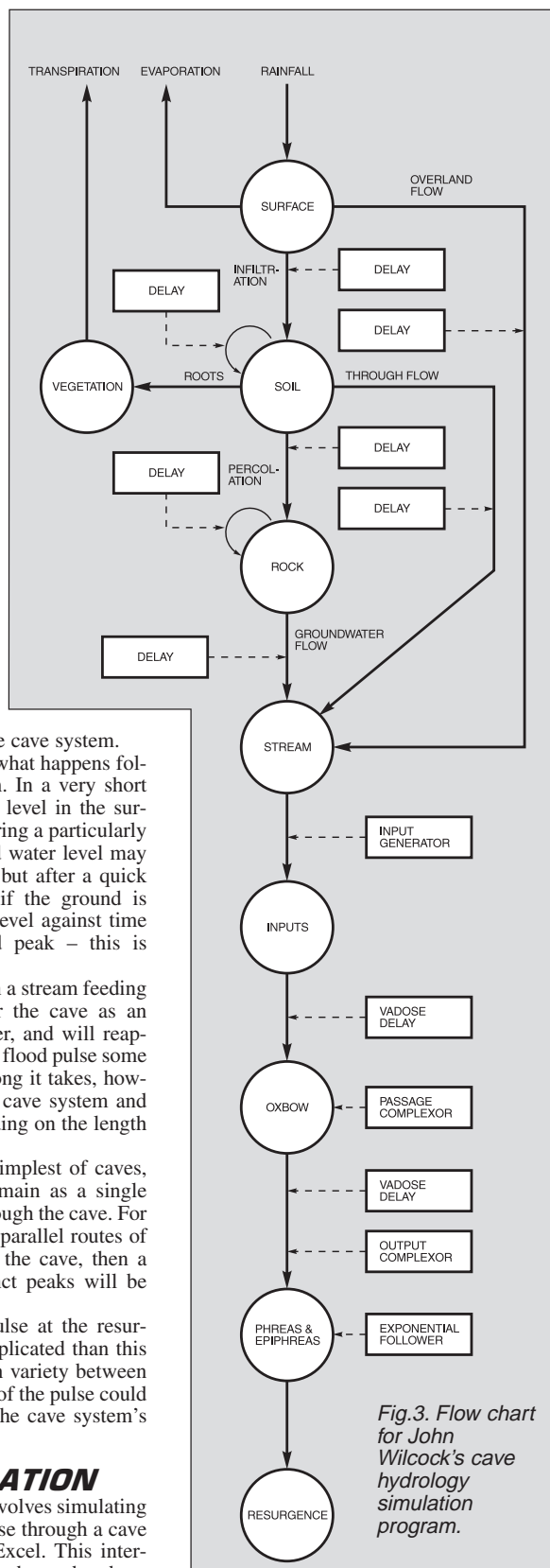


Fig.3. Flow chart for John Wilcock's cave hydrology simulation program.

The actual expressions in the cells are remarkably simple. For example, when stream passages join at a junction, the output flow is simply the sum of the input flows arriving at the junction, and conversely, when stream passages diverge at a junction the flows along each output passage are proportions of the input flow in the ratio of the cross-sectional areas of each passage.

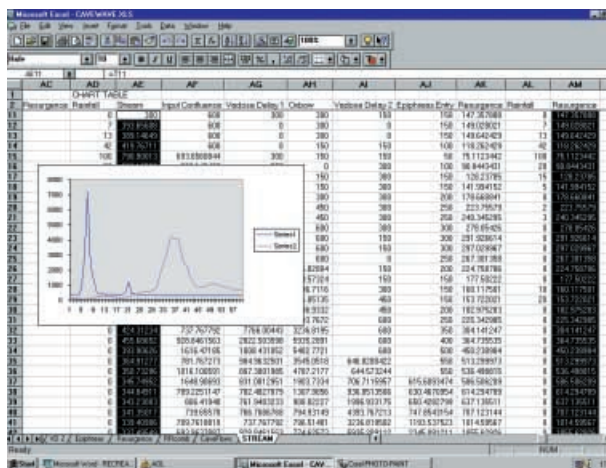


Fig.4. Typical output of John Wilcock's hydrology simulation spreadsheet.

Simulation of a straight passage is even simpler. Flow at the end of the passage is simply the flow at the start of the passage delayed by a few time intervals, that is a few rows in the spreadsheet. Obviously the length of the delay depends on the characteristics of the passage – most notably its length.

Only the phreas (the flooded area) is slightly more complicated. Here, increased flow cannot be transmitted immediately to the resurgence, so the phreas backs up into the previously non-flooded passages, until the pressure is sufficient for the increased volume of water to be forced through the restricted passages.

This is simulated by a simple exponential function. Fig.3 shows the flow diagram on which the simulation is based, and Fig.4 shows a typical screen output. You can download (free) the software from <http://www.sat.dundee.ac.uk/~arb/creg/download/cavewave.html>.

## PUSHING CAVES

Interesting, if you happen to be a cave hydrologist, you might wonder what has this to do with finding new caves. Actually it has very little to do with finding new caves, but it could just help to find new passages in existing caves, or “pushing” caves as potholers tend to call this.

Let's see how this might work. Experimentally, you collect data on rainfall in the vicinity of the cave you're studying and also measure the characteristics of the flood pulse at the resurgence. Now, you use the data on the rainfall near the sinks as input for the simulation and observe the simulated shape of the output flood pulse.

If there is a good match between the experimental and the simulated results, then you can be reasonably sure that the simulation is a good representation of reality. If the two are notably different, however, this would suggest that the simulation is flawed and this could mean, of course, that there are passages which have not been included in the simulation – presumably because nobody knew about them. And this is something which could make cave explorers very excited.

## PASSAGE LOCATION

Of course, just knowing that there are more cave passages in the system than have

already been discovered is only of limited value. Far more valuable is a knowledge of where those passages might be located, and even here John Wilcock is thinking of a possible solution.

Using techniques such as genetic algorithms, for example, it should be possible to modify the description of the passage network automatically until the simulated results match those obtained experimentally. Armed with the resulting information, finding that new passage may just be a bit easier.

Not that this is ever going to be a simple job – needles and haystacks come to mind – but the years of squeezing through impenetrable slots in the ground, of diving through flooded passages and of digging out tonnes of silt may just get reduced to months.

## GEOPHYSICAL CAVE DETECTION

In our discussion of cave hydrology we've wandered slightly off the subject of finding new caves to that of finding new passages in known caves. But what of the big one? How might electronics and computers be used to find a brand new cave?

Whenever cave detection is discussed by technically minded cavers, geophysics always seems to crop up. Various characteristics of the earth – its resistance,

permittivity and gravitational pull, for example – can be measured, and the argument goes that these characteristics will be influenced by the presence of underground voids such as caves.

Techniques such as earth resistance tomography, microgravity and ground penetrating radar are used on a regular basis by mining engineers and civil engineers. But the equipment for most forms of geophysical void detection is expensive and the data processing requirements needed to extract useful information from the background noise can be very significant.

Cavers, of course, tend to have a limited budget so attempts to find caves using geophysical methods have been comparatively rare. One notable exception is earth resistance surveying – sometimes described as the poor man's geophysical method – which the South Wales Caving Club (SWCC) have been experimenting with for some years as part of their *Greensites* project.

## RESISTIVITY METER

The use of earth resistance measurements for detecting the presence of underground artefacts has already been described at some length in *EPE*. Specifically, an instrument for carrying out resistivity surveys for archaeological purposes was described by Robert Beck (*Earth Resistivity Meter*, *EPE* Jan-Feb '97).

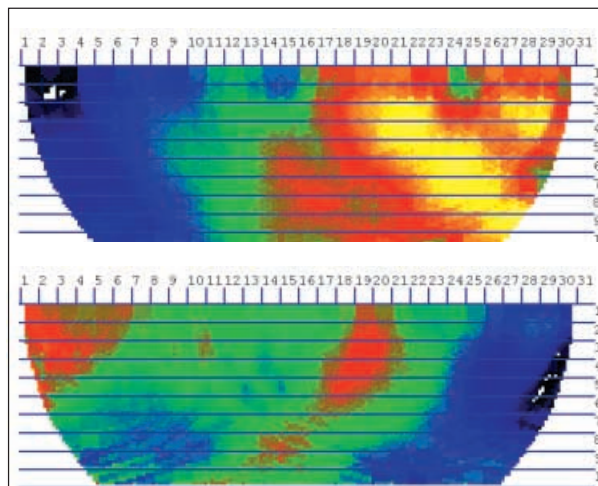


Fig.5. Resistivity techniques confirm the locations of known underground features.



SWCC's *Greensites* Project equipment used to generate resistance maps of vertical slices through the earth, such as those in Fig.5.



In general terms, this type of design works by injecting an electrical current into the ground through one pair of electrodes and measuring the potential difference between a separate pair of sense electrodes. The apparent resistance is calculated by Ohm's Law and depends on the geometry and separation of the various electrodes and the resistance of the earth.

Exactly which portion of the earth you are looking at depends on the electrode separation and the geometry. Normally, the geometry remains constant and different electrode positions and separations are used to select the portion of earth which is being sampled, either horizontally or vertically.

Robert Beck's equipment was designed to generate a 2D map of resistance at a fixed depth. Measurements were made manually in the field and subsequently processed by software to generate the map. The SWCC Greensites design, on the other hand, is used to generate a resistance map of a vertical slice through the earth.

To make the measurement less time consuming, the hundreds of measurements are made under the control of a laptop PC and a graphical representation is generated in the field. The only labour intensive part of the procedure is laying out the 32 electrodes – and this must be repeated for every vertical slice.

## INTERPRETING RESULTS

Interpreting the results requires some care, though. Large cavities at depth can be swamped out by much smaller cavities closer to the surface and the horizontal dimensions of cavities are exaggerated at depth.

Nevertheless, to a first approximation, what you see in a resistance map of a vertical section of earth and a high resistance anomaly – a cave chamber, for example – should be clearly visible. Fig.5 shows a couple of slices through a known cave – Ogof Ffynnon Ddu in South Wales and each scan reveals known features.

The first scan is near the entrance and the large entrance chamber plus a sloping passage to the surface are clearly visible. The second scan is over Gnome Passage and known underground features are, once again, recognisable.

But even with this technological advantage, finding new caves still hasn't proved to be easy. Despite SWCC's initial success

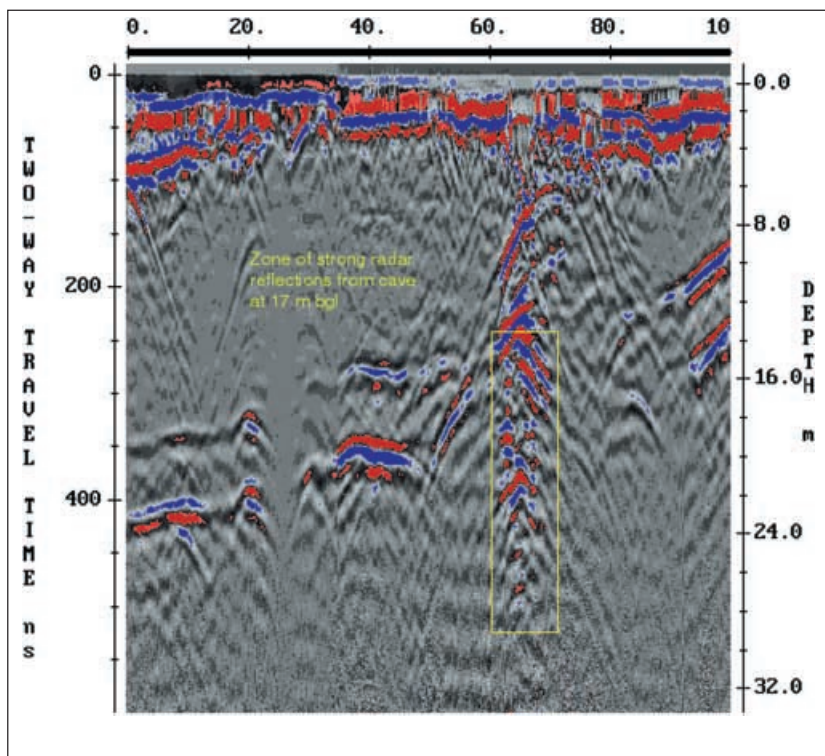


Fig.6. GPR survey beyond known extremities of Poole's Cavern shows unknown passages.

in confirming known features in known caves, the equipment has, so far, failed to find any significant new caves at all.

So how can we explain this, especially in view of the fact that this sort of technique reaps benefits commercially? Part of the difference between commercial and caving use lies in the fact that commercial users, such as mining companies, know fairly accurately what they're looking for.

For example, if you're looking for old mine passages, you'll know approximately where they're located and their approximate dimensions. So, if you find a high resistance feature of the wrong size or shape or in the wrong place, you discount it.

Cavers, on the other hand, don't know what they're looking for since cave passages and chambers come in all shapes and sizes. Unfortunately, many geological features other than cave passages will also show as areas of high resistance using this technique.

So South Wales cavers have found unknown areas of high resistivity, certainly, but in many cases, these will have been areas of shattered rock rather than voids. Commercial organisations with lots of money to spend attempt to overcome this sort of ambiguity by using a combination of geophysical methods rather than relying on any single method.

## WHERE'S THE ENTRANCE?

But there's another interesting question which is unique to finding caves. Even if geophysical methods succeed in finding a genuine void, how does this help the cave explorer? With conventional cave exploration techniques, the first thing you find is the entrance. With geophysical techniques, the first thing you find is a void, perhaps fifty metres below your feet. This isn't a great deal of help if you don't know where the entrance is.



Using GPR probes to survey the terrain at Poole's Cavern.



Borehole drilling at Poole's Cavern.



Stills of video footage taken down the Poole's Cavern borehole show a cave passage in which nobody has ever set foot.

This is exactly the problem facing cavers at Poole's Cavern, a show cave in Buxton, Derbyshire. The cave currently comes to an end in a boulder choke, but cavers have long suspected that there may be further cave passages beyond or above this obstacle. But finding a way through a boulder choke, basically a huge unstable pile of massive rocks precariously balanced one on another, is both time consuming and risky.

So after years of futile work in the boulder choke, a high tech solution in the form of a ground-penetrating radar (GPR) was brought in by Rod Eddies. Fig.6 shows a portion of the survey made above an area beyond the boulder choke where no cave passages were known. It reveals features which geologists interpreted as cave passage.

Undeterred by the difficulty of finding a way into that passage, a couple of boreholes were drilled from the surface, the second of which broke into a sizeable void 16.5 metres down. To investigate this further, video recording equipment consisting of a monochrome CCD camera and 50W dichroic lamps was lowered down the borehole.

The equipment was built by Phil Gregson and is similar to equipment which is used commercially for investigating large voids within restricted locations – mainly for civil engineering applications. The results were pretty impressive, as the accompanying video stills show.

The passage was estimated to be 2.5 metres high and was lavishly decorated with flowstone, gour pools, straws, stalactites, stalagmites and curtains. The amazing thing on watching the video footage, however, is that this is a cave in which nobody has ever set foot. If efforts at finding a way into that passage continue to concentrate on the boulder choke, it could still be many years before man does set foot there.

Of course, there is an alternative method, namely to drop a shaft into the cave, indeed the merits of this approach are currently being discussed. Whether this is really in the true spirit of cave exploration, though, is open to debate.

## OTHER APPLICATIONS

We've seen some of the main areas in which electronics and computing are being applied to caving but these aren't the only

applications by any means. Cave hydrologists are using rugged microprocessor-controlled data loggers to record trends in water level, pH and temperature.

To avoid having to make frequent trips into the cave to download the results, researchers have experimented with telemetry to send the data to the surface using low frequency induction.

Cave conservationists, intent on ensuring that caves aren't destroyed by over-use, are installing caver counters in some of the more popular caves. These are small PIC-based devices, hidden from the prying eyes of cavers, which detect cavers' cap-lamps.

Cave biologists are using electronics to translate the inaudible high frequency bat calls to a frequency which we can hear. There's even talk of using a digital signal processor (DSP) to analyse the sound and thereby record which species of bats were present at particular times and dates.

We must not forget cave photography. Electronics comes to the fore here in the area of slave units to trigger one flashgun

from another, and in the flashguns themselves. Although most cave photographers currently use standard flashguns off the shelf, there's a growing interest in providing specialised flashguns with features designed specifically for caving use.

One such feature is a long burn time so that moving water actually looks blurred as if it's moving, as opposed to looking frozen – something which happens which ordinary flashguns which provide illumination for as little as a millisecond.

## ELECTRIC STOP

To give you an idea for some of the more bizarre ideas which cave electronics enthusiasts dream up, let's describe the concept of the Electric Stop. But before I start, let it be made perfectly clear that this is most *definitely not* something to be attempted as a DIY project. Certainly I wouldn't want to be around to pick up the pieces if you did try it out.

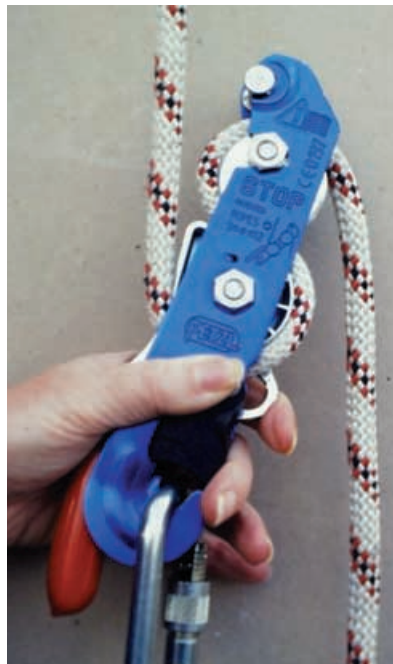
With that disclaimer, let me introduce you to SRT, otherwise known as Single Rope Techniques, the methods by which cavers descend and ascend vertical pitches using a rope. With space running out I'm not going to tell you how we get back up the rope but the downward trip is made by abseiling.

But, rather than using the figure-of-eight device which you may have seen used on outdoor pursuit courses, cavers favour the "rack" or the "stop", most commonly the Petzl Stop named after the French company which manufactures it.

All abseiling devices work by friction, but the stop has the added advantage of being virtually fail-safe. So long as the stop is attached to your harness, once you've correctly threaded the rope through its pulleys, you're only able to descend by squeezing the handle on the stop – as soon as you let go of the handle, a brake is applied.

At the bottom of a long dry pitch it's common to find that your stop is pretty hot. After all, you had a lot of potential energy at the top of the pitch which has now been dissipated as heat. So some bright sparks have figured that it must be possible to conserve this energy, converting it to electricity and using it, perhaps, to recharge your lamp battery.

This, of course, is the principle of regenerative braking which is used to increase



"Petzl Stop" which some cavers think might be modified to generate electrical power.



the efficiency of electric vehicles. In principle, you arrange for the rope to pass through a series of pulleys which are connected to a generator. So long as the generator is connected to some electrical load, a battery charger for example, then a degree of braking is applied and you make a controlled descent.

I don't think I'd be rushing out to buy one, however, even if they were on the market – one broken wire and it's the hospital if you're lucky. It's not the sort of thing you'd want to be protected by a fuse, either. If the fuse blows, the electrical load is removed and all braking is lost. And it doesn't give you too long to change the fuse when you're plummeting to the bottom of a pitch at an ever increasing velocity!

## DON'T TRY THIS AT HOME

Many of the articles which appear in this magazine are constructional projects so the whole idea is that you try it out yourself at home. By way of contrast, I'm concluding this article with that well known phrase "don't try this at home" and this doesn't apply only to the Electric Stop.

Of course, the purely electronic side of building the kit described here isn't dangerous. What you shouldn't do, howev-

er, is pack your creations up in a backpack and wander down your nearest cave or pothole. If it's not already obvious to you, let me point out that caves are dangerous environments in which you could fall to your death, drown, get lost, get stuck or simply suffer from exhaustion and hypothermia and find that you're unable to make your own way out.

That's the bad news, but the good news is that most people, so long as they're reasonably fit, could become cavers. Despite public perception to the contrary, you don't even need to be particularly thin to get down most caves and potholes.

## TAKING UP POTHOLING

If you do fancy taking up potholing, it's vitally important that you make contact with experienced cavers who can show you the ropes – literally – this is one area in which self-tuition just isn't a sensible option.

Unless you already know some cavers, your best bet is to join a club – a list of the UK's caving clubs can be found at <http://web.ukonline.co.uk/nca/clubs.htm>.

If your main interest is the electronic side, you should consider joining CREG who arrange regular field meetings, in addition to publishing a quarterly journal.

## FURTHER READING

If you want to read about the communications aspects of cave electronics, and particularly about low frequency induction or "cave radio", the following two-part article is introductory in nature:

*Introducing Cave Radio*, Mike Bedford, *Radio Active*, Jan-Feb '99.

If you want to delve more deeply (pun intended) into all aspects of cave radio and electronics, the world's premier publication on this subject is the *Journal of the Cave Radio & Electronics Group*. This 32-page magazine is published quarterly and covers the subject from both a theoretical and a practical viewpoint.

For further information on CREG and the CREG Journal, please send a self-addressed stamped envelope to Rob Gill, 61 Cross Deep Gardens, Twickenham, Middx TW1 4QZ.

<http://www.bcrn.org.uk/creg/>.

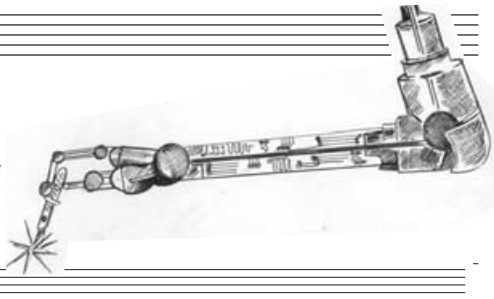
## ACKNOWLEDGEMENTS

We express our thanks to the following for supplying illustrative material used in this article: Rod Eddies, Phil Gregson, Clive Jones, Alan Walker, South Wales Caving Club. □

## Regular Clinic

# CIRCUIT SURGERY

ALAN WINSTANLEY  
and IAN BELL



**Our in-house circuit surgeons investigate piecewise linear (PWL) approximation and explain the meaning of the lambda parameter associated with FETs.**

## Get wise about Piecewise

Our thanks to **Mr P. T. Hall** of Mansfield, Notts, who writes with questions relating to circuit simulation computer software. Mr. Hall asks:

*Please could you explain the meaning of piecewise linear component definitions? Also, the software that I use has a facility for creating components from data sheet information. In order to create a model of a JFET transistor a parameter called lambda is called for. On the data sheets that I use lambda is not mentioned, could you explain what lambda is and how to calculate this parameter?*

Circuit simulation software had until recent years only been used by professional electronics engineers, but now, as a look through the adverts in *EPE*, or a web search will indicate, some of these tools are being marketed to schools and hobbyists where they are proving invaluable for helping with circuit development. Of

course, if you start using a simulator there are a whole load of new concepts and jargon to learn, as Mr. Hall discovered. We would be interested to hear from readers who have used simulators and will be happy to try to answer general questions, although we probably cannot help with the use, features and (whisper) bugs of specific products.

The term **piecewise linear** has two obvious parts: piecewise and linear! The meaning of *piecewise* is straightforward, that is in "parts" or "sections" (we'll come back to this in a moment). The term *linear* has a specific mathematical meaning which is also related to the way in which circuits can be modelled mathematically (e.g. for simulation or other calculations).

Mathematically speaking, the term *linear* has a precise meaning, usually defined with respect to particular situations, perhaps the most basic being "linear functions", so it helps to know what a *function* is in order to define "linear". Functions are of interest to us because when we model a

circuit (run a simulation or perform a calculation) we are in effect saying that the behaviour of the circuit can be expressed as a mathematical function.

## Functional meaning

A function is simply a relationship between the values of two or more variables. For example,  $y=2x$  means that the value of  $y$  is twice that of  $x$ , so if  $x$  is 4 then  $y$  will be 8. Just as  $x$  stands for "any" value then we can write  $f(x)$  to mean "any function of  $x$ ". In our example  $f(x)$  is  $2x$ . Now functions relate to circuits as we have already hinted, so for example if  $x$  represents the input to a circuit (e.g. in volts) and  $y$  represents the output voltage, then if the circuit function  $y = f(x)$  is  $y = 2x$ , then the output voltage is twice the input voltage. This could represent a voltage amplifier with a gain of 2.

So what is a *linear* function of  $x$ ? It is one of the form  $f(x) = ax + b$ , in which  $a$  and  $b$  are constants. For example, the function  $f(x) = 60x + 100$  is linear. The exponential function,  $f(x) = \exp(x)$  ( $e$  to the power of  $x$ , or  $e^x$ ) is an example of a non-linear function. The use of the term "linear" for the property we are discussing should make sense if you plot graphs of functions – for a linear function you'll see a straight line; this is illustrated in Fig.1 which shows a graph of the two functions just mentioned.

You may know that a diode has an exponential relationship between forward voltage and current and therefore the diode is a *non-linear* device. You have probably also heard terms such as "linear amplifier". Returning to our earlier example of  $y = 2x$ , you should be to see that this is a linear function. In fact real amplifiers and other real linear circuits are only linear over a limited range (an amplifier has a maximum input voltage



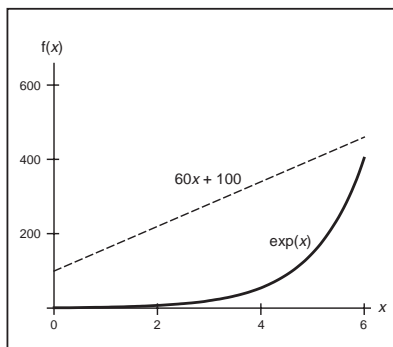


Fig. 1. Two functions of  $x$ , one is linear, the other non-linear.

above which the output is no longer equal to the gain times the input).

Conversely some devices, with transistors being particularly relevant, have some *non-linear* characteristics, but behave with a good approximation to linearity over very small ranges of input variation. This allows linear circuits to be built using these devices as long as the input signals are small, and allows us to use what are known as “small signal models” for calculation and simulation. A good example of this is the differential amplifier which forms the input stage of op.amps (see *Circuit Surgery* April and May 2000).

## Calculating

Performing calculations with linear functions is easier (and in computing terms, often faster) than using non-linear functions, so in some situations non-linear functions can be modelled by a set of linear functions which are applied for different values. Graphically we replace a curve with a set of straight lines. This is known as a **piecewise linear approximation** (PWL). It may be used to model components or input waveforms to a simulator. For example, we could represent the forward bias characteristics of a diode (Fig. 2a) with a PWL approximation using two straight lines (Fig. 2b).

Simulators such as SPICE generally use more sophisticated models for standard components such as basic diodes and transistors which are built into the simulator. Some of these built-in models are based on the device physics and fundamental parameters (such as the lambda value discussed below), however, you can only use them if the simulator has the

model and you know the various parameter values.

The PWL technique allows users to describe special components and those for which the basic parameters are not known. The PWL model can be thought of as a set of “data points” from a graph: for instance you could describe Fig. 2b using co-ordinates of the ends of each line segment (0,0) (0.7,0) (0.8,1.5). You will need to refer to your simulator’s documentation to find out the format for entering PWL model data. Obviously, using many short lines in the PWL model provides a higher resolution which gives greater accuracy than a small number of long lines. Some simulators “smooth” the corners to the PWL model to give more realistic results.

## FET Lambda

Moving on to the next part of Mr. Hall’s question, lambda ( $\lambda$ ) is described as the “channel length modulation parameter”. This models the effect of varying the drain-source voltage ( $V_{DS}$ ) on drain current ( $I_D$ ) when the device is in saturation (i.e. when the gate voltage above pinch-off ( $V_p$ ) is less than  $V_{DS}$ ). Ideally when a JFET is in saturation,  $I_D$  is set by the gate-source voltage ( $V_{GS}$ ) and does not vary as  $V_{DS}$  is varied; however for real devices, increasing  $V_{DS}$  with a fixed  $V_{GS}$  results in a change in  $I_D$ .

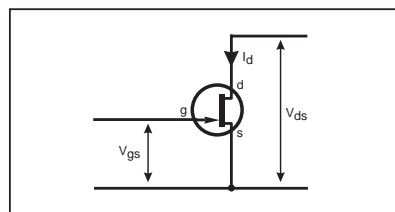


Fig. 3. JFET voltages and currents.

The channel length modulation effect can be modelled by multiplying the ideal drain current in saturation by the factor  $(1 + \lambda V_{DS})$ . Ideal devices with  $\lambda = 0$  have an infinite output resistance (behave like an ideal current source), for non-zero lambda the device has a finite output resistance. Fig. 3 shows the JFET symbol, voltages and currents, and Fig. 4 shows typical output ( $I_D$ - $V_{DS}$ ) characteristics.

If you do not specify the value of lambda in SPICE (we assume you are using a

SPICE style simulator), it may use a default value of zero (the ideal case). You could try entering zero if your software insists on a value and see what happens. Of course, a value of zero will not give you a very accurate simulation but it may be sufficient. A typical value for real devices is  $\pm 0.01 \text{ V}^{-1}$  (positive for *n*-channel, negative for *p*-channel), which may be a better guess. You could also try measuring lambda for an actual device (if you have one handy) by measuring and plotting the output characteristics (i.e.  $I_D$  against  $V_{DS}$  for various fixed  $V_{GS}$ ) and using these to obtain a value for lambda. Alternatively you can use the output characteristics given on the data sheet.

To calculate the value of lambda from the output characteristics, take two points on the same  $V_{GS}$  curve in the saturation part of the characteristic (i.e. on one of the dotted lines in Fig. 4). The values at these points are  $V_{DS1}, I_{D1}$  for the first point (lower values) and  $V_{DS2}, I_{D2}$  for the second point (higher values). Lambda is then given by

$$\lambda = \frac{I_{D2} - I_{D1}}{V_{DS2} I_{D1} - V_{DS1} I_{D2}}$$

For example, if using the  $V_{GS} = 0$  curve, we get  $I_{DS} = 14.7 \text{ mA}$  when  $V_{DS} = 10 \text{ V}$  and  $I_{DS} = 15.0 \text{ mA}$  when  $V_{DS} = 12 \text{ V}$ , then  $\lambda = (0.015 - 0.0147) / (12 \times 0.0147 - 10 \times 0.015) = 0.0114$ . These values are for illustration only and do not relate to any specific real device.

Lambda is a pretty basic device parameter so it is unlikely that you will be able to calculate it from other data sheet values (apart from the output characteristic curves) if it is not specified. In general, if you are using SPICE remember it is orientated towards integrated circuit simulation. The parameters on data sheets for discrete components do not always match up very well with SPICE model parameters. For example, SPICE models do not use the  $h$  parameters (e.g.  $h_{fe}$  often quoted for bipolar transistors). The information may be there under a different name, or you may have to read it off of one of the curves on the datasheet.

If any other readers have any general queries on the principles of using circuit simulators we will try to help, so write to us or E-mail to the usual address.

IMB.

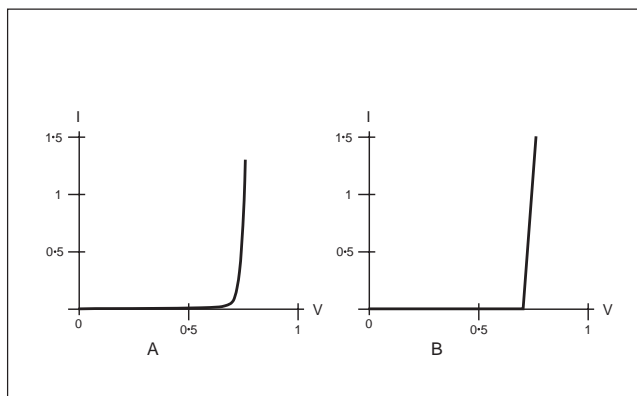


Fig. 2. Diode characteristics graph (a) and piecewise approximation graph (b).

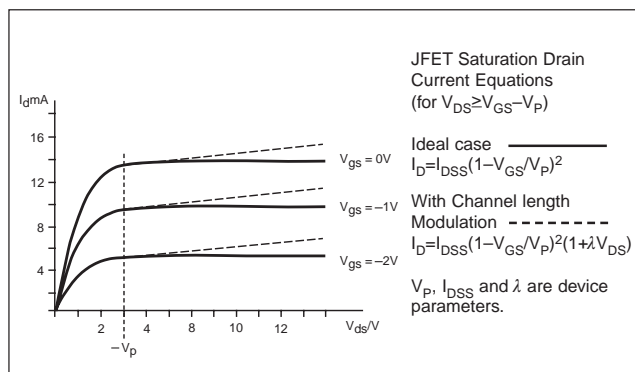


Fig. 4. FET output characteristics (the values in this figure do not relate to a real device. For a real device you should be able to find these characteristics on the data sheet).

# READOUT

**John Becker addresses some of the general points readers have raised. Have you anything interesting to say? Drop us a line!**

## WIN A DIGITAL MULTIMETER

A 3 $\frac{1}{2}$  digit pocket-sized l.c.d. multimeter which measures a.c. and d.c. voltage, d.c. current and resistance. It can also test diodes and bipolar transistors.

Every month we will give a Digital Multimeter to the author of the best Readout letter.



## ★ LETTER OF THE MONTH ★

### TIDAL DYNAMICS

Dear EPE,

In his very interesting *Canute Tide Predictor* (June '00), John Becker enquired as to the reason the tides do not move up the English Channel at 330mph, and requested replies to *Readout*.

His assertions would be true for the case of the mythical "ideal" flooded planet, where the entire surface is ocean. In that case, the two tidal bulges he shows in Figs.1 and 2 (June '00) would indeed track the rotation of the Earth, there being nothing to impede them.

In reality, the Earth is divided into a number of oceanic basins and other bodies of water, and the effect that this has can be imagined if you consider a single large ocean occupying less than half of one hemisphere (e.g. the Atlantic or the Pacific).

As the Earth rotates, the tidal forces will tend to drag the water towards the western edge of this ocean, but then it must return to the eastern edge for the next cycle, so a circulation is set up with the effect that the high tide progresses around the coastline of the ocean, arriving at any point according to the dynamics of the system rather than the time of maximum tidal force exerted.

The world's oceans are only loosely coupled through relatively narrow straights, so act more-or-less independently of each other. The

North Sea, for instance, provides its own tidal circulation, but is strongly influenced by the Atlantic. Meanwhile, the English Channel connects the two through what an electrical engineer would consider a transmission line, so the tide "signal" propagates along it at the velocity appropriate to its "inductance" and "capacitance".

Thus, the dynamics of the tides are way more complex than the schoolbook explanation most people are familiar with. Circulations in ocean basins can have null points (where there is no rise and fall at all), double tides (four maxima and four minima per day), and produce resonances where inlets act like organ pipes and amplify the tidal oscillation to extreme proportions (e.g. the Severn Estuary and the Bay of Fundy). As John points out, the tidal profile in the Solent has a kink in it, and this is due to the circulation of tidal streams either side of the Isle of Wight.

**Ken Wood,  
Blackwood, Gwent, via the Net**

*Ah, I believed the land masses might be the cause, but had not thought to equate tidal flow with electronic transmission lines. Nor had I appreciated that the great ocean currents, such as the Gulf Stream, were caused by tidal movements. How extremely interesting, thank you.*

### DOUBLY TIDE UP

Dear EPE,

Your most interesting constructional project *Canute Tide Predictor* of June '00 asks for explanations about the confusing flow of tides in the English channel. Perhaps the tides around the south coast of England vie with John Becker's analysis and program for their complexity.

I cannot claim to give him a full explanation, but a few years ago I read an excellent book (regrettably I cannot recall its name) which discussed the problem and presented many diagrams. The following is based on my memory of its detailed contents.

The high tides at Dover and at Lands End/Penzance arrive from different directions. The high tide at Lands End comes from the Atlantic Ocean whilst that at Dover surges down the North Sea. They meet between Portsmouth and Weymouth, resulting in a confused tidal pattern in that region. The Isle of Wight adds its own complications and the Cherbourg peninsula is blamed for disturbed tidal patterns on the French coast.

At Poole Harbour entrance there are two high tides and one low tide in the normal twelve and a half hours. About three hours after the first high tide there is a second high, rather less than the first and then the water goes right out to low tide. The pattern changes along the coast, being quite different at Weymouth.

**Ken Beard,  
Cornwall, via the Net**

### PICTUTOR CD-ROM ODDITY

Dear EPE,

When using the Virtual PIC programs on the *PICtutor CD-ROM*, which I think is very good, I have found that instructions BCF, BSF, BTFSC and BTFSS cannot be programmed correctly if bit 7 is selected. The program listing just shows "BCF REG" and not "BCF REG,7" which, of course, is a syntax error, something the Virtual PIC should be incapable of. The running program just seems to treat the instruction as an NOP.

Also, from time to time, and I can't detect any consistency, except that it usually, but not always, occurs when the INDF register is used, is the presentation of the error message "There is no object named FSRRegister".

The "error" does not seem to affect program execution, except that it pauses until the error window is closed.

I thought your Tutorials were excellent, both from the point of view of the Virtual PIC and the hardware itself. I had coded 8080, 8085, 8088 and 8051 back in the 1980s but was rusty on machine language. Your Tutorials brought me back up to speed very quickly. My colleague had taken assembler language at the under-graduate level and hated it, but she took to the PIC quite happily.

I first tried to use the *PICtutor* hardware with Windows (shudder) NT, but could not get it to work, then found out it can't work with NT. Then I had trouble with Windows 95, until I discovered that software for a (non-existent) scanner was accessing the printer port!

If the Virtual PIC software is to be upgraded, may I suggest some additional features:

- The display should show the current .PSF file in use.
- A warning dialogue should be shown if the user tries to quit with a file not saved.
- The behaviour of GOTO statements, where lines are inserted, could be improved. I believe the label following the GOTO should be preserved wherever possible, and not replaced by the hex address of the label's former position (where this occurs).

**John Waller, via the Net**

*Whilst I had the pleasure of authoring the PICtutor text and PIC programming listings etc, we collaborated with Matrix Multimedia in the production of the CD-ROM and its Virtual PIC screen demos. They replied to John with the following:*

There is a workaround to the bit-7 bug: click on the far right of the number "7" when entering the bit. Most of the "7" is "underneath" another textbox, so clicking on that "hidden" part of the "7" produces the error. The far-right of the "7" is not "hidden" and produces the correct result.

We could not replicate the "There is no object named FSRRegister" error, so it is difficult to solve. If someone could supply a program that reproduces this error, then I could look into it and maybe produce a fix (send to [support@matrixmultimedia.co.uk](mailto:support@matrixmultimedia.co.uk)).

We have placed a new software version on our website with a fully clickable "7"! Your helpful suggestions have been noted.

**Matrix Multimedia**

*Thank you Ken. In fact, not even the excellent WXTide program I referred to in the article attempts to predict tides for Poole Harbour. It does, though, simulate for various locations around the Isle of Wight.*

*Interestingly, while in Jersey recently I saw Tidal Clocks on sale. Seemingly they use the familiar electronically controlled type of clock-work mechanism, but in which the rate has been changed from the standard 12-hour rotation to probably 12 hours 25 minutes during manufacture. A single hand rotates around the clockface indicating the expected state of the tide, but without any correction for different states and positions of the Sun and Moon.*

*I was amused to read the number of disclaimers about the accuracy of this clock. However, as I pointed out in Canute, many people only need to know an approximation of tide conditions and I feel that the clock probably adequately fulfils that need.*

### TELL US A STORY

Dear EPE,

Thank you so much for my lovely surprise. Not only do I get a smashing magazine monthly but am surprised to find that ordinary people with stories are welcome to write in and even better, see themselves in print.

**Dave Bishop,  
Tatsfield, Westerham, Kent, via the Net**

*Dave's letter about Sputnik was chosen as Letter of the Month in June '00, for which we sent him a digital multimeter.*

*Thanks Dave, we are as much interested in the history of technology as in its applications.*

## QBASIC AVAILABILITY

We have had numerous queries, over many months, from readers on how they could obtain QBasic or QuickBASIC. Having been told by reader Alan S. Raistrick that it was included on Windows 95 and 98 CD-ROMs (see Readout July '00), I found that QBasic was indeed on my CD and I asked readers to check if their CDs included it too. Here's a selection of the replies:

I've checked my CD-ROMs with the following results:

Early, upgrade version(?), **Windows95\other\oldmsdos\qbasic**, 11 July 1995 09:50:00. Later, full version – Windows 95 version 4.00.950 B. Here QBasic is filed as – **Windows95\other\oldmsdos\qbasic**. In the same directory is another executable – **HELP.COM**. Both these and their associated **.HLP** files are date and time stamped 24 August 1996 11:11:11.

Arthur Dyas, via the Net

My Win98 CD-Rom does include QBasic in <Drive>:\Tools\oldmsdos, both application and helpfile. Searching for QuickBASIC does not find anything.

Keith Tunstall, via the Net

I checked my Win 98 (second edition) and QBasic is there all right – under the **x:\tools\oldmsdos** directory. But hey, why are some so intent on hanging onto this? I found Visual Basic extremely easy to understand (more so than QBasic), and produces better user interfaces in next to no time.

Admittedly some of the I/O functions have been left out but there are a number of third party (public domain) add-ons to remedy this. With Windows 98 soon to amalgamate with the Windows 2000 generation with almost no support for MS-DOS, wouldn't it be wise to wean everyone to something else more appropriate?

David Reid,

Bardsley, Oldham, via the Net

I've checked my Windows 95 and 98 CDs. Both contain only Qbasic. QuickBASIC is, as you say, readily available on the Internet. Download sites seem to change almost daily. Try <http://members.tripod.com/~qbds/qb45.htm>.

Various petitions exist to try and persuade Microsoft to release QuickBASIC as freeware (see <http://netpower.8m.com/computers/programming/qb/>) which suggests that downloading at present is not legal. A possible alternative is a compilable version of Basic called Asic, which is a shareware program. My copy was included on a disk of Basic and QBasic programs which I purchased for £1 from the PC Information Centre on 01543 415 816 (very helpful). ([www.pcinfocentre.com](http://www.pcinfocentre.com).)

Dave Squibb, via the Net

Thank you to all of you who replied. We hope the rest of you now know where to find QB – on your Windows CD!

Regarding VB as highlighted by David Reid, I've tried to get into it, but have so far failed. David kindly says he'll send me some example programs. Would anyone else care to E-mail me some shortish examples as source code (i.e. code I can read via a text editor)?

## ARCHAEOLOGICAL LOGGING

Dear EPE,

Three years ago at the Bridgwater & District Archaeological Society, we constructed Robert Beck's *Earth Resistivity Meter* (EPE Jan/Feb '97). Working with the concurrence of the County Archaeology Officer, the Society has made important discoveries which have been recorded in the national Sites and Monuments Record (Somerset). We intend to continue with this voluntary work.

Now we have seen your remarkable *8-Channel Data Logger* (Aug/Sep '99) which would appear to us to be suitable, with minor adjustments, to replace our existing laborious

and error-prone methods of on-site recording. (In each session covering 30 square metres of ground we manually record 900 3-digit readings.)

Your logger in many aspects is ideally matched to our input and output requirements. The main concern is that it is far too sophisticated for us, in that we only need a single channel (and no prospects of ever needing more). Our input would be at an indeterminate rate by hand triggering via socket SK10 (RATE=EXT), and we are warned that transfer of data to a CP/M operating system such as ours (this is shoe-string stuff!) should not exceed 2400 baud: we could not happily tolerate an 8-channel system where only every eighth word carried meaningful data.

All these factors suggest that we need to build a smaller system. We wonder whether there is available on the market a single-channel (or even a twin-channel) EEPROM? Or, better still, an i.c. that does not need programming: programming EEPROMs and Internet access are further hurdles for us backward boys. We also thought that there was a chance that you had already built a smaller development model, the circuit details of which you would be prepared to divulge in a good cause!

Peter Ellison,

Ashcott, Bridgwater, Somerset

*Although my logger can run eight channels it is perfectly happy with just the first one (see the original text). The data is output to the computer one channel at a time and if a channel does not exist it is ignored. You do not need to intercept every eighth word as you think.*

*I regret I cannot directly advise on converting to 2400 baud but if you study the PIC and Basic programs you should be able to see where the codes can be changed. I have not designed another version.*

*The follow-up article PIC16F87x Mini Tutorial of Oct '99 discusses serial output/input control and baud rate setting, with reference to the PIC and the Basic program on the PC.*

*The listings should be read in conjunction with the PIC source code (.ASM) and Basic (.BAS) software files.*

*Incidentally, you will be interested to know that we have another type of Earth Resistivity Monitor scheduled for a few months time.*

## ICEBREAKER QUERY

Dear EPE,

Referring to Mark Stuart's brilliant *EPE ICEbreaker* project (Mar '00), I think there are a couple of bugs in the control software which limit its usefulness a bit.

The article says "Sometimes the highlight does not track the source code exactly and is one line above or below the current line". What I see is that the l.c.d. doesn't stop accurately at breakpoints and it can be more than one instruction out, often two or three.

In practice what it means is that NOPs have to be scattered around the code in areas being investigated until one giving the desired stop point is found. This means additional unnecessary assemblies and PIC code reprograms, and a certain amount of klunky trial and error, which can be less than straightforward if you've got a program that isn't working correctly anyway.

The other problem is one which appears to corrupt the communications between the PIC monitor program and the host PC. This happens when any of pins RB0 to RB5 are written to. There is nothing in the article description nor the on-line help that implies any restriction in the use of these lines, and indeed this is precisely what the demo program **icelcd1.asm** does.

Once the comms link has been corrupted in this way, breakpoints do not work, and the STOP button on the *ICEbreaker* main window, although it does stop the PIC program, causes a dialogue box with the error message: "Communications error: unexpected character received: A5 expected xx received" to be

displayed. Once it is in this state, registers etc cannot be displayed, and it is usually necessary to reload **icebreaker.exe** on the PC or power cycle the PIC board to get out of it, although sometimes the Reset button on the *ICEbreaker* window will work. However, single stepping past a write to PORTB works OK.

One other minor point: "Program" programs the data EEPROM as well as the program memory. I can't find anything that documents this, and I think there should be because it is different behaviour from other *EPE* PIC programmers (e.g. *Toolkit V2*) in which code and data programming are separate functions, and reprogramming the code memory leaves the data EEPROM intact.

Although MPASM allows you to embed EEPROM data in the source code, so this behaviour once understood isn't a problem, I'd actually prefer an option to turn EEPROM programming off. I use EEPROM for tables such as decimal conversion, 7-segment l.e.d. driving, stepper motor driving etc, and once these things are set up they don't need to change.

Malcolm Wiles, via the Net

Mark Stuart replied to Malcolm:

I'm glad you're finding *ICEbreaker* useful. The fact that the l.c.d. does not stop accurately at breakpoints is a "feature" of the PIC16F87x chips which operate in three modes: normal run, single step and run until breakpoint.

If you set a breakpoint then the Windows program sends the breakpoint address to the PIC. The PIC will run until it hits the breakpoint then saves the program counter (PC) and starts executing the debugger code. The debugger code communicates with the Windows program and tells it the stored value of the PC.

Unfortunately, the PIC doesn't always stop where it's meant to. That's especially true near to instructions that cause a skip, instructions that are skipped, jumps, calls, and destinations of jumps and calls. The breakpoint is implemented in hardware inside the PIC and there's nothing we can do to alter its behaviour.

With the other problem which appears to corrupt the communications between the PIC and PC, I don't understand what's going wrong. As you say, **icelcd1.asm** writes to these pins without any problems.

That's a good point about EEPROM and data programming. Should an upgrade version of the *ICEbreaker* software ever be produced (but no current plans for this), separate commands for writing to each could be added.

Mark Stuart

## BRIDGE THAT GAP

Dear EPE,

Thank you for an excellent magazine. I subscribed on-line just as the *Teach-In 2000* series began and have found it to be the perfect bridge between "here buy these parts, build this project" and "on a sub-atomic level the valance electrons fill the blah blah blahh . . ."

Keep the usable and accurate hobby electronics information coming.

A correction to your February *Teach-In 2000* lesson that will help to keep your Yank readers from tearing their hair out is that on a US SPP, ECP or EPP port the ERROR line that you have as pin 23 is pin 15 on a DB-25 and pins 18-25 are ground (earth).

Stephen Skibbe,  
Minneapolis, MN, USA, via the Net

Thank you Stephen, *Teach-In's* been fun to write (about two years off and on) and its gratifying to know that so many of you appreciate the series.

Connector-wise, you will by now have seen Panel 9.5 of Part 9 (July '00) which sorts out a matter we did not know about. Thanks for your suggestions for using a wire-wrap socket instead of doing soldering. Regretably we are too far down the line now for it to be usable by our TI followers.



# DOOR PROTECTOR

OWEN BISHOP

## Project 1



This short collection of projects, some useful, some instructive and some amusing, can be made for around the ten pounds mark. The estimated cost does not include an enclosure, for many of them work just as well as an open board.

All of the projects are built on stripboard, and have been designed to fit on to boards of standard dimensions. All of the projects are battery-powered, so are safe to build. In a few cases in which, by its nature, the project is to be run for long periods, power may be provided by an inexpensive mains adaptor. Again, the cost of such a unit is not included because most spares boxes contain a few of these, possibly pensioned off from powering now obsolete electronic gadgets.

**E**VEN if you already have an electronic security system installed in your home or workplace, there is likely to be a use for this Door Protector. With any security system, or even with none, it is important that all doors and windows should be protected by bolts, bars, grids or other physical means. It costs relatively little to fix strong bolts or locks to windows and doors, to make it virtually impossible for anyone to gain access without employing drastic measures.

Unfortunately, there is nearly always one weak point. This is the Exit Door, the door by which you normally leave the house when you are going out. This is also the door by which you enter the house when you come back home.

Other doors (and the windows) are bolted or locked from the *inside*. Once secured, they can only be opened by someone who is already inside the house. Only physical protection is needed.

On the other hand, the Exit/Entry Door has to be openable from *outside* the house as well. There is a limit to the number of locks that can be fitted, and usually it is not practicable to fit any bolts.

### DOOR GUARD

This is where electronics, in the form of this month's project, can be of help. The Door Protector system described here can be set to one of two states:

- **Disarmed:** After pressing the Disarm button, you may open and close the Exit/Entry Door as often as you like and this has no effect on the siren.
- **Armed:** You arm the system by pressing the Arm button and then have 20 seconds to leave the house via the Exit Door without making the siren sound. On re-entering the house through the same door, nothing happens for the first 20 seconds but your entry has triggered the system and the siren will start to sound after 20 seconds unless you press the Disarm button.

The timings can be altered to suit individual locations. Of course, the function buttons are hidden away so that an intruder cannot quickly find them.

### HOW IT WORKS

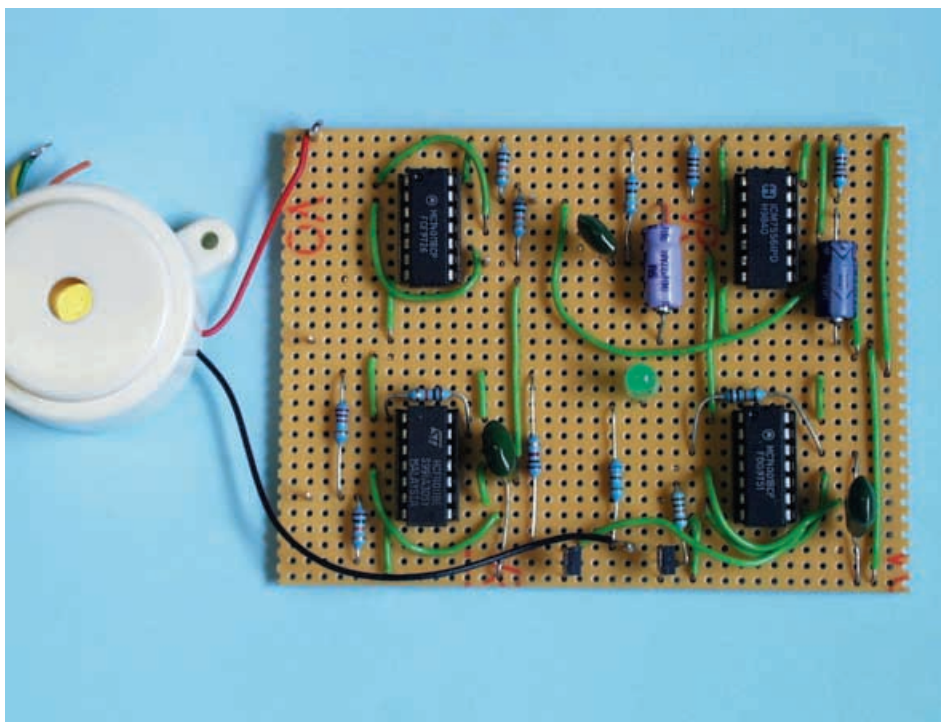
The circuit is triggered by a switch mounted on the door, and accessible only from the inside. This may be a microswitch or more conveniently a reed switch that closes when a permanent magnet is near it. Usually the switch is mounted on (or in) the frame of the door and the magnet is mounted on (or in) the door.

When the door is open, the magnet no longer has an effect on the switch, which springs open. When the door is closed the magnet comes very close to the switch, causing it to close.

As shown in the full circuit diagram for the Door Protector in Fig.1, the door switch S1 is closed whenever the door (with magnet insert) is closed, so pin 9 of IC1a is held at logic low. If the door is opened, even by only a few centimetres and for only a fraction of a second, the input at pin 9 is pulled to logic high, via resistor R1, for long enough to trigger the circuit.

If the circuit is in the "disarmed" state the other input (pin 8 of IC1a) is at logic low, so the output of the gate at pin 10 remains at logic high, whatever the input to pin 9. Opening and closing the door has no effect on the system.

If the system is in the "armed state", the input at IC1a pin 8 is high. Then any high level at pin 9 caused by opening the door



causes the output at pin 10 to become low. This output goes to a set-reset flip-flop consisting of two NAND gates, IC1b and IC1c.

In the reset state, pin 11 of IC1c is high but this goes low (and stays low) when the flip-flop is triggered. The low-going level passes across capacitor C1 and produces a short low pulse that triggers the timer IC2a. The timer output at pin 5 is normally low but now goes high for 20 seconds.

The next stage is a pulse generator, formed by IC4a/IC4b which normally has a low output at IC4b pin 11, but produces a short high pulse when the input from the timer goes low, that is, after 20s. The output from the pulse generator goes to another flip-flop, formed this time from a pair of NOR gates IC4c/IC4d.

When this receives a high pulse its output at pin 10 goes high and stays high. It turns on transistor TR1, which in turn switches on the siren (WD1). The siren sounds until the system is disarmed or the power is switched off.

The remainder of the circuit is concerned with arming and disarming. Pressing the Arm button of switch S2 has two effects. It resets the flip-flop IC1b/IC1c, making its output at pin 11 go high. It is now ready to trigger the timer (IC2a) as already described.

The second effect is to trigger another timer, IC2b. The output of this goes high for 20s and, at the end of this period, another pulse generator (IC3a/IC3b) produces a short high pulse. This sets flip-flop IC3c/IC3d, making its output at pin 10 go high.

This output is fed back to pin 8 of the input gate IC1a that also received input from the door switch S1. With pin 8 high, pulses from the door switch are passed through to the flip-flop of IC1, so triggering IC2a. The system is now armed, but not until 20s after pressing the Arm button.

The Disarm button of pushswitch S3 also has two actions. One function is to produce a low pulse to reset the arm/disarm flip-flop at pin 6 of IC3. The low pulse is also inverted by transistor TR2 and then used to reset the siren flip-flop (IC4c/IC4d) and turn the siren off.

If you want to make one or both delay times longer, recalculate the values of the timing capacitor and resistor (R3, C2 or R5, C4), using the formula,  $t = 1.1RC$ . The delay time is  $t$  seconds,  $R$  is in ohms and  $C$  is in farads.

## POWER NEEDS

Although Fig.1 shows the circuit operating at 12V, it will operate at any voltage suitable for powering CMOS i.c.s and the siren. The minimum for a reasonably loud siren is 6V, and the maximum for CMOS is 15V. We chose 12V to suit the 3-tone piezo buzzer that we had decided on. It operates between 6V and 12V but is louder at 12V, with an output of 107dB.

Its power leads are red (positive) and black (0V) and there are two additional leads for determining the kind of sound it makes. With the yellow and green leads connected, it makes a 2-tone warble. Orange and green connected give a single-tone pulsed sound. If these leads are left unconnected, the tone is continuous.

The circuit requires only a small current when the siren is not sounding, so a power

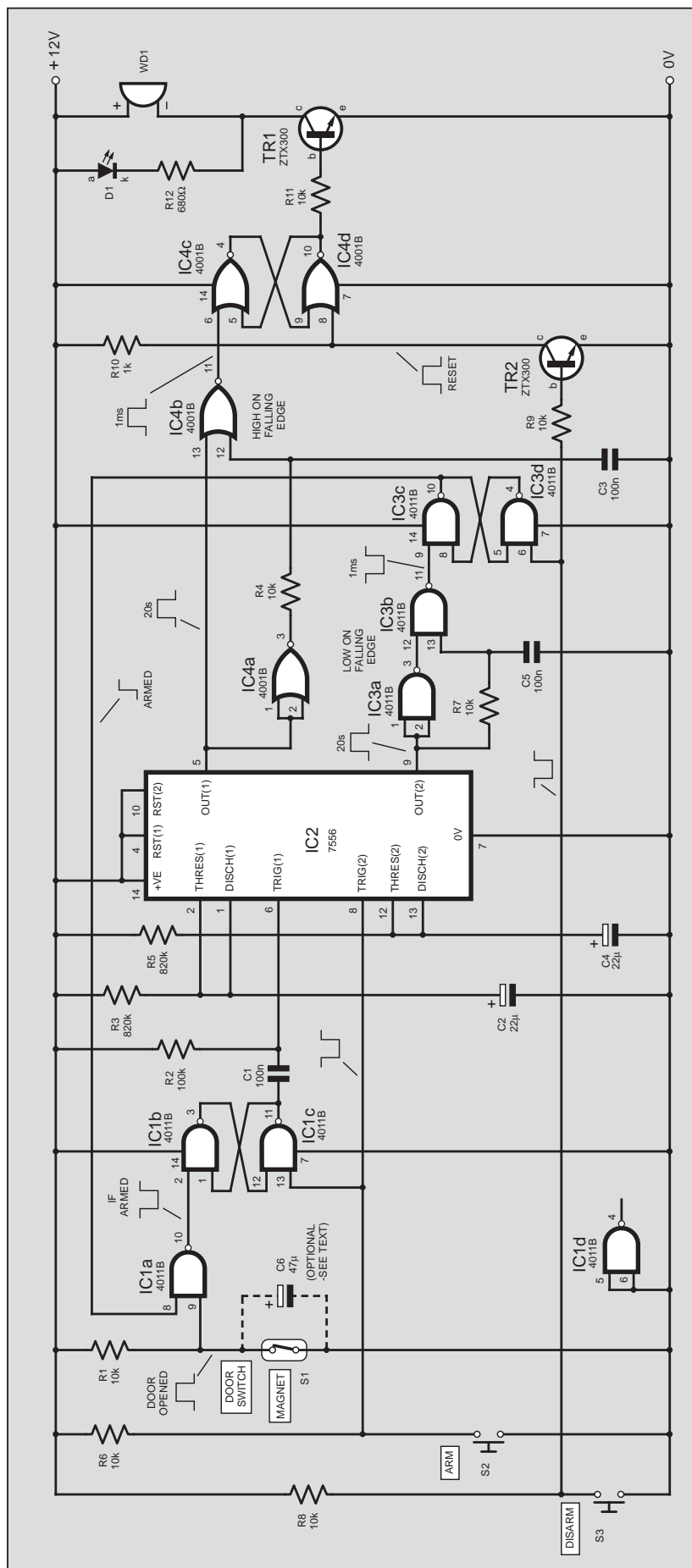
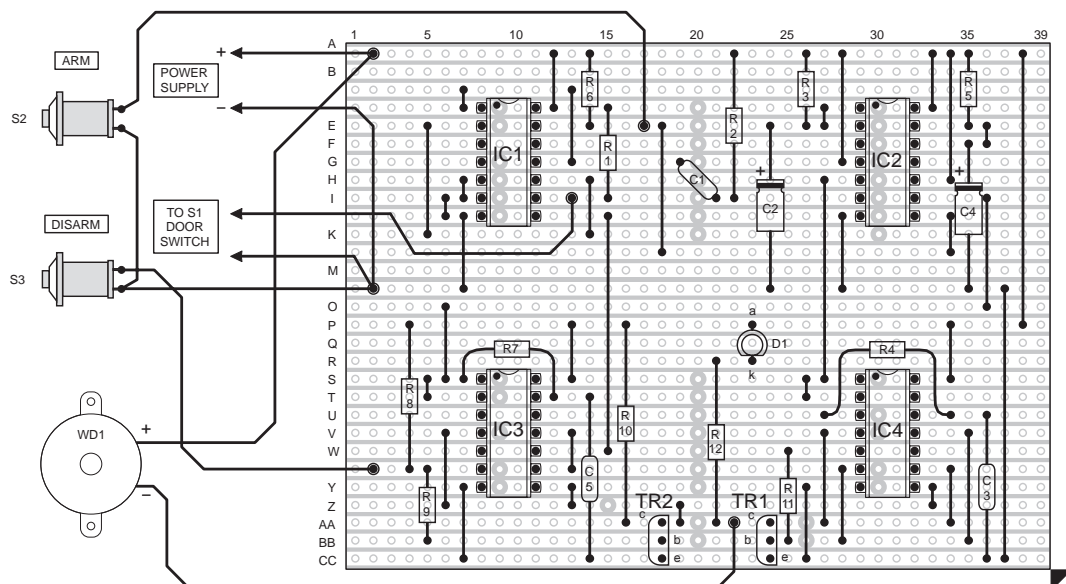


Fig.1. Complete circuit diagram for the Door Protector.



## COMPONENTS

### Resistors

R1, R4, R6  
to R9,  
R11 10k (7 off)  
R2 100k  
R3, R5 820k (2 off)  
R10 1k  
R12 680Ω

All 0.25W 5% carbon film

### Capacitors

C1, C3, C5 100n polyester (3 off)  
C2, C4 22μ radial elect. 16V  
C6 47μ radial elect. 16V  
(optional)

### Semiconductors

D1 3mm or 5mm light-emitting diode (l.e.d.), red  
TR1, TR2 ZTX300 *n*pn low-power transistor or similar (2N3704) (2 off)  
IC1, IC3 4011 CMOS quad 2-input NAND gate (2 off)  
IC2 7556 dual timer  
IC4 4001 CMOS quad 2-input NOR gate

### Miscellaneous

S1 magnetic reed switch, with magnet  
S2, S3 pushbutton switch, push-to-make (2 off)  
WD1 audible warning device (triple tone piezo buzzer)

Stripboard 0.1 inch matrix, size 29 copper strips by 39 holes; 14-pin i.c. socket (4 off); D-type alkaline cells or unregulated mains adaptor – see text; 1mm solder terminal pins (6 off); solder, etc.

Approx. Cost  
Guidance Only  
excluding batts or mains adpt.

See  
**SHOP  
TALK**  
page

**\$16**

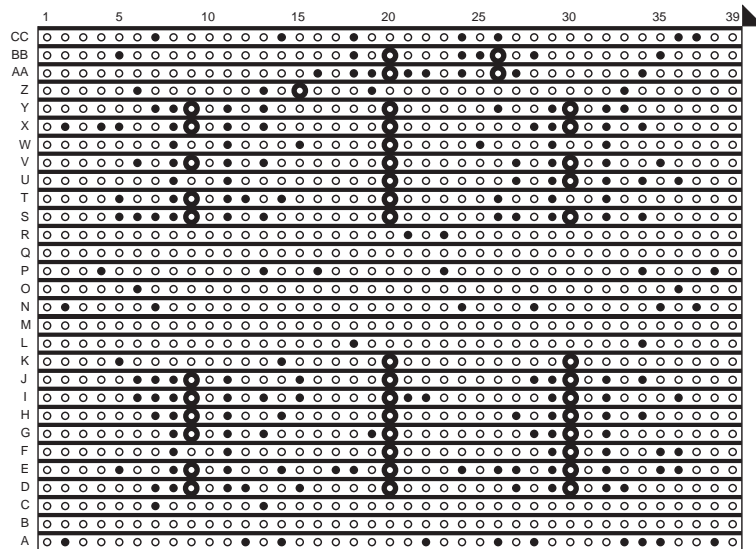


Fig.2. Stripboard topside component layout, interwiring to off-board components and details of breaks required in the underside copper tracks.

supply from four (or eight) D-type alkaline cells could be used. Using a battery makes the system immune to power cuts. However, in the long run it is more economical to use a 6V, 9V or 12V unregulated mains adaptor rated at 300mA or more.

## CONSTRUCTION

The Door Protector circuit is built up on a piece of 0.1in. matrix stripboard, containing 29 copper strips by 39 holes. The board topside component layout and details of breaks required in the underside copper tracks are shown in Fig.2. It is recommended that d.i.l. sockets are used for all the i.c.s.

As there is plenty of room on the circuit board, there should be no problems in making the connections. Note that the copper strips are cut at certain places but that some of the strips beneath the i.c.s are NOT cut.

If you are intending to construct several of the projects in this series, it may be worthwhile investing an extra "fiver" in purchasing a special spot-face cutter for cutting the copper tracks neatly. Alternatively, a handheld twist drill bit of around 5mm dia. will do the job just as well. Whichever method you use to make the breaks you should always double-check for any slivers of copper bridging any surrounding tracks or cuts before moving on to the next break.

You can assemble the whole circuit before testing it, working from the smallest (link wires) up to the largest components, but you may prefer to check each stage as you go. If so, begin with the main triggering circuit, starting at switch S1 and working stage by stage through IC1, IC2a and IC4 to the siren.

You will need to enable the circuit by temporarily wiring pin 8 of IC1 to the



positive rail. Also, temporarily connect pin 8 of IC4 to 0V through a 10 kilohm resistor. Provide a flying lead for briefly bringing pin 8 to logic high when you need to reset the flip-flop to silence the siren.

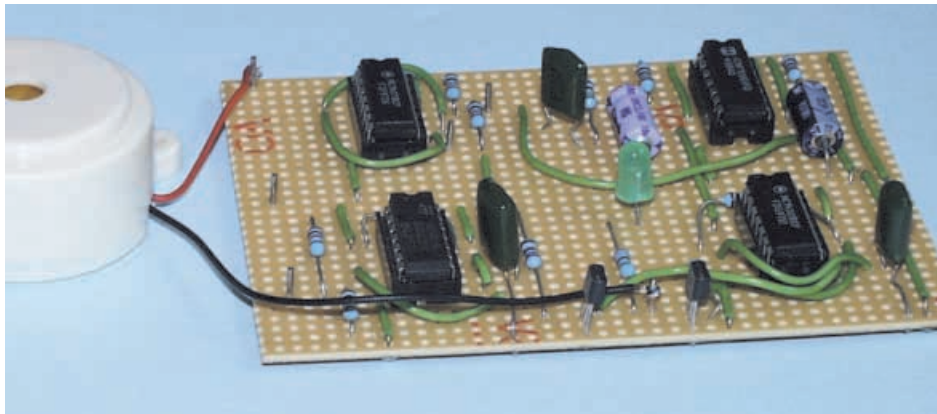
After this, assemble and test the arming and disarming sections of the circuit, based on the second half of timer IC2 and IC3.

## INSTALLING THE SYSTEM

The entire Door Protector system, including the siren, can be located at a convenient point and housed in a single enclosure. However, it makes more sense to mount the siren in a remote and relatively inaccessible place where it can easily be heard and cannot be interfered with.

Wiring between the board and siren must be concealed as far as possible. Power leads should likewise be hidden as much as possible.

Similarly, you should hide the pushbutton switches, particularly the Disarm one, in a place where they are difficult for an intruder to find but are quick and easy for



Component layout on the prototype Door Protector circuit board. No case details are given as it is left to individual choice – you may wish to hide the switches remote from the main unit.

you to reach. It is not so important to hide the leads to the door switch because, if the intruder finds and cuts these leads, it has the same effect as opening the door; the siren sounds 20 seconds later!

In some situations there may be false

triggering due to electromagnetic interference picked up in the leads joining the circuit board to the door switch. If this occurs, it can usually be cured by wiring a capacitor between IC1 pin 9 and the 0V rail (C6 in Fig.1). □

## SHOP TALK with David Barrington

### EPE Moodloop

No real trouble should be encountered when searching for components for the *EPE Moodloop* project. The OP279 rail-to-rail, high current, dual op.amp is an Analog Devices chip and was originally ordered from **Maplin**, code NP18U. The BD135 and BD136 power transistors should be widely stocked, try **Cricklewood** (☎ 020 8452 0161, Fax 020 8208 1441).

The 15-way D-type chassis socket and 15-way IDC plug connectors should now be widely available, check out **ESR Components** (☎ 0191 2514363 or [www.esr.co.uk](http://www.esr.co.uk)) and **Magenta** (☎ 01283 565435 or [www.magenta2000.co.uk](http://www.magenta2000.co.uk)).

The Frequency 12-way rotary switch used in the model is a Lorlin type, with a limit end-stop behind its mounting bolt to set the required number of "ways". This was removed completely when mounting the switch in the unit. Most of our component advertisers should be able to offer a suitable 12-way single-pole switch as well as the 4-way d.i.l. switch (if required).

If you intend to invest in the same case, this is from the Vero Patina range which succeeds the popular 200 series. The top and bottom sections clip together, without screws, and we understand that the Vero part number is 75-265742E (series 215 box code 75-3008J). The Maplin code for the Patina case is: NC91Y.

Turning now to the software. For those readers unable to program their own PICs, the author is able to supply ready-programmed PIC16F84 microcontrollers for the sum of £6 each, inclusive of postage (overseas add £1 per order). Orders should be sent to: **Andy Flind, 22 Holway Hill, Taunton, Somerset, TA1 2HB**. Payments should be made out to **A. Flind**. For those who wish to program their own PICs, the software is available from the Editorial offices on a 3.5-in. PC-compatible disk, see *PCB Service* page 637. It is also available free via the *EPE* web site: [ftp://epemag.wimborne.co.uk/pubs/PICS/moodloop](http://epemag.wimborne.co.uk/pubs/PICS/moodloop).

Finally, the printed circuit board is available from the *EPE PCB Service*, code 271 (see page 637).

### Door Protector

For the first of our "Top Tenner" projects, the author has chosen a simple *Door Protector* alarm circuit. The components for this project seem to be fairly straightforward and should not cause any sourcing problems as most of our component advertisers should be able to provide suitable items.

Several of our current advertisers such as **Suma Design, Quasar Electronics, Bull Electrical** and **Maplin** may be able to offer a suitable reed switch and magnet combination. Maplin list a surface mounting type (YW47B) and a self-adhesive UPVC type (MM11M). The triple-tone buzzer, used in the prototype, also came from the last mentioned company, code KU60Q.

One last point, we understand that some overseas readers may have difficulty in obtaining the ZTX300 transistor. We suggest that they try a general purpose, low power, type such as the 2N3704.

### Quiz Game Indicator

Although the formats may differ slightly, most of the components required for the *Quiz Game Indicator*, this month's Starter Project, should be "off-the-shelf" items. However, some readers may prefer to use the identical parts as listed and shown in the article.

This may apply particularly to the neat-looking contestant boxes and the accompanying pushbutton switches. These were both purchased from **Rapid Electronics** (☎ 01206 751166 or E-mail [sales@rapidelec.co.uk](mailto:sales@rapidelec.co.uk)). The snap-together ABS mini-box carries the code 30-1905, and the miniature push-button switch is coded 78-1520.

The transistor used in this circuit is the BC184L, and the suffix **L** after its type number indicates a different pinout line-up to other BC184s. In practice, virtually any low power, small signal, *npn* general purpose transistor can be used here, but take care to check the leadouts and place them in the correct order on the p.c.b.

The small printed circuit board is available from the *EPE PCB Service*, code 272 (see page 637).

### Handy Amp

The most likely component to cause concern when ordering parts for the *Handy Amp* project is the Analog Devices SSM2211 power amplifier i.c. For those readers who do have trouble finding this chip, the one in the model came from **Maplin** ([www.maplin.co.uk](http://www.maplin.co.uk)), code OA20W. The 5534 low-noise op.amp should be generally available and is an improved version of the standard 741 type.

When ordering the headphone 6.35mm stereo jack socket you **must** specify that you need a plastic bodied (*insulated*) chassis mounting type; this should have all its connections, including the mounting bezel, isolated from the metal case. Most headphones now seem to terminate with a 3.5mm jack plug, so you will probably need a 6.35mm to 3.5mm adaptor. Both these forms of socket/plug should be available from your usual component supplier or one of our component advertisers.

Selecting an internal 8 ohm loudspeaker is left to the constructor, you do not have to use an elliptical type, just make sure it will fit inside the metal case. However, do not use one having a rating less than 2W.

The choice of a "vinyl-effect" aluminium case for this project is one that has proved popular in past constructionals and many of our component stockists now carry them. This is sometimes referenced as a WB3 type. Do not use a plastic box, as this will not provide any screening and hum pick-up could be a problem.

The printed circuit board is available from the *EPE PCB Service*, code 273 (see page 637).

### Cave Electronics

Some readers may be intrigued to experiment with the L.E.D.-based Caving Lamp circuit (Fig.1) contained in the *Cave Electronics* feature. We can offer the following assistance with components for this circuit.

We are informed that the 10μH inductors L1 and L2 are made up from Philips 432202097180 toroid formers (**Farnell** – ☎ 0113 263 6311 or [www.farnell.com](http://www.farnell.com)) – code 180-008) wound with four twisted strands (two parallel pairs) for the primary and secondary windings. The cores are wound with the four strands of 0.315mm wire until a single layer is completed. This gives approximately 28 turns. The ends are sorted out and soldered to the terminal pins.

Also, checkout the Linear Technology web site ([www.linear-tech.com](http://www.linear-tech.com)) where a circuit using their LT1513 switching regulator (IC1/IC3) shows the use of a twin-winding Coiltronics CTX10-1 10μH common core inductor. All we can tell you about IC2 and IC4 is that they are Motorola devices. It seems that the Schottky diodes also came from Farnell (see above).

We are told that the main "lighting" i.e.d.s are of Hewlett Packard manufacture and made up as follows: green (8 off) HLMP-BM01; blue (4 off) HLMP-BB01; red (2 off) HLMP-BD06 and yellow (amber) (2 off) HLMP-BL06. Once again, the above mentioned component supplier is a possible source.

# INTERFACE

Robert Penfold



## FOUR-RANGE RESISTANCE METER PC INTERFACE

THE *Interface* article in *EPE* February 2000 featured a 12-bit serial analogue-to-digital converter (ADC) for use with a PC printer port. This circuit is useful as the basis of various PC-based measuring devices, such as the four-range resistance meter featured here. The four ranges covered are 0 to 4·094k, 40·94k, 409·4k and 4·094M.

In normal test meter terms a resolution of 12-bits is quite respectable at something between 3·5 and 4-digit operation. On-screen buttons are used to select the desired range, and the resistance meter interface is controlled from the printer port via electronic switches. It would presumably be possible to implement automatic ranging in the software, but the current software does not include this feature. This would be an interesting line for experimenters to pursue.

### A/D Circuit

The circuit for the analogue-to-digital converter appears in Fig.1, and this is much the same as the original design. One important change is that the supply potential for the converter is reduced from 5V to a little under 4V by IC1, which operates as a simple voltage follower driven from a potential divider (R1 and R2).

The full-scale input voltage of the converter is equal to the supply potential, which makes it awkward to realise the full resolution of the converter. Either the circuit driving the converter must be powered from a supply potential of more than 5V or the supply to the converter must be reduced slightly. Reducing the converter's supply voltage is the easier option. The supply potential is still well above the 2·7V minimum requirement of the AD7896AN converter IC2, and is also high enough to give reliable interfacing to the printer port.

### Resistance to Voltage

For the converter to operate as a resistance meter it must be preceded by a suitable resistance-to-voltage converter. This form of conversion is easily achieved, and it is just a matter of feeding the test resistance from a constant current generator. The higher the resistance, the greater the voltage needed to force the current through the test resistor.

In order to cover a wide resistance range this interface uses four measuring ranges, and it therefore has four constant current generators, see Fig.2. These are conventional in design and are based on TR1 to TR4, which are respectively used to provide the 4·094k, 40·94k, 409·4k, and 4·094M ranges.

Preset resistors VR1 to VR4 enable their respective ranges to be calibrated against precision (1 per cent or better) resistors. Ideally these should be multi-turn trimpots. The calibration components should have values approaching the full-scale values. Suitable values are 3k9, 39k, 390k and 3M9.

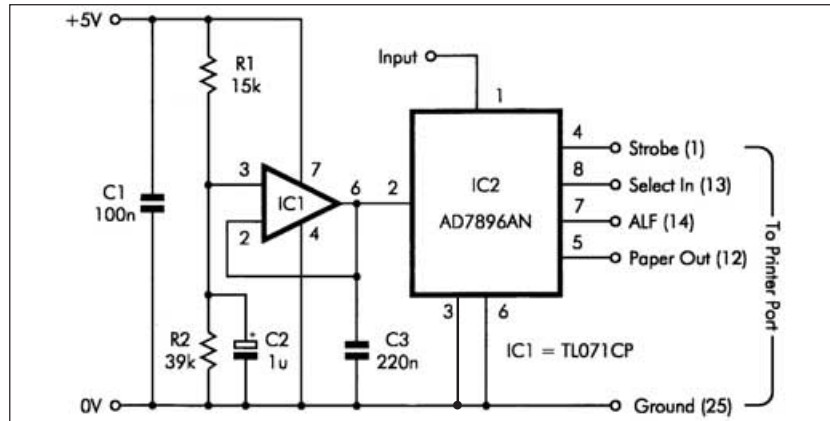


Fig.1. The circuit diagram for the A/D converter. IC1 supplies a reduced supply potential to IC2.

Resistor R4 is common to all four current generators, but in normal operation it is only connected to one of them at a time. IC3 is a CMOS quad s.p.s.t. analogue switch, but in this circuit it is connected to act as a 4-way single pole switch. R4 can be connected to any one of the current generators by taking the appropriate control input high.

The transistors in the other three generators are cut off and provide no significant output current. For example, taking pin 6 of IC3 high switches on TR3 and sets the unit to the 409·4k range. The analogue to digital converter is interfaced via some of the printer port's handshake lines, leaving the data lines free. Lines D0 to D3 are used to control the range switching.

The test component is connected across sockets SK1 and SK2. The drive current on the highest range is only about one microamp, but the high input resistance of the converter ensures that minimal loading occurs.

With 12-bit resolution noise can be something of a problem, so try to keep the circuit reasonably well away from the computer and monitor, both of which will inevitably generate a fair amount of electrical noise. Try to avoid touching the test component's leadout wires when making measurements, especially when using the highest range.

The 5V supply must be reasonably stable and noise-free. The current consumption of the circuit depends on the range in use, but it is never more than a few milliamps.

### Software

The program for the resistance interface (Listing 1) is written in Delphi 1.0 and will run under Windows 3.1, 95 or 98. It requires a form containing six command buttons, a panel and a label. The layout used for the prototype software can be seen from Fig.3, which shows the program in action, but you

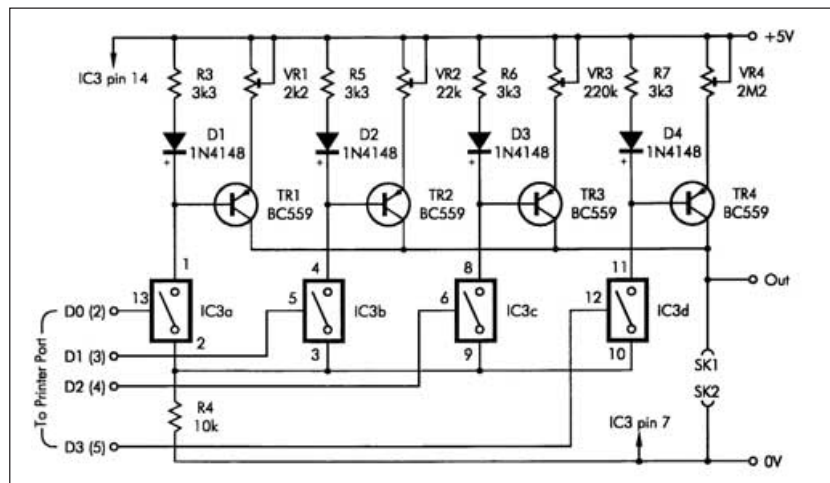


Fig.2. The resistance-to-voltage converter. There are four current generators, one for each range.

## LISTING 1

```

unit Rmet;

interface

uses
  SysUtils, WinTypes, WinProcs, Messages, Classes, Graphics,
  Controls,
  Forms, Dialogs, ExtCtrls, StdCtrls;

type
  TResMeter = class(TForm)
    Panel1: TPanel;
    Timer1: TTimer;
    Button1: TButton;
    Button2: TButton;
    Button3: TButton;
    Button4: TButton;
    Button5: TButton;
    Button6: TButton;
    Label1: TLabel;
    procedure Timer1Timer(Sender: TObject);
    procedure Button1Click(Sender: TObject);
    procedure Button2Click(Sender: TObject);
    procedure Button3Click(Sender: TObject);
    procedure Button4Click(Sender: TObject);
    procedure Button5Click(Sender: TObject);
    procedure Button6Click(Sender: TObject);
  private
    { Private declarations }
  public
    { Public declarations }
  end;

var
  ResMeter: TResMeter;
  Prn1: Word;
  Prn2: Word;
  Prn3: Word;
  Reading: Word;
  Dta: Byte;
  Busy: Byte;
  Digits: Byte;
  DecPos: Byte;
  Rding: String;
  Padding: String;

implementation

{$R *.DFM}

procedure TResMeter.Timer1Timer(Sender: TObject);
begin
  Prn1 := 888;
  Prn2 := 889;
  Prn3 := 890;
  Port[Prn3] := 1;
  Port[Prn3] := 3;
  Port[Prn3] := 1;
  Repeat
    Busy := Port[Prn2] AND 16;
    application.ProcessMessages;
    Until Busy = 0;
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Reading := 0;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := 2048;
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 1024);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 512);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 256);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 128);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 64);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 32);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 16);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 8);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 4);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 2);
    Port[Prn3] := 0;
    Port[Prn3] := 1;
    Dta := Port[Prn2] AND 32;
    If Dta = 32 Then Reading := (Reading + 1);
    Str(Reading, Rding);
    Digits := Length(Rding);
    If Digits = 1 Then Padding := '000';
    If Digits = 2 Then Padding := '00';
    If Digits = 3 Then Padding := '0';
    If Digits = 4 Then Padding := '';
    Insert(Padding, Rding, 1);
    Insert('.', Rding, DecPos);
    Panel1.Caption := Rding;
    If Reading = 4095 Then Panel1.Caption := 'OVER';
  end;
  procedure TResMeter.Button1Click(Sender: TObject);
  begin
    Timer1.Enabled := False;
  end;
  procedure TResMeter.Button2Click(Sender: TObject);
  begin
    Timer1.Enabled := True;
  end;
  procedure TResMeter.Button3Click(Sender: TObject);
  begin
    Port[Prn1] := 1;
    DecPos := 2;
    Label1.Caption := 'KILOHMS';
  end;
  procedure TResMeter.Button4Click(Sender: TObject);
  begin
    Port[Prn1] := 2;
    DecPos := 3;
    Label1.Caption := 'KILOHMS';
  end;
  procedure TResMeter.Button5Click(Sender: TObject);
  begin
    Port[Prn1] := 4;
    DecPos := 4;
    Label1.Caption := 'KILOHMS';
  end;
  procedure TResMeter.Button6Click(Sender: TObject);
  begin
    Port[Prn1] := 8;
    DecPos := 2;
    Label1.Caption := 'MEGOHMS';
  end;
end.

```



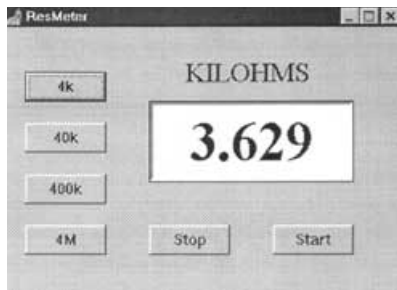


Fig.3. The resistance meter program in action.

can obviously use any layout you like.

The form also requires a timer component, and most of the code is attributed to this item. Of course, the timer component is visible on the form, but it is a

“transparent” component that does not appear when the program is run.

The timer is set to take readings at 500 millisecond intervals, and the routine used to take the readings is much the same as the one supplied in the Feb. 2000 issue. Refer to this for details of how the converter chip is controlled and the data is clocked out.

The values of 888, 889, and 890 assigned to variables Prn1 to Prn3 are the port addresses, and these will normally be correct for printer Port 1. They can obviously be changed to alternative port addresses if necessary.

The Stop button switches off the timer and freezes the display with the current reading. Operating the Start button resumes normal operation.

Once a reading has been taken it could be displayed in its raw form, but it is better if a decimal point is added at the correct position for the range in use. In

BASIC some simple mathematics is all that is required, but Delphi 1.0 seems to go into scientific notation if any floating-point mathematics is attempted. The method adopted here is to convert the reading to a string and then use the Length function to determine the number of characters.

Where necessary, leading zeros are then added using the Insert instruction so that the value is always four digits long. Another Insert instruction is then used to add the decimal point at the appropriate position. The correct position for the decimal point is contained in the variable DecPos, which is set at the correct value when a range button is operated.

Operating one of the buttons also sets the hardware to the correct range by writing the appropriate value to the data lines of the printer port. The label over the display is also set to read MEGOHMS or KILOHMS, as appropriate.

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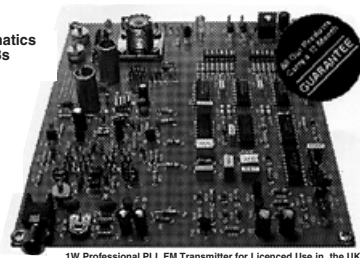
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